

P-Channel 30-V (D-S) MOSFET

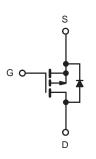
PRODUCT SUMMARY							
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A) ^d	Q _g (Typ.)				
- 30	0.050 at V _{GS} = - 10 V	- 7.6	13 nC				
- 30	0.056 at V _{GS} = - 4.5 V	- 6.0	13110				

FEATURES

- Halogen-free According to IEC 61249-2-21
 Definition
- TrenchFET[®] Power MOSFET
- 100 % R_g Tested

APPLICATIONS

- Load Switch
- Battery Switch



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	「 _A = 25 °C, unless othe	erwise noted			
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V _{DS}	- 30	V		
Gate-Source Voltage	V _{GS}	± 20	v		
	T _C = 25 °C		- 7.6		
Continuous Drain Current (T ₁ = 150 °C)	T _C = 70 °C		- 5.8		
Continuous Drain Current $(T_j = 150 \text{ C})$	T _A = 25 °C	I _D	- 6.0 ^{a, b}		
	T _A = 70 °C		- 5.2 ^{a, b}	А	
Pulsed Drain Current	I _{DM}	- 35			
Continuous Courses Drain Diada Current	T _C = 25 °C		- 3.5		
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S	- 2.1 ^{a, b}		
	T _C = 25 °C		6.5		
Mauiaum Davies Dissis ation	T _C = 70 °C		3.5	10/	
Maximum Power Dissipation	T _A = 25 °C	P _D	2.5 ^{a, b}	W	
	T _A = 70 °C	1	1.6 ^{a, b}		
Operating Junction and Storage Temperature Rang	T _J , T _{stg}	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS								
Parameter	Symbol	Typical	Maximum	Unit				
Maximum Junction-to-Ambient ^{a, c}	t ≤ 10 s	R _{thJA}	40	50	°C/W			
Maximum Junction-to-Foot	Steady State	R _{thJF}	24	30	C/ W			

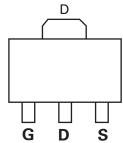
Notes:

a. Surface mounted on 1" x 1" FR4 board.

b. t = 10 s.

c. Maximum under Steady State conditions is 95 °C/W.

d. Package limited.





FREE

Available



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = - 250 μA	- 30			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I _D = - 250 μA		- 31		m)//9	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	i _D = - 250 μA		4.5		mV/°0	
Gate-Source Threshold Voltage			- 1.0		- 2.5	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
Zana Osta Mallana Dasia Osana di	1	$V_{DS} = -30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			- 1	μA	
Zero Gate Voltage Drain Current	IDSS	V _{DS} = - 30 V, V _{GS} = 0 V, T _J = 55 °C			- 5		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le -5 V, V_{GS} = -10 V$	- 20			A	
		V _{GS} = - 10 V, I _D = - 7.0 A		0.050		Ω	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 5.6 A		0.056			
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = - 7.0 A		18		S	
Dynamic ^b	-			•		<u>. </u>	
Input Capacitance	C _{iss}			1355			
Output Capacitance	C _{oss}	$V_{DS} = -15 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$		180		pF	
Reverse Transfer Capacitance	C _{rss}			145		- '	
	Qg	V _{DS} = - 15 V, V _{GS} = - 10 V, I _D = - 7.0 A		25	38		
Total Gate Charge				13	20	1	
Gate-Source Charge	Q_{gs} $V_{DS} = -15$ V, $V_{GS} = -4.5$ V, $I_D = -7.4$			3.5		nC	
Gate-Drain Charge	Q _{gd}			5.5		1	
Gate Resistance			0.4	2.0	4.0	Ω	
Turn-On Delay Time	t _{d(on)}			10	20		
Rise Time	t _r	V_{DD} = - 15 V, R _L = 2.7 Ω		13	20	-	
Turn-Off DelayTime	t _{d(off)}	$I_D \cong$ - 5.6 A, V_{GEN} = - 10 V, R_q = 1 Ω		23	35		
Fall Time	t _f			9	18	-	
Turn-On Delay Time	t _{d(on)}			38	57	ns	
Rise Time	t _r	V_{DD} = - 15 V, R _L = 2.7 Ω		89	134		
Turn-Off DelayTime	t _{d(off)}	I_{off} $I_{\text{D}} \cong$ - 5.6 A, V_{GEN} = - 4.5 V, R_{g} = 1 Ω		22	33	1	
Fall Time	t _f			11	17	1	
Drain-Source Body Diode Characteris	stics					<u> </u>	
Continous Source-Drain Diode Current	١ _s	T _C = 25 °C			- 6.5		
Pulse Diode Forward Current	I _{SM}				- 30	A	
Body Diode Voltage	V _{SD}	I _S = - 5.6 A, V _{GS} = 0 V		- 0.71	- 1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			22	33	ns	
Body Diode Reverse Recovery Charge	Q _{rr}			17	26	nC	
Reverse Recovery Fall Time	t _a	I _F = - 5.6 A, dl/dt = 100 A/μs, T _J = 25 °C		13		ns	
Reverse Recovery Rise Time	t _b			9			

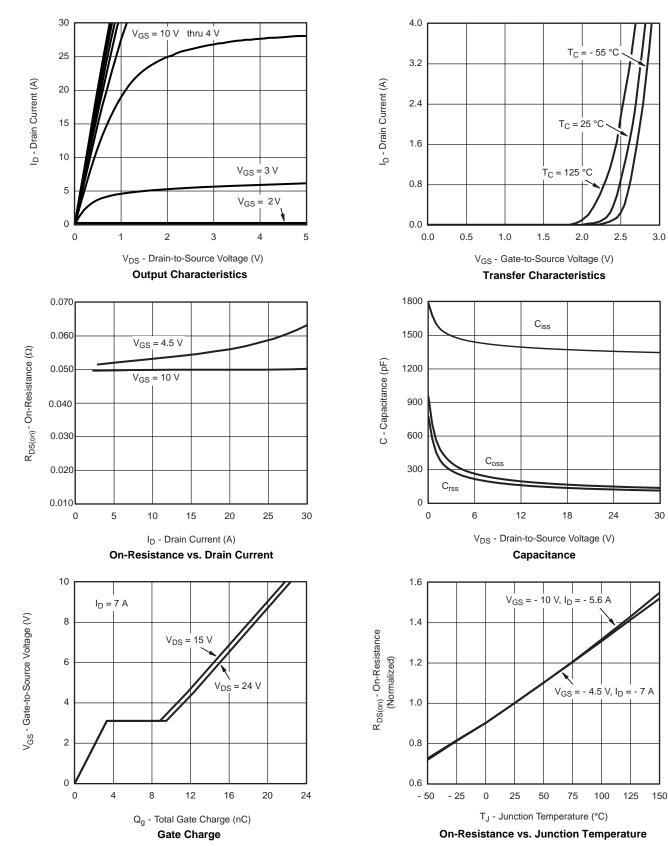
Notes:

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

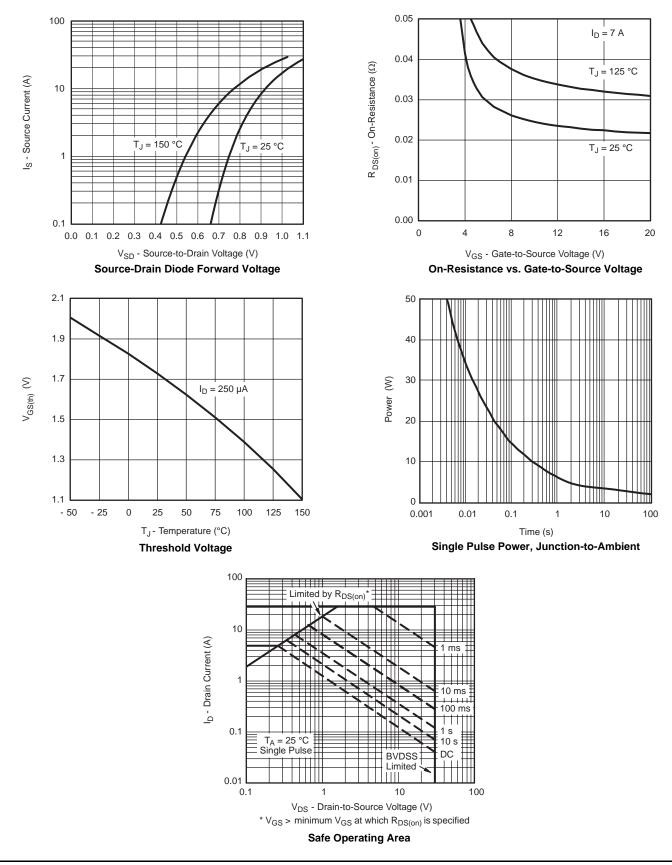
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

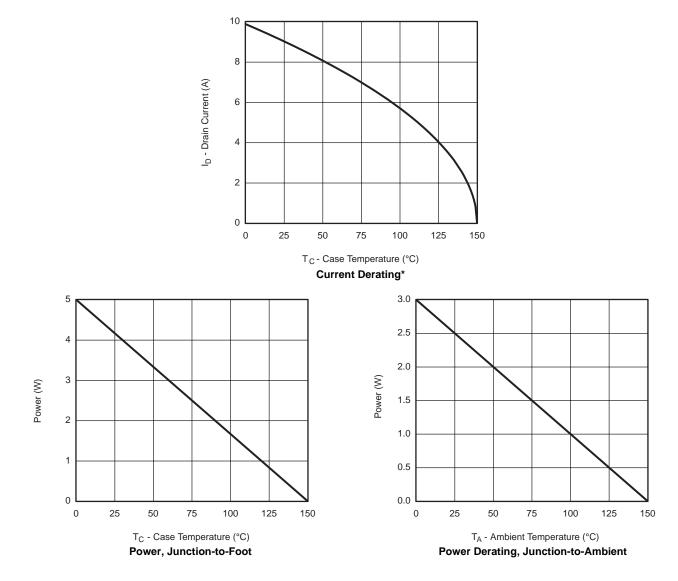






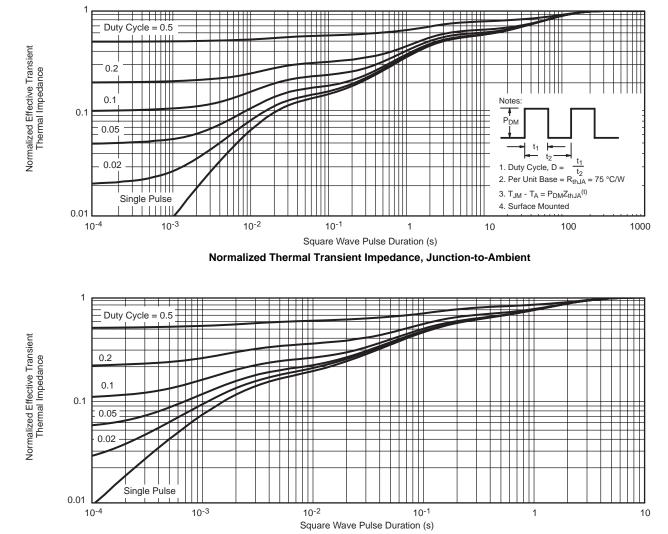






* The power dissipation P_D is based on $T_{J(max)}$ = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

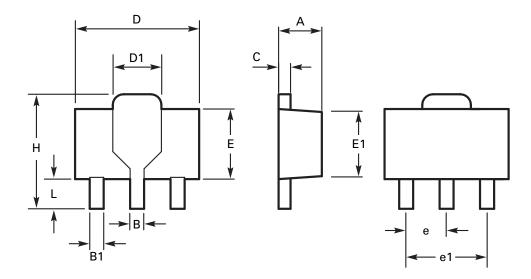




Normalized Thermal Transient Impedance, Junction-to-Foot



Package outline - SOT89



DIM	Millin	neters	Inc	Inches DIM Millimeters Inche		Millimeters		hes	
	Min	Max	Min	Max		Min	Max	Min	Max
А	1.40	1.60	0.550	0.630	E	2.29	2.60	0.090	0.102
В	0.44	0.56	0.017	0.022	E1	2.13	2.29	0.084	0.090
B1	0.36	0.48	0.014	0.019	е	1.50 BSC		0.059 BSC	
С	0.35	0.44	0.014	0.017	e1	3.00 BSC		0.118	BSC
D	4.40	4.60	0.173	0.181	Н	3.94	4.25	0.155	0.167
D1	1.62	1.83	0.064	0.072	L	0.89	1.20	0.035	0.047

Note: Controlling dimensions are in millimeters. Approximate dimensions are provided in inches



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