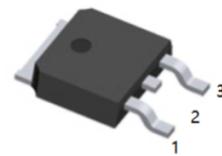


## Description

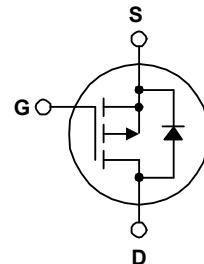
This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, audio amplifier, DC motor control, and variable switching power applications.



1.G    2.D    3.S  
TO-252(DPAK) top view

## Features

- $V_{DS}$  (V) = -60V
- $I_D$  = -12A ( $V_{GS}$  = -10V)
- $R_{DS(ON)} < 135m\Omega$  ( $V_{GS}$  = -10V)



## Absolute Maximum Ratings $T_C = 25^\circ\text{C}$ unless otherwise noted.

Symbol	Parameter		FQD17P06 / FQU17P06	Unit
$V_{DSS}$	Drain-Source Voltage		-60	V
$I_D$	Drain Current	- Continuous ( $T_C = 25^\circ\text{C}$ )	-12	A
		- Continuous ( $T_C = 100^\circ\text{C}$ )	-7.6	A
$I_{DM}$	Drain Current	- Pulsed (Note 1)	-48	A
$V_{GSS}$	Gate-Source Voltage		$\pm 25$	V
$E_{AS}$	Single Pulsed Avalanche Energy (Note 2)		300	mJ
$I_{AR}$	Avalanche Current (Note 1)		-12	A
$E_{AR}$	Repetitive Avalanche Energy (Note 1)		4.4	mJ
$dv/dt$	Peak Diode Recovery $dv/dt$ (Note 3)		-7.0	V/ns
$P_D$	Power Dissipation ( $T_A = 25^\circ\text{C}$ ) *		2.5	W
	Power Dissipation ( $T_C = 25^\circ\text{C}$ )		44	W
	- Derate above $25^\circ\text{C}$		0.35	W/ $^\circ\text{C}$
$T_J, T_{STG}$	Operating and Storage Temperature Range		-55 to +150	$^\circ\text{C}$
$T_L$	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds.		300	$^\circ\text{C}$

## Thermal Characteristics

Symbol	Parameter	FQD17P06	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	2.85	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient (Minimum Pad of 2-oz Copper), Max.	110	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient (*1 in <sup>2</sup> Pad of 2-oz Copper), Max.	50	

\* When mounted on the minimum pad size recommended (PCB Mount)

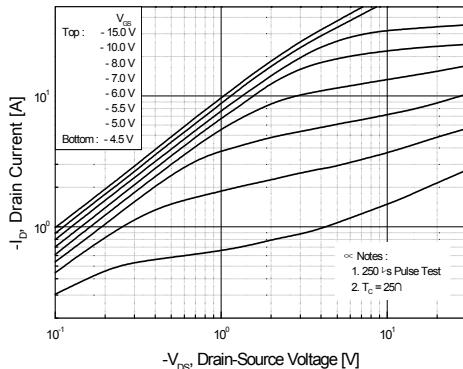
**Electrical Characteristics**  $T_C = 25^\circ\text{C}$  unless otherwise noted.

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
<b>Off Characteristics</b>						
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}} = 0 \text{ V}, I_D = -250 \mu\text{A}$	-60			V
$\Delta \text{BV}_{\text{DSS}} / \Delta T_J$	Breakdown Voltage Temperature Coefficient	$I_D = -250 \mu\text{A}$ , Referenced to $25^\circ\text{C}$		-0.06		$\text{V}/^\circ\text{C}$
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{\text{DS}} = -60 \text{ V}, V_{\text{GS}} = 0 \text{ V}$			-1	$\mu\text{A}$
		$V_{\text{DS}} = -48 \text{ V}, T_C = 125^\circ\text{C}$			-10	$\mu\text{A}$
$I_{\text{GSSF}}$	Gate-Body Leakage Current, Forward	$V_{\text{GS}} = -25 \text{ V}, V_{\text{DS}} = 0 \text{ V}$			-100	nA
$I_{\text{GSSR}}$	Gate-Body Leakage Current, Reverse	$V_{\text{GS}} = 25 \text{ V}, V_{\text{DS}} = 0 \text{ V}$			100	nA
<b>On Characteristics</b>						
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}} = V_{\text{GS}}, I_D = -250 \mu\text{A}$	-1.1	-2	-3.0	V
$R_{\text{DS(on)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}} = -10 \text{ V}, I_D = -6.0 \text{ A}$		110	135	$\text{m}\Omega$
$g_{\text{FS}}$	Forward Transconductance	$V_{\text{DS}} = -30 \text{ V}, I_D = -6.0 \text{ A}$		8.7		S
<b>Dynamic Characteristics</b>						
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}} = -25 \text{ V}, V_{\text{GS}} = 0 \text{ V}, f = 1.0 \text{ MHz}$		690	900	pF
$C_{\text{oss}}$	Output Capacitance			325	420	pF
$C_{\text{rss}}$	Reverse Transfer Capacitance			80	105	pF
<b>Switching Characteristics</b>						
$t_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}} = -30 \text{ V}, I_D = -8.5 \text{ A}, R_G = 25 \Omega$		13	35	ns
$t_r$	Turn-On Rise Time			100	210	ns
$t_{\text{d(off)}}$	Turn-Off Delay Time			22	55	ns
$t_f$	Turn-Off Fall Time		(Note 4)	60	130	ns
$Q_g$	Total Gate Charge	$V_{\text{DS}} = -48 \text{ V}, I_D = -17 \text{ A}, V_{\text{GS}} = -10 \text{ V}$		21	27	nC
$Q_{\text{gs}}$	Gate-Source Charge			4.2		nC
$Q_{\text{gd}}$	Gate-Drain Charge		(Note 4)	10		nC
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
$I_S$	Maximum Continuous Drain-Source Diode Forward Current				-12	A
$I_{\text{SM}}$	Maximum Pulsed Drain-Source Diode Forward Current				-48	A
$V_{\text{SD}}$	Drain-Source Diode Forward Voltage	$V_{\text{GS}} = 0 \text{ V}, I_S = -12 \text{ A}$			-4.0	V
$t_{\text{rr}}$	Reverse Recovery Time	$V_{\text{GS}} = 0 \text{ V}, I_S = -17 \text{ A}, dI_F / dt = 100 \text{ A}/\mu\text{s}$		92		ns
$Q_{\text{rr}}$	Reverse Recovery Charge			0.32		$\mu\text{C}$

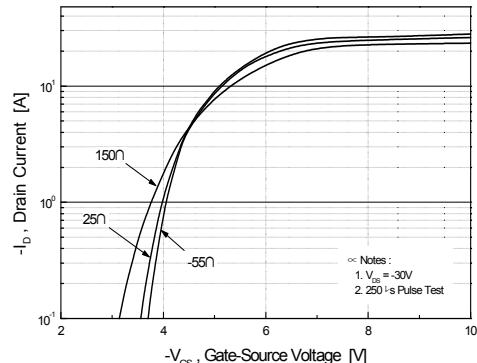
**NOTES:**

- Repetitive rating: pulse-width limited by maximum junction temperature.
- $L = 2.4 \text{ mH}, I_{\text{AS}} = -12 \text{ A}, V_{\text{DD}} = -25 \text{ V}, R_G = 25 \Omega$ , starting  $T_J = 25^\circ\text{C}$ .
- $I_{\text{SD}} \leq -17 \text{ A}, di/dt \leq 300 \text{ A}/\mu\text{s}, V_{\text{DD}} \leq \text{BV}_{\text{DSS}}$ , starting  $T_J = 25^\circ\text{C}$ .
- Essentially independent of operating temperature typical characteristics.

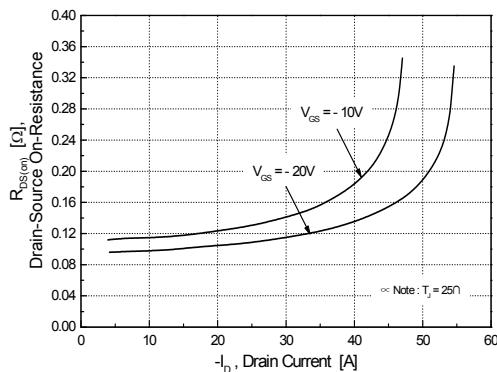
## Typical Performance Characteristics



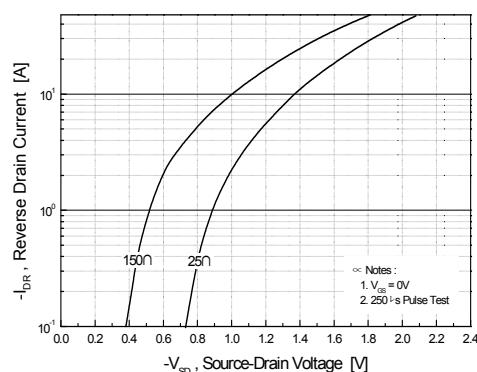
**Figure 1. On-Region Characteristics**



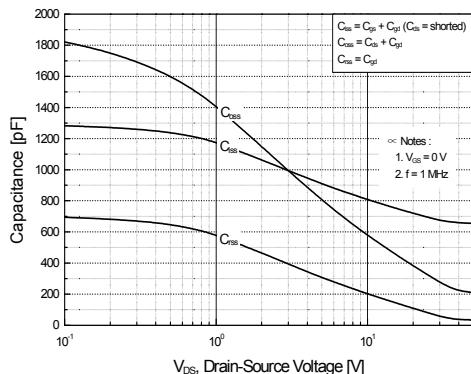
**Figure 2. Transfer Characteristics**



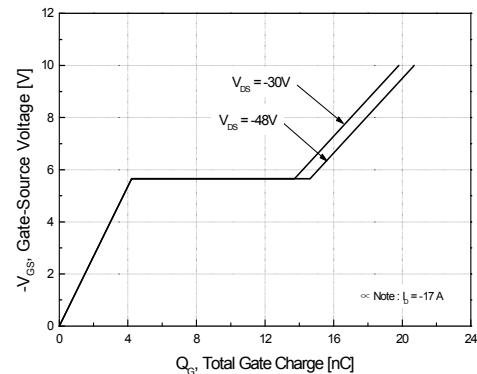
**Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage**



**Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature**

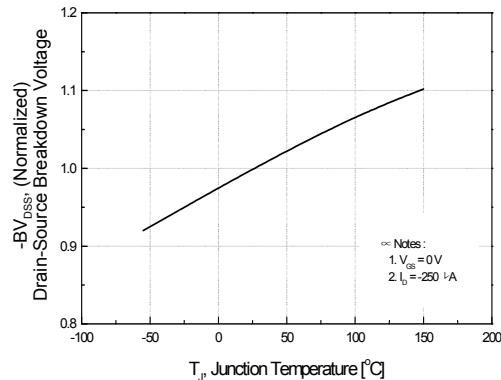


**Figure 5. Capacitance Characteristics**

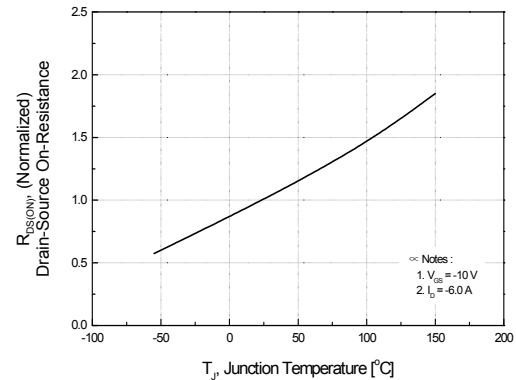


**Figure 6. Gate Charge Characteristics**

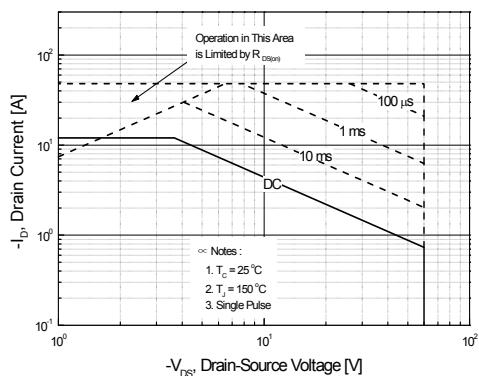
## Typical Performance Characteristics



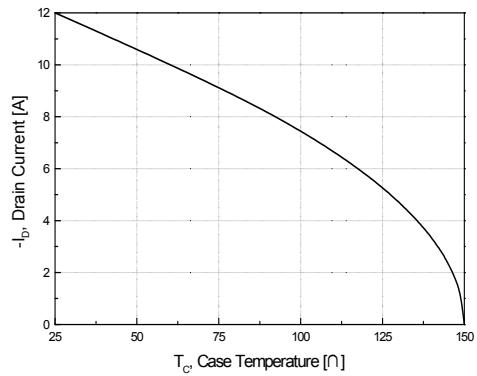
**Figure 7. Breakdown Voltage Variation  
vs. Temperature**



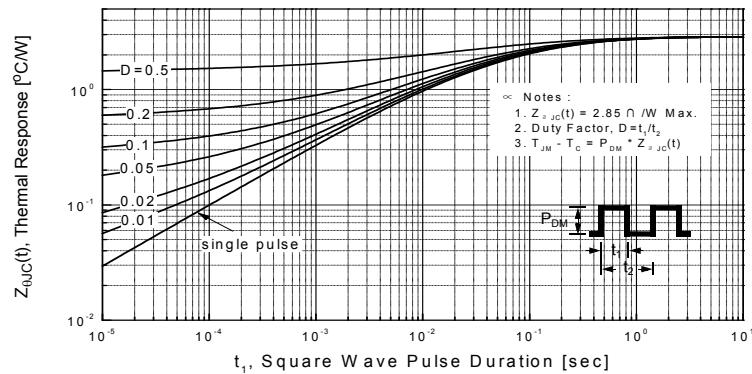
**Figure 8. On-Resistance Variation  
vs. Temperature**



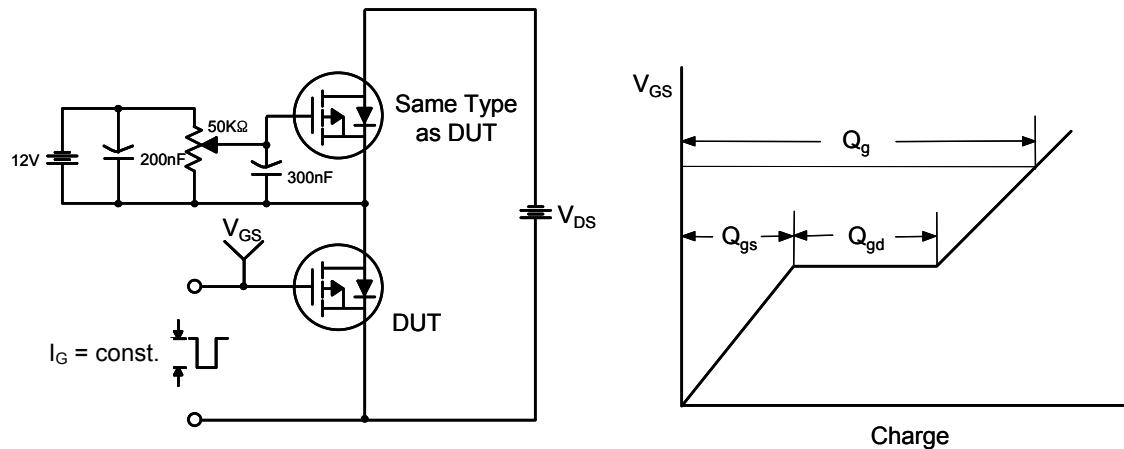
**Figure 9. Maximum Safe Operating Area**



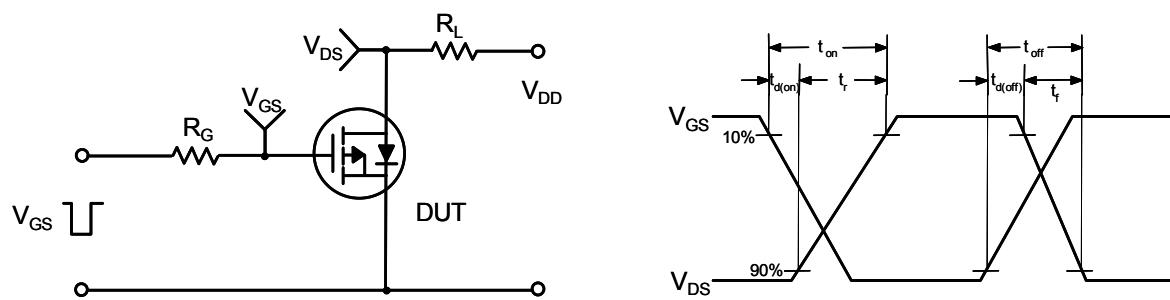
**Figure 10. Maximum Drain Current  
vs. Case Temperature**



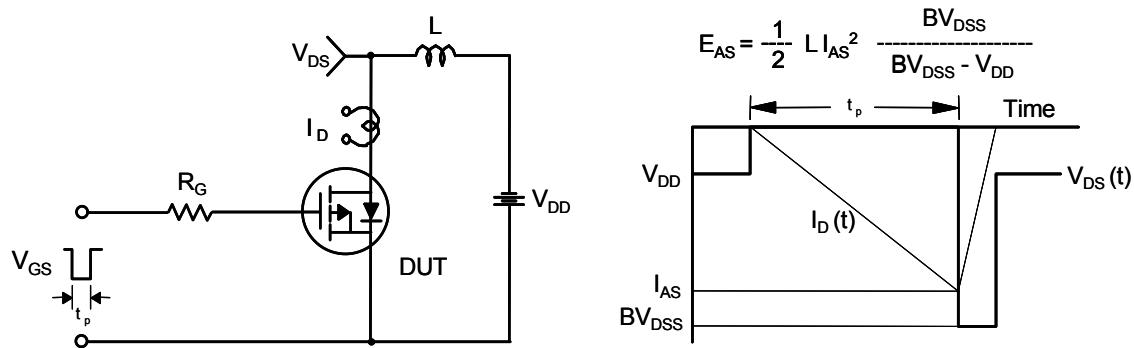
**Figure 11. Transient Thermal Response Curve**



**Figure 12. Gate Charge Test Circuit & Waveform**



**Figure 13. Resistive Switching Test Circuit & Waveforms**



**Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms**

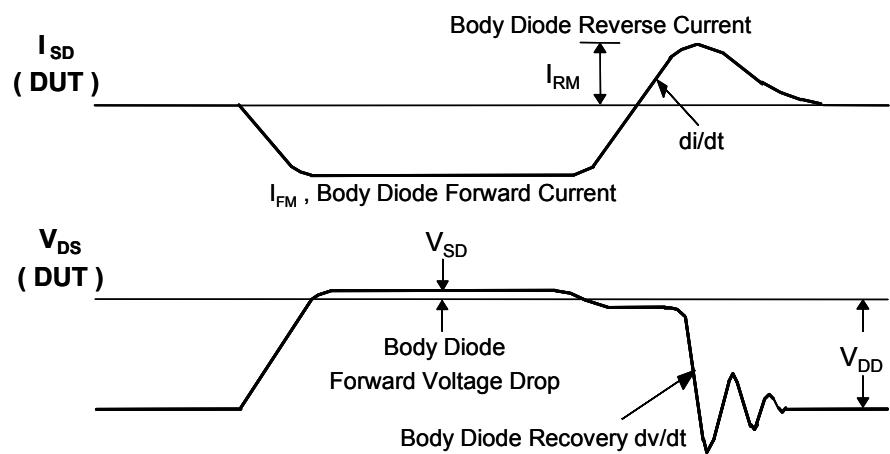
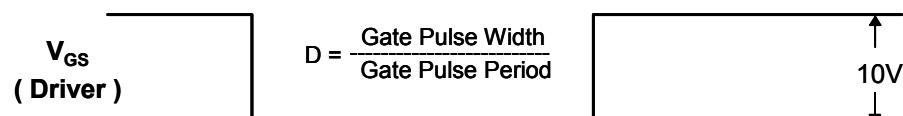
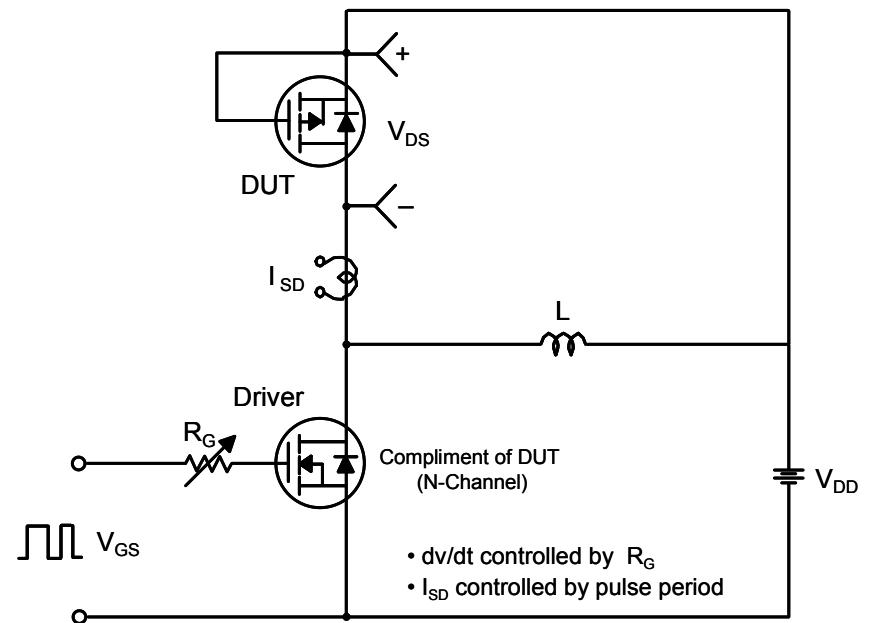
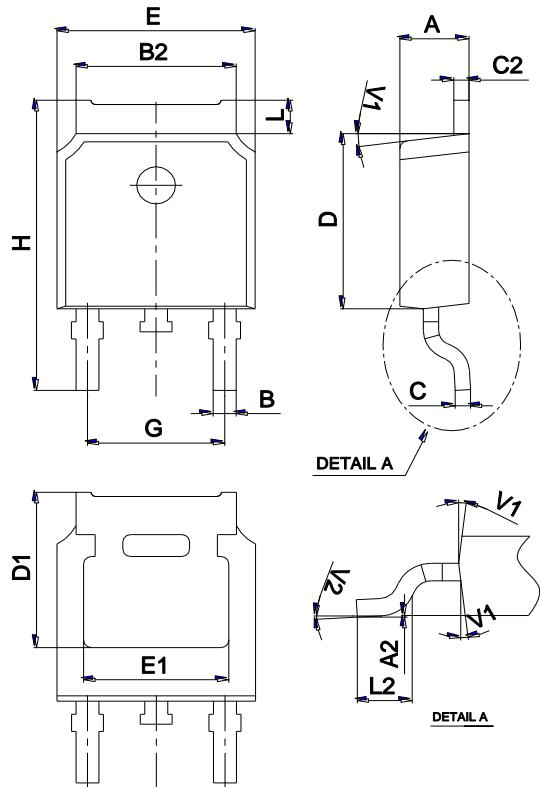


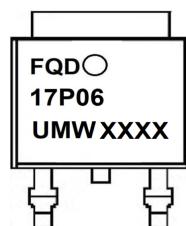
Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

## Package Mechanical Data TO-252



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.10		2.50	0.083		0.098
A2	0		0.10	0		0.004
B	0.66		0.86	0.026		0.034
B2	5.18		5.48	0.202		0.216
C	0.40		0.60	0.016		0.024
C2	0.44		0.58	0.017		0.023
D	5.90		6.30	0.232		0.248
D1	5.30REF			0.209REF		
E	6.40		6.80	0.252		0.268
E1	4.63			0.182		
G	4.47		4.67	0.176		0.184
H	9.50		10.70	0.374		0.421
L	1.09		1.21	0.043		0.048
L2	1.35		1.65	0.053		0.065
V1		7°			7°	
V2	0°		6°	0°		6°

## Marking



## Ordering information

Order code	Package	Baseqty	Deliverymode
UMWFQD17P06TM	TO-252	2500	Tape and reel