

## Ceramic Singlelayer DC Disc Capacitors, 4 kV<sub>DC</sub> General Purpose



### FEATURES

- High capacitance in small sizes
- Low losses
- Wide range of different lead styles
- Material categorization:  
for definitions of compliance please see  
[www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)


**RoHS**  
COMPLIANT

### APPLICATIONS

- Lighting ballasts
- Switching power supplies
- Bypassing, coupling and decoupling
- DC blocking

### DESIGN

The capacitors consist of a ceramic disc which is silver plated on both sides. Connection leads are made of tinned copper having diameters of 0.6 mm or 0.8 mm.

The capacitors may be supplied with straight or kinked leads having a lead spacing of 10.0 mm or 12.5 mm.

Coating is made of blue colored flame retardant epoxy resin in accordance with UL 94 V-0.

### CAPACITANCE RANGE

10 pF to 4.7 nF

### RATED VOLTAGE

4 kV<sub>DC</sub>

### DIELECTRIC STRENGTH

6000 V<sub>DC</sub>, 2 s Component test

### INSULATION RESISTANCE AT 500 V<sub>DC</sub>

≥ 10 000 MΩ (60 s)

### TOLERANCE ON CAPACITANCE

± 10 %, ± 20 %

### DISSIPATION FACTOR

Class 1:

$C < 30 \text{ pF: } \left( \frac{100 \text{ pF}}{C} + 0.7 \right) \times 10^{-4} \text{ max. (1 MHz)}$

$C \geq 30 \text{ pF: max. 0.1 % (1 MHz)}$

Class 2: max. 2.5 % (1 kHz)

QUICK REFERENCE DATA		
DESCRIPTION	VALUE	
Ceramic Class	1	2
Ceramic Dielectric	N750, Y5U	
Voltage (V <sub>DC</sub> )	4000	
Min. Capacitance (pF)	10	33
Max. Capacitance (pF)	470	4700
Mounting	Radial	

### OPERATING TEMPERATURE RANGE

-40 °C to +85 °C <sup>(1)</sup>

#### Note

<sup>(1)</sup> For explanation about the difference of operating temperature range and temperature characteristic of capacitance please see  
[www.vishay.com/doc?48299](http://www.vishay.com/doc?48299)

### TEMPERATURE CHARACTERISTICS

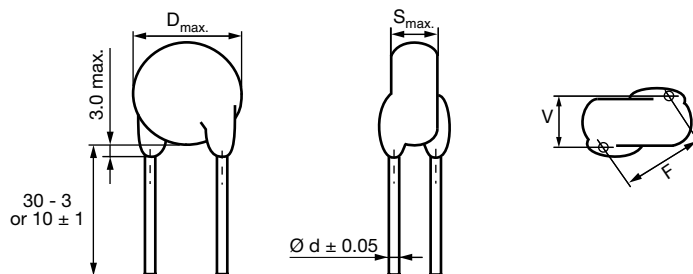
Class 1: N750

Class 2: Y5U

### SECTIONAL SPECIFICATIONS

Climatic category (according to EN 60068-1):  
40 / 085 / 21

## DIMENSIONS in millimeters



## ORDERING INFORMATION

CAPACITANCE (pF)	TOLERANCE (%)	BODY DIAMETER D <sub>max.</sub> (mm)	BODY THICKNESS S <sub>max.</sub> (mm)	LEAD SPACING <sup>(1)</sup> F (mm) ± 1 mm	LEAD DIAMETER <sup>(1)</sup> d (mm) ± 0.05 mm	WIDTH <sup>(1)</sup> V (mm) ± 0.5 mm	ORDERING CODE	
							MISSING DIGITS SEE ORDERING CODE BELOW	
N750								
10	± 10	7.0	4.3	10.0	0.6	1.7	HDU100KBD####KR	
15							HDU150KBD####KR	
22							HDU220KBD####KR	
33						1.9	HDU330KBD####KR	
47		HDU470KBD####KR						
68		HDU680KBD####KR						
82		HDU820KBD####KR						
100		HDU101KBD####KR						
150		HDU151KBD####KR						
220		HDU221KBD####KR						
330		HDU331KBD####KR						
470		HDU471KBD####KR						
Y5U								
33	± 20 <sup>(2)</sup>	8.0	4.5	12.5	0.6	1.9	HDE330#BD####KR	
47			5.0			2.3	HDE470#BD####KR	
68							HDE680#BD####KR	
100							HDE101#BD####KR	
150							HDE151#BD####KR	
220							HDE221#BD####KR	
330							2.5	HDE331#BD####KR
470								HDE471#BD####KR
680		HDE681#BD####KR						
1000		0.8	2.7		HDE102#BD####KR			
1500					HDE152#BD####KR			
2200					HDE222#BD####KR			
3300					HDE332#BD####KR			
4700					HDE472#BD####KR			

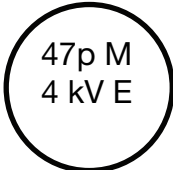

### Notes

<sup>(1)</sup> Standard lead configuration, other lead spacing and diameter available on request

<sup>(2)</sup> ± 10 % available on request



ORDERING CODE							
#	7 <sup>th</sup> digit	Capacitance tolerance	$\pm 10\% = K, \pm 20\% = M$				
###	10 <sup>th</sup> to 12 <sup>th</sup> digit	Lead configuration	See "General Information" <a href="http://www.vishay.com/doc?22001">www.vishay.com/doc?22001</a>				
Example	HDE	100	M	BD	EF0	K	R
	Series	Capacitance value	Tolerance code	Voltage code	Lead configuration	Internal code	RoHS compliant

MARKING	
 <p>HDU 10 pF to 100 pF HDE 33 pF to 1.5 nF</p>	 <p>HDU 150 pF to 470 pF HDE 2.2 nF to 4.7 nF</p>

## STORAGE

The capacitors must not be stored in a corrosive atmosphere, where sulphide or chloride gas, acid, alkali or salt are present. Exposure of the components to moisture, should be avoided. The solderability of the leads is not affected by storage of up to 24 months (temperature +10 °C to +35 °C, relative humidity up to 60 %). Class 2 ceramic dielectric capacitors are also subject to aging, see [www.vishay.com/doc?22001](http://www.vishay.com/doc?22001).

## SOLDERING

SOLDERING SPECIFICATIONS		
Soldering test for capacitors with wire leads: (according to IEC 60068-2-20, solder bath method)		
	SOLDERABILITY	RESISTANCE TO SOLDERING HEAT
Soldering temperature	235 °C $\pm$ 5 °C	260 °C $\pm$ 5 °C
Soldering duration	2 s $\pm$ 0.5 s	10 s $\pm$ 1 s
Distance from component body	$\geq$ 2 mm	$\geq$ 5 mm

## SOLDERING RECOMMENDATIONS

Soldering of the component should be achieved using a Sn60/40 type or a silver-bearing Sn62/36/2Ag type solder. Ceramic capacitors are very sensitive to rapid changes in temperature (thermal shock) therefore the solder heat resistance specification (see Soldering Specifications table) should not be exceeded. Subjecting the capacitor to excessive heating may result in thermal shocks that can crack the ceramic body. Similarly, excessive heating can cause the internal solder junction to melt.

## CLEANING

The components should be cleaned immediately following the soldering operation with vapor degreasers.

## SOLVENT RESISTANCE

The coating and marking of the capacitors are resistant to the following test method: IEC 60068-2-45 (method XA).

## MOUNTING

If a defined product stop is required for mounting on a PCB, a mechanically formed product stop (kinked or inline wire) or a mounting tool should be used.

We do not recommend modifying the lead terminals, e.g. bending or cropping. This action could break the coating or crack the ceramic insert. If however, the lead must be modified in any way, we recommend support of the lead with a clamping fixture next to the coating.



## OPERATING VOLTAGE

In case the voltage is applied to the circuit, starting as well as stopping, may generate irregular voltage for a transit period because of resonance or switching. Be sure to use a capacitor with a rated voltage range that includes these irregular voltages.

## OPERATING TEMPERATURE AND SELF-GENERATED HEAT

Keep the surface temperature of a capacitor below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself. When the capacitor is used in a high frequency, pulse, or similar application, it may have self-generated heat due to dielectric dissipation.

Temperature increase due to self-generated heating should not exceed 20 °C while operating at an atmosphere temperature of 25 °C.

When measuring, the surface temperature, make sure that the capacitor is not affected by radiant, conductive and convective heat by its surroundings. Excessive heat may lead to thermo-mechanical deterioration of the capacitor's characteristics and reliability.

## RELATED DOCUMENTS

General Information

[www.vishay.com/doc?22001](http://www.vishay.com/doc?22001)



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