

Features

- 1.2kV Schottky Rectifier
- Zero Reverse Recovery Current
- High-Frequency Operation
- Temperature-Independent Switching
- Extremely Fast Switching
- Positive Temperature Coefficient on V_F

Benefits

- Replace Bipolar with Unipolar Rectifiers
- Essentially No Switching Losses
- Higher Efficiency
- Reduction of Heat Sink Requirements
- Parallel Devices Without Thermal Runaway

Applications

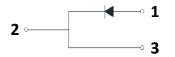
- Switch Mode Power Supplies (SMPS)
- Boost diodes in PFC or DC/DC stages
- Free Wheeling Diodes in Inverter stages
- AC/DC converters





Symbol	Parameter	Value	Unit	Test Conditions	Note
V_{RRM}	Repetitive Peak Reverse Voltage	1200	V		
V_{RSM}	Surge Peak Reverse Voltage	1300	V		
V_R	DC Peak Reverse Voltage	1200	V		
$\boldsymbol{I}_{\text{F}}$	Continuous Forward Current	19 9.5 5	А	T _c =25°C T _c =135°C T _c =161°C	Fig. 3
\mathbf{I}_{FRM}	Repetitive Peak Forward Surge Current	26 18	А	T_c =25°C, t_p =10 ms, Half Sine Pulse T_c =110°C, t_p =10 ms, Half Sine Pulse	
I _{FSM}	Non-Repetitive Forward Surge Current	46 36	А	T_c =25°C, t_p =10 ms, Half Sine Pulse T_c =110°C, t_p =10 ms, Half Sine Pulse	Fig. 8
$\mathbf{I}_{\text{F,Max}}$	Non-Repetitive Peak Forward Current	Current $\begin{array}{cccccccccccccccccccccccccccccccccccc$		T_c =25°C, t_p =10 μ s, Pulse T_c =110°C, t_p =10 μ s, Pulse	Fig. 8
P_{tot}	Power Dissipation			T _c =25°C T _c =110°C	Fig. 4
dV/dt	Diode dV/dt ruggedness	200	V/ns	V _R =0-650V	
∫i²dt	i²t value	10.6 6.5	A²s	T_c =25°C, t_p =10 ms T_c =110°C, t_p =10 ms	
T,	Operating Junction Range	-55 to +175	°C		
T _{stg}	Storage Temperature Range	-55 to +135	°C		
	TO220-2L Mounting Torque	1 8.8	Nm lbf-in	M3 Screw 6-32 Screw	







Electrical Characteristics

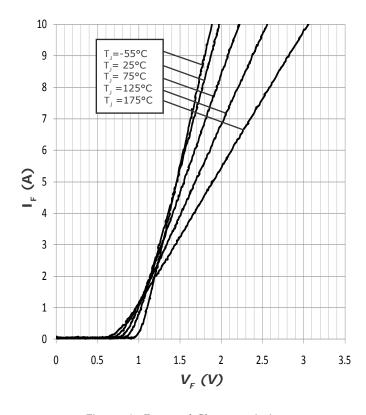
Symbol	Parameter	Тур.	Max.	Unit	Test Conditions	Note
V _F	Forward Voltage	1.4 1.9	1.8 3	V	$I_F = 5 \text{ A } T_J = 25^{\circ}\text{C}$ $I_F = 5 \text{ A } T_J = 175^{\circ}\text{C}$	Fig. 1
I_R	Reverse Current	20 40	150 300	μΑ	$V_R = 1200 \text{ V } T_J = 25^{\circ}\text{C}$ $V_R = 1200 \text{ V } T_J = 175^{\circ}\text{C}$	Fig. 2
Q _c	Total Capacitive Charge	27		nC	$V_R = 800 \text{ V}, I_F = 5\text{A}$ $di/dt = 200 \text{ A}/\mu\text{s}$ $T_J = 25^{\circ}\text{C}$	Fig. 5
С	Total Capacitance	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$		$V_R = 0 \text{ V, } T_J = 25^{\circ}\text{C, } f = 1 \text{ MHz}$ $V_R = 400 \text{ V, } T_J = 25^{\circ}\text{C, } f = 1 \text{ MHz}$ $V_R = 800 \text{ V, } T_J = 25^{\circ}\text{C, } f = 1 \text{ MHz}$	Fig. 6	
E _c	Capacitance Stored Energy	8.0		μĴ	V _R = 800 V	Fig. 7

Note: This is a majority carrier diode, so there is no reverse recovery charge.

Thermal Characteristics

Symbol	ymbol Parameter		Unit	Note
$R_{\theta JC}$	Thermal Resistance from Junction to Case	1.5	°C/W	Fig. 9

Typical Performance





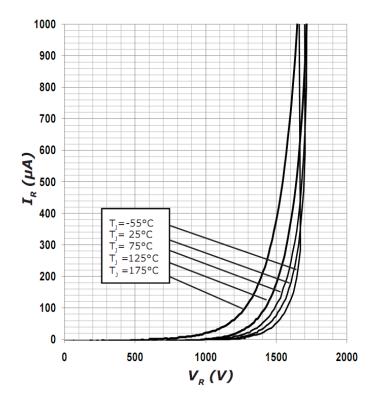
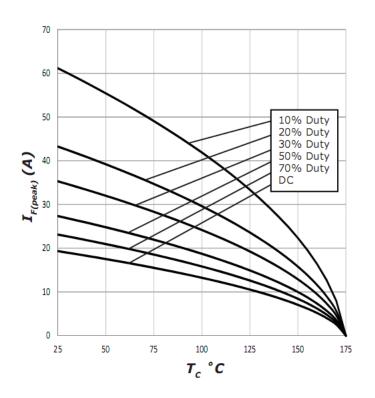


Figure 2. Reverse Characteristics



Typical Performance



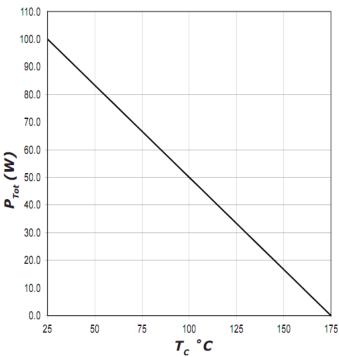


Figure 3. Current Derating

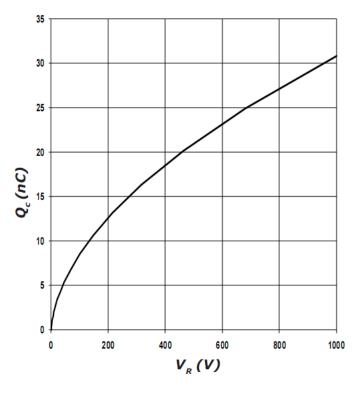


Figure 4. Power Derating

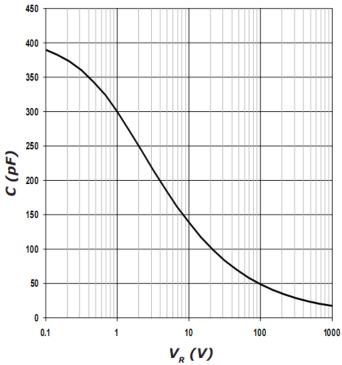


Figure 5. Recovery Charge vs. Reverse Voltage

Figure 6. Capacitance vs. Reverse Voltage

Diode Model

$$Vf_{T} = V_{T} + If * R_{T}$$

$$V_{T} = 0.96 + (T_{j} * -1.22*10^{-3})$$

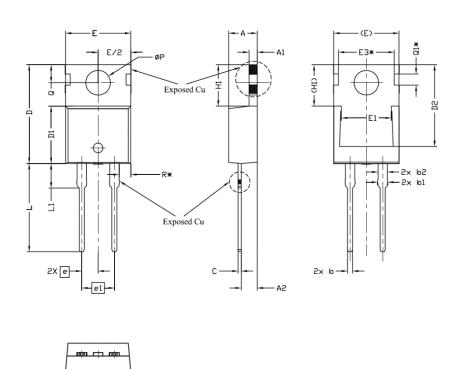
$$R_{T} = 0.08 + (T_{j} * 8.5*10^{-4})$$

$$V_{T} = R_{T}$$

Note: T_i is diode junction temperature in degrees Celsius

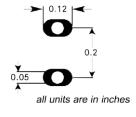


Package Information TO-220C-2L

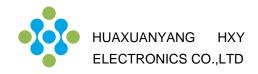


6) // ID 61		NOTES		
SYMBOL	MIN.	NOM.	MAX.	NOTES
Α	4,24	4,44	4,64	
A1	1.15	1.27	1.40	
A2	2.30	2.48	2.70	
ь	0.70	0.80	0.90	
b1	1.20	1.55	1.75	
b2	1.20	1.45	1.70	
С	0.40	0.50	0.60	
D	14.70	15.37	16.00	4
D1	8.82	8.92	9.02	
D2	12.43	12.73	12.83	5
E	9.96	10.16	10.36	4,5
E1	6,86	7,77	8.89	5
E3*				
e				
e1	5.08BSC			
H1	6.30	6.45	6.60	5,6
L	13,47	13.72	13.97	
L1	3.60	3.80	4.00	
ØP	3,75	3.84	3.93	
Q	2,60 2,80		3,00	
Q1*				
R*				

Recommended Solder Pad Layout



TO-220C-2L



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