

### **Discription**

The HESDNC3V3B1GF-A protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. Excellent clamping capability, low leakage, low capacitance, and fast response time provide best in class protection on designs that are exposed to ESD.

It gives designer the flexibility to protect one bi-directional line in applications where arrays are not practical.



DFN0603-2L

#### **Features**

- ★ Small Body Outline Dimensions: 0.61 mm x 0.31 mm
- ★ Low Body Height: 0.28 mm
- ★ Low Leakage
- ★ Response Time is Typically < 1 ns
- ★ ESD Rating of Class 3 per Human Body Model
- ★ IEC61000-4-2 Level 4 ESD Protection
- ★ These are Pb-Free Devices
- ★ We declare that the material of product compliance with RoHS requirements and Halogen Free.



Circuit Diagram

## Ordering information

Product ID	Pack	Qty(PCS)		
HESDNC3V3B1G-A	DFN0603-2L	15000		

### Absolute Ratings (T<sub>amb</sub>=25°C)

Parameter	Value	Units	
Peak Pulse Power (t <sub>p</sub> = 8/20µs)	90	W	
Maximum lead temperature for soldering during 10s	260	°C	
Storage Temperature Range	-55 to +150	°C	
Operating Temperature Range		-40 to +125	°C
Maximum junction temperature		150	°C
		±25 +20	KV
	Peak Pulse Power (tp = 8/20µs)  Maximum lead temperature for soldering during 10s  Storage Temperature Range  Operating Temperature Range  Maximum junction temperature  IEC61000-4-2 (ESD)	Peak Pulse Power (t <sub>p</sub> = 8/20µs)  Maximum lead temperature for soldering during 10s  Storage Temperature Range  Operating Temperature Range  Maximum junction temperature	Peak Pulse Power ( $t_p = 8/20\mu s$ )  Maximum lead temperature for soldering during 10s  Storage Temperature Range  -55 to +150  Operating Temperature Range  -40 to +125  Maximum junction temperature  150  IEC61000-4-2 (ESD)  air discharge

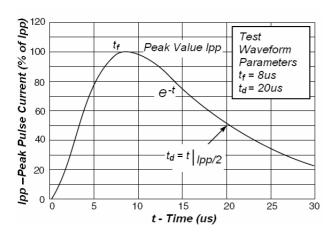


#### **ELECTRICAL CHARACTERISTICS**

	V <sub>RWM</sub> (V)	I <sub>R</sub> (μΑ) @ V <sub>RWM</sub>		V) @ I <sub>T</sub> ote 2)	I <sub>T</sub>	V <sub>C</sub> (V) @ I <sub>PP</sub> = 1 A (Note 3)	V <sub>C</sub> (V) @MAX I <sub>PP</sub> (Note 3)	I <sub>PP</sub> (A) (Note 3)	P <sub>PK</sub> (W) (Note 3)	C (pF)
Device	Max	Max	Min	Max	mA	Max	Max	Max	Max	Тур
HESDNC3V3B1GF-A	3.3	0.1	5.0	6.5	1.0	7	10	9	90	15

Other voltage available upon request.

- 2.  $V_{BR}$  is measured with a pulse test current IT at an ambient temperature of 25  $^{\circ}{\!\!\!^{\circ}}{\!\!\!^{\circ}}$
- 3. Surge current waveform per Figure 1.



100 90 80 70 % of Rated Power 60 50 Peak Pluse Power 40 8/20µs 30 20 10 25 50 75 100 125 150 Lead Temperature-  $TL(\mathcal{C})$ 

Fig1. Pulse Waveform

Fig2.Power Derating Curve

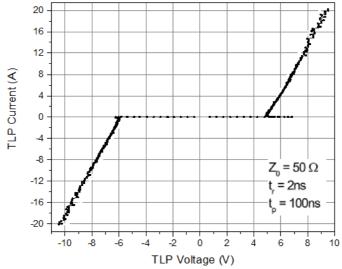
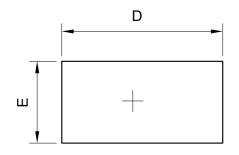
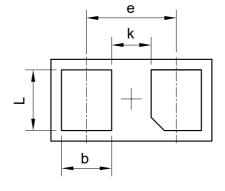


Fig3.TLP Measurement



# **Package Outline Dimension**

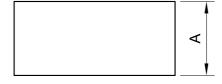




**TOP VIEW** 

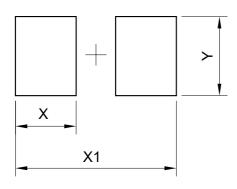
**BOTTOM VIEW** 

DFN0603-DL				
Dim	Min Typ. Ma			
D	0.58	0.61	0.64	
Е	0.28	0.31	0.34	
е	1	0.34	-	
L	0.20	0.23	0.26	
b	0.16	0.19	0.22	
Α	0.25	0.28	0.31	
k	0.12	0.15	0.18	
All Dimensions in mm				



SIDE VIEW

# **Suggested Pad layout**



DFN0603-DL		
DIM (mm)		
Х	0.23	
X1	0.61	
Υ	0.30	

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