

VBFB165R08S Datasheet N-Channel 650V (D-S) Super Junction Power MOSFET

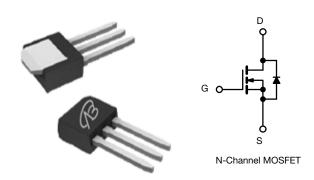
PRODUCT SUMMAI	RY	
V _{DS} (V) at T _J max.	6	50
R _{DS(on)} typ. (Ω) at 25 °C	V _{GS} = 10 V	0.550

FEATURES

- Low figure-of-merit (FOM) Ron x Qq
- Low input capacitance (Ciss)
- · Reduced switching and conduction losses
- Ultra low gate charge (Q_a)
- Avalanche energy rated (UIS)



TO-251



APPLICATIONS

- Server and telecom power supplies
- Switch mode power supplies (SMPS)
- Power factor correction power supplies (PFC)
- Lighting
 - High-intensity discharge (HID)
 - Fluorescent ballast lighting
- Industrial
 - Welding
 - Induction heating
 - Motor drives
 - Battery chargers
 - Renewable energy
 - Solar (PV inverters)

ABSOLUTE MAXIMUM RATINGS (T _C	= 25 °C, unl	ess otherwis	se noted)		
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-source voltage		V_{DS}	650	V	
Gate-source voltage			V_{GS}	± 30	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
Continuous drain current (T. – 150 °C)	uous drain current ($T_J = 150 ^{\circ}\text{C}$) V_{GS} at 10 V $\frac{T_C = 25 ^{\circ}\text{C}}{T_C = 100 ^{\circ}\text{C}}$		8		
Continuous drain current (1) = 130 °C)		T _C = 100 °C	I _D	5	Α
Pulsed drain current ^a		I _{DM}	24		
Linear derating factor			1.7	W/°C	
Single pulse avalanche energy b		E _{AS}	185	mJ	
Maximum power dissipation		P_{D}	128	W	
Operating junction and storage temperature range		T _J , T _{stg}	-55 to +150	°C	
Drain-source voltage slope	$T_{J} = 1$	125 °C	d\//d+	50	V/ns
Reverse diode dV/dt ^d		dV/dt	5.1	V/IIS	
Soldering recommendations (peak temperature) ^c	For	10 s		260	°C

Notes

- a. Repetitive rating; pulse width limited by maximum junction temperature
- b. V_{DD} = 100 V, starting T_J = 25 °C, L = 30 mH, R_g = 25 Ω , I_{AS} = 8.0 A
- c. 1.6 mm from case
- d. $I_{SD} \le I_D$, $dI/dt = 100 \text{ A/}\mu\text{s}$, starting $T_J = 25 \,^{\circ}\text{C}$

服务热线:400-655-8788

1



THERMAL RESISTANCE RATINGS				
PARAMETER	SYMBOL	TYP.	MAX.	UNIT
Maximum junction-to-ambient	R _{thJA}	=	62	°C/W
Maximum junction-to-case (drain)	R _{thJC}	-	0.65	G/ VV

PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static							•
Drain-source breakdown voltage	V _{DS}	Vgs =	= 0 V, ID = 250 μA	650	-	-	V
V _{DS} temperature coefficient	$\Delta V_{DS}/T_{J}$	Referenc	e to 25 °C, I _D = 1 mA	-	1.08	-	V/°C
Gate-source threshold Voltage (N)	V _{GS(th)}	V _{DS} =	= V _{GS} , I _D = 250 μA	2.0	-	4.0	V
Oale as a salada a			V _{GS} = ± 20 V	-	-	± 100	nA
Gate-source leakage	I _{GSS}		V _{GS} = ± 30 V	-	-	± 1	μΑ
		V _{DS} =	$V_{DS} = 650 \text{ V}, V_{GS} = 0 \text{ V}$		-	1	
Zero gate voltage drain current	I _{DSS}	V _{DS} = 520 \	/, V _{GS} = 0 V, T _J = 125 °C	-	-	10	μA
Drain-source on-state resistance	R _{DS(on)}	V _{GS} = 10 V	ID =2.5 A	_	0.550	=	Ω
Forward transconductance	9 _{fs}	V_{DS}	= 30 V, I _D = 2.5A	-	8.7	-	S
Dynamic							
Input capacitance	C _{iss}	$V_{GS} = 0 V$,		-	2600	-	pF
Output capacitance	C _{oss}	1	$V_{GS} = 0 \text{ V},$ $V_{DS} = 100 \text{ V},$		81	-	
Reverse transfer capacitance	C _{rss}	f = 1 MHz		-	9	-	
Effective output capacitance, energy related ^a	C _{o(er)}			-	58	-	
Effective output capacitance, time related ^b	C _{o(tr)}	$V_{DS} = 0$	/ to 480 V, V _{GS} = 0 V	-	296	-	
Total gate charge	Qg			-	40	122	
Gate-source charge	Q _{gs}	V _{GS} = 10 V	$I_D = 5 A, V_{DS} = 480 V$	-	16	-	nC
Gate-drain charge	Q _{gd}			-	20	-	
Turn-on delay time	t _{d(on)}			-	22	44	
Rise time	t _r	$V_{DD} = 480 \text{ V}, I_{D} = 5 \text{ A},$		-	24	48	
Turn-off delay time	t _{d(off)}	V _{GS}	$V_{DD} = 480 \text{ V}, I_D = 3 \text{ A},$ $V_{GS} = 10 \text{ V}, R_a = 9.1 \Omega$		71	142	ns
Fall time	t _f		J	-	26	52	
Gate input resistance	R_g	f = 1 MHz, open drain		0.3	0.7	1.4	Ω
Drain-Source Body Diode Characteristic	s						•
Continuous source-drain diode current	I _S	MOSFET sym showing the	MOSFET symbol showing the		-	8	
Pulsed diode forward current	I _{SM}	integral revers p - n junction	ブープ	-	-	24	A
Diode forward voltage	V _{SD}	T _J = 25 °C	C, I _S = 5 A, V _{GS} = 0 V	-	-	1.2	V
Reverse recovery time	t _{rr}	-		-	66	132	ns
Reverse recovery charge	Q _{rr}	$T_J = 25 \text{ °C, } I_F = I_S = 5 \text{ A,}$ $dI/dt = 100 \text{ A/}\mu\text{s, } V_R = 25 \text{ V}$		-	6.4	12.8	μC
Reverse recovery current	I _{RRM}			_	27	_	A

Notes

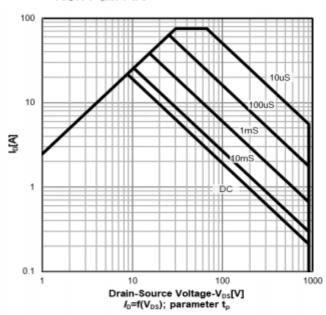
- a. $C_{oss(er)}$ is a fixed capacitance that gives the same energy as C_{oss} while V_{DS} is rising from 0 % to 80 % V_{DSS} b. $C_{oss(tr)}$ is a fixed capacitance that gives the same charging time as C_{oss} while V_{DS} is rising from 0 % to 80 % V_{DSS}

服务热线:400-655-8788 2

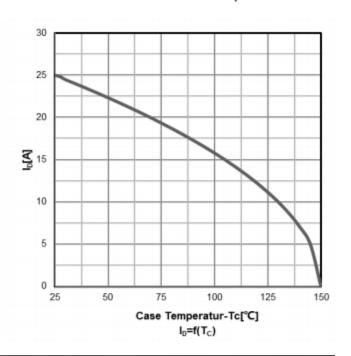


TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

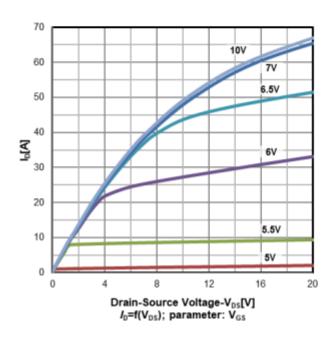
Safe operating area TC=25 °C Non FullPAK



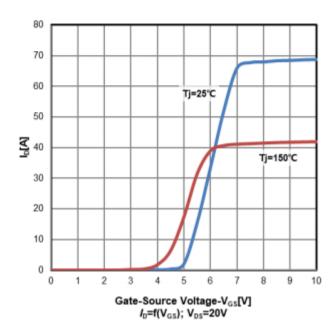
Drain current vs temperature



Typ. output characteristics T_j =25 $^{\circ}C$



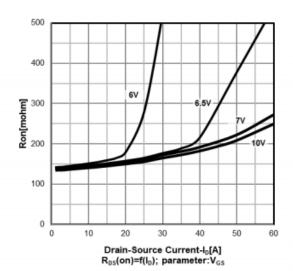
Typ. transfer characteristics



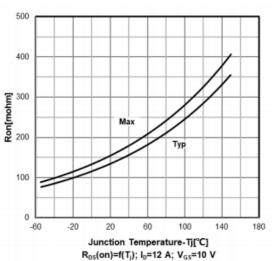
服务热线:400-655-8788



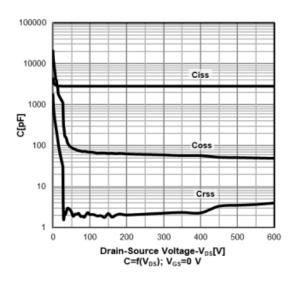
Typ. drain-source on-state resistance



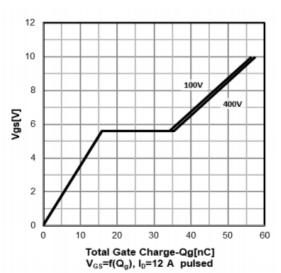
On resistance vs temperature



Typ. capacitances

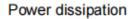


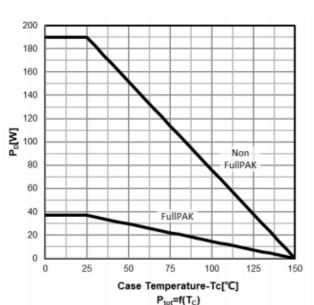
Typ. gate charge characteristics



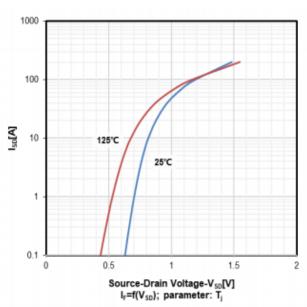
报务热线:400-655-8788



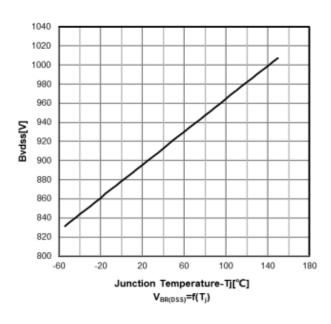




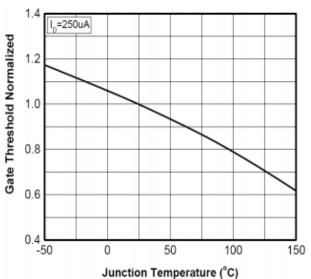
Forward characteristics of reverse diode



Drain-source breakdown voltage



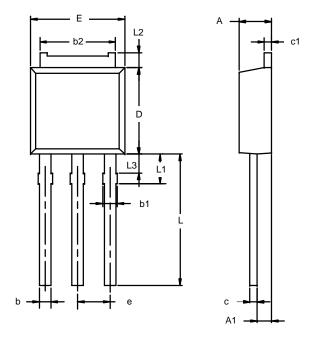
Normalized $V_{\text{GS(th)}}$ characteristics



服务热线:400-655-8788 5



TO-251



Note: Dimension L3 is for reference only.

	MILLIN	IETERS	INCHES		
Dim	Min	Max	Min	Max	
Α	2.21	2.38	0.087	0.094	
A 1	0.89	1.14	0.035	0.045	
b	0.71	0.89	0.028	0.035	
b1	0.76	1.14	0.030	0.045	
b2	5.23	5.43	0.206	0.214	
С	0.46	0.58	0.018	0.023	
с1	0.46	0.58	0.018	0.023	
D	5.97	6.22	0.235	0.245	
Е	6.48	6.73	0.255	0.265	
е	2.28 BSC		0.090	BSC	
L	8.89	9.53	0.350	0.375	
L1	1.91	2.28	0.075	0.090	
L2	0.89	1.27	0.035	0.050	
L3	1.15	1.52	0.045	0.060	

服务热线:400-655-8788 6



Disclaimer

All products due to improve reliability, function or design or for other reasons, product specifications and data are subject to change without notice.

Taiwan VBsemi Electronics Co., Ltd., branches, agents, employees, and all persons acting on its or their representatives (collectively, the "Taiwan VBsemi"), assumes no responsibility for any errors, inaccuracies or incomplete data contained in the table or any other any disclosure of any information related to the product.(www.VBsemi.com)

Taiwan VBsemi makes no guarantee, representation or warranty on the product for any particular purpose of any goods or continuous production. To the maximum extent permitted by applicable law on Taiwan VBsemi relinquished: (1) any application and all liability arising out of or use of any products; (2) any and all liability, including but not limited to special, consequential damages or incidental; (3) any and all implied warranties, including a particular purpose, non-infringement and merchantability guarantee.

Statement on certain types of applications are based on knowledge of the product is often used in a typical application of the general product VBsemi Taiwan demand that the Taiwan VBsemi of. Statement on whether the product is suitable for a particular application is non-binding. It is the customer's responsibility to verify specific product features in the products described in the specification is appropriate for use in a particular application. Parameter data sheets and technical specifications can be provided may vary depending on the application and performance over time. All operating parameters, including typical parameters must be made by customer's technical experts validated for each customer application. Product specifications do not expand or modify Taiwan VBsemi purchasing terms and conditions, including but not limited to warranty herein.

Unless expressly stated in writing, Taiwan VBsemi products are not intended for use in medical, life saving, or life sustaining applications or any other application. Wherein VBsemi product failure could lead to personal injury or death, use or sale of products used in Taiwan VBsemi such applications using client did not express their own risk. Contact your authorized Taiwan VBsemi people who are related to product design applications and other terms and conditions in writing.

The information provided in this document and the company's products without a license, express or implied, by estoppel or otherwise, to any intellectual property rights granted to the VBsemi act or document. Product names and trademarks referred to herein are trademarks of their respective representatives will be all.

Material Category Policy

Taiwan VBsemi Electronics Co., Ltd., hereby certify that all of the products are determined to be oHS compliant and meets the definition of restrictions under Directive of the European Parliament 2011/65 / EU, 2011 Nian. 6. 8 Ri Yue restrict the use of certain hazardous substances in electrical and electronic equipment (EEE) - modification, unless otherwise specified as inconsistent.(www.VBsemi.com)

Please note that some documents may still refer to Taiwan VBsemi RoHS Directive 2002/95 / EC. We confirm that all products identified as consistent with the Directive 2002/95 / EC European Directive 2011/65 /.

Taiwan VBsemi Electronics Co., Ltd. hereby certify that all of its products comply identified as halogen-free halogen-free standards required by the JEDEC JS709A. Please note that some Taiwanese VBsemi documents still refer to the definition of IEC 61249-2-21, and we are sure that all products conform to confirm compliance with IEC 61249-2-21 standard level JS709A.