

High Efficiency, 5-Watt PA Tuning Range: 0.7 to 1.0 GHz





Features

Reference: 5.0V/915MHz/CW

Gain: 34.0 dBIccq: 125 mA

OP1dB: 36.0 dBmPsat: 37.0 dBm

PAE: 58% @Psat

Reference: 3.5V/915MHz/CW

Gain: 34.0 dBIccq: 125 mAOP1dB: 34.0

Psat: 35.0 dBmPAE: 58% @Psat

Flexible Bias Voltage and Current

Process: InGaP HBT

Applications

900 MHz ISM

Revision Date: 05/07/20

Automatic Meter Reader

RFID

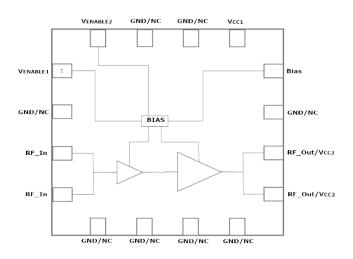
Product Description

GRF5509 is a high efficiency PA that delivers 4 Watts OP1dB with Psat approaching 5 watts with Vcc at 5.0 volts and an Iccq of 125 mA. PAE at Psat is roughly 58%.

The device can be tuned over a range of frequencies from around 700 MHz to 1000 MHz with typical fractional bandwidths of 3 to 5%.

For frequencies in the 400-500 MHz range, GRF5504 offers high output power and slightly higher efficiency with the same package and pinouts.

Consult with the GRF applications engineering team for custom tuning/evaluation board data.



3.0 x 3.0 mm QFN-16



Preliminary GRF5509

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Absolute Ratings:

Parameter	Symbol	Min.	Max.	Unit
Drain Voltage	Vcc		5.5	V
Transient Average RF Input Power: (Load VSWR < 2:1; Duration: <1 hour)	P _{IN MAX}		TBD	dBm
Operating Temperature (Package Heat Sink)	T _{AMB}	-40	85	°C
Maximum Junction Temperature (MTTF > 10^6 Hours)	Тмах		170	°C
Maximum Dissipated Power Stage 1 (at Psat)	P _{DISS MAX}		400	mW
Maximum Dissipated Power Stage 2 (at Psat)	P _{DISS MAX}		3800	mW
Ruggedness: Vcc: 5.0 volt at Psat (all phase angles)	VSWR	8:1		_
Electrostatic Discharge:				
Charged Device Model: (TBD)	CDM	1500		V
Human Body Model: (TBD)	HBM	250		V
Storage:				
Storage Temperature	T _{STG}	-65	150	°C
Moisture Sensitivity Level	MSL		1	



Caution! ESD Sensitive Device



Exceeding Absolute Maximum Rating conditions may cause permanent damage to the device.

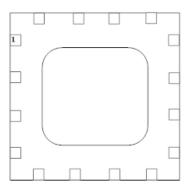
Note: For manufacturing information, see the Guerrilla-RF.com website for the following document located on the GRF5509 landing page: Manufacturing Note—MN-001 Product Tape and Reel, Solderability and Package Outline Specification.

Link to manufacturing note



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Pin Out (Top View)



Pin Assignments:

Pin	Name	Description	Note		
1	VENABLE1	Enable1 Voltage Input	Venable1 and series resistor set lccq1 for the input stage. Venable < =0.2 volts disables device. On-die pull-down resistor will turn the part off if this node is allowed to float.		
2	GND/NC	No Connect or Ground	No internal connection to die		
3	RF_In	RF Input	Pins 3-4 tied together on system board		
4	RF_In	RF Input	Pins 3-4 tied together on system board		
5	GND/NC	Ground or No Connect	No internal connection to die		
6	GND/NC	Ground or No Connect	No internal connection to die		
7	GND/NC	Ground or No Connect	No internal connection to die		
8	GND/NC Ground or No Connect		No internal connection to die		
9	RF_Out/Vcc2	PA Output/Bias Voltage	Pins 9-10 tied together on system board. Supply Vcc2 here.		
10	RF_Out/Vcc2	PA Output/Bias	Pins 9-10 tied together on system board. Supply Vcc2 here.		
11	GND/NC	Ground or No Connect	No internal connection to die		
12	Bias	Bias Circuit Supply	Connect to Vcc2 through external resistor		
13	VCC1	Bias Voltage	Supply Vcc1 here.		
14	GND/NC	Ground or No Connect	No internal connection to die		
15	GND/NC	Ground or No Connect	No internal connection to die		
16	VENABLE2	Enable2 Voltage Input	Venable2 and series resistor set lccq2 for the output stage. Venable < =0.2 volts disables device. On-die pull-down resistor will turn the part off if this node is allowed to float.		
PKG BASE	GND	Ground	Provides DC and RF ground for LNA, as well as thermal heat sink. Recommend multiple 8 mil vias beneath the package for optimal RF and thermal performance. Refer to evaluation board top layer graphic on schematic page.		



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Nominal Operating Parameters:

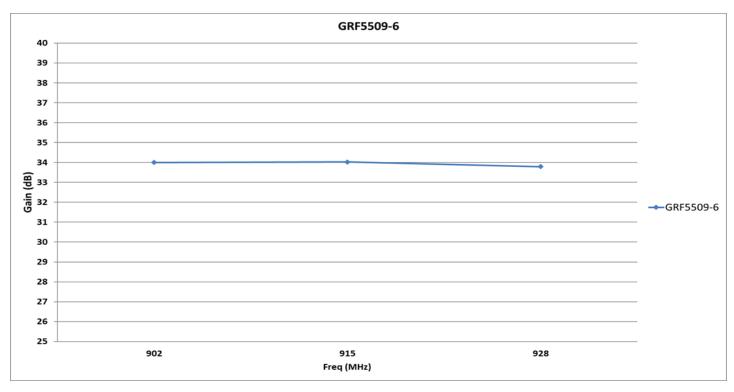
Boundary	Complete	Specification					
Parameter	Symbol	Min. Typ.		Max.	Unit	Condition	
Performance: (902 to 928 MHz Tune @25C)						Bias: 5.0 V /125 mA lccq unless otherwise noted.	
Test Frequency	FTEST		915		MHz		
Gain (Small Signal)	S(2,1)		34.0		dB		
Output 1dB Compression Point	OP1dB		36.0		dBm		
Saturated Output Power	Psat		37.0		dBm		
Power Added Efficiency (at Psat)	PAE		58.0		%		
Switching Rise Time	Trise		TBD		ns		
Switching Fall Time	TFALL		TBD		ns		
Quiescent Supply Current (Iccq1+Iccq2)	IDDQ		125		mA	Vcc = Venable1/2 = 5.0 volts	
Enable Current 1	lenable1		1.5		mA		
Enable Current 2	lenable2		TBD		mA		
Disabled Mode							
Supply Current (Leakage)	I _{DD}		1.0		uA		
Thermal Data (RF Applied)							
Thermal Resistance: (IR Scan Method)	Θјс		19		°C/W	RF Input: 5 dBm	
Junction Temperature @ +85C Reference (package heat sink)	Tchannel		152		°C	Vcc: 5.0 volts; Icc: 1700 mA Pre: 5.0 W; Polss: 3.5 W	

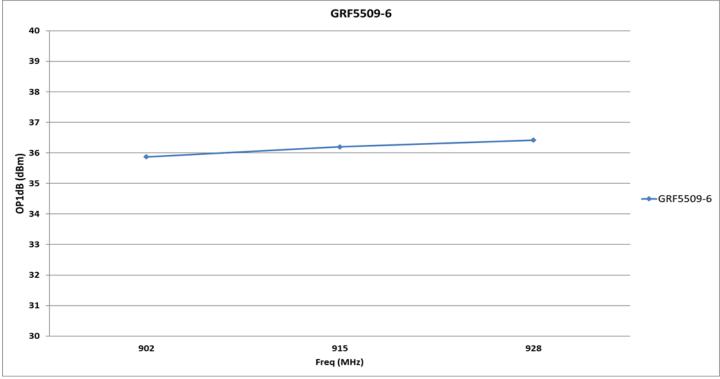
Note: MTTF >10^6 hours for TJUNCTION < =170 degrees C.



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GRF5509 Evaluation Board Measured Data; Vcc: 5.0 volts

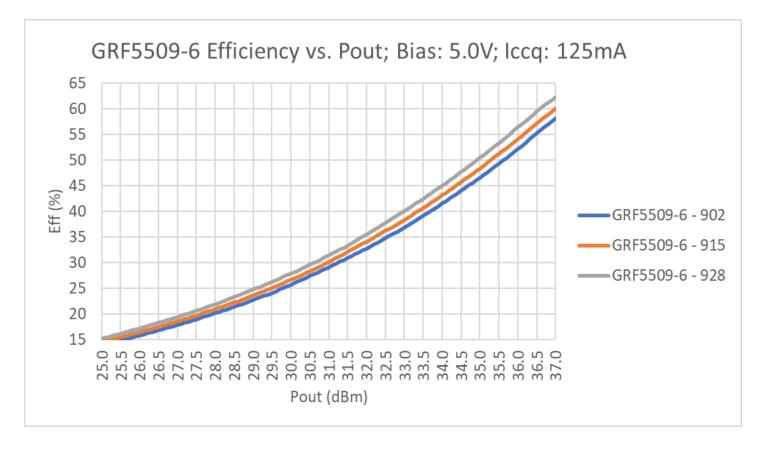






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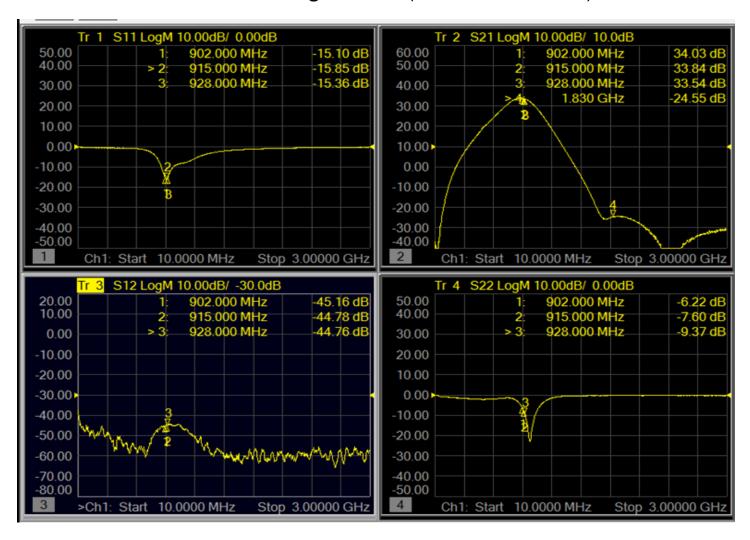
GRF5509 Evaluation Board Measured Data; Vcc: 5.0 volts





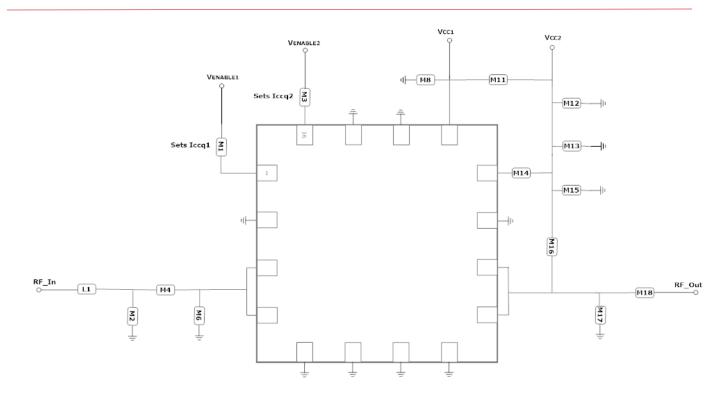
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GRF5509 Evaluation Board Small Signal S-Pars: (902 to 928 MHz Tune)

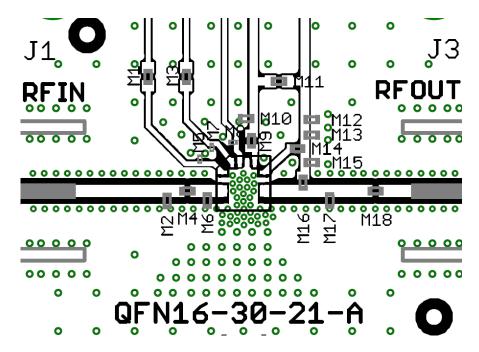




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GRF5509 Application Schematic: (902 to 928 MHz)



GRF5509 Evaluation Board Assembly Drawing



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GRF5509 Evaluation Board BOM: (902 to 928 MHz)

Component	Туре	Manufacturer	Family	Value	Package Size	Substitution
M1	Resistor	Various	5%	Sets Iccq1	0402	ok
L1 (Adjacent to M2)	Inductor	Murata	LQG	6.8 nH	0402	ok
M2	Inductor	Murata	LQG	6.8 nH	0402	ok
M3	Resistor	Various	5%	Sets Iccq2	0402	ok
M4	Capacitor	Murata	GJM	5.6 pF	0402	ok
M9	Resistor	Various	5%	0 Ohm	0402	ok
M10	Capacitor	Murata	GRM	0.1 uF	0402	ok
M11	Resistor	Various	5%	0 Ohm	0402	ok
M13	Capacitor	Murata	GRM	0.1 uF	0402	ok
M14	Resistor (Jumper)	Various	5%	0 Ohm	0402	ok
M15	Capacitor	Murata	GRM	100 pF	0402	ok
M16	Inductor: High Q	Coilcraft	НР	24 nH	0603	ok
M17	Capacitor	Murata	GJM	12.0 pF	0402	ok
M18	Capacitor	Murata	GJM	20 pF	0402	ok
Evaluation Board	QFN16-30-21-A					

GRF5509 Bias Resistor (M1) Selection Curves (TBD)

GRF5509 Bias Resistor (M2) Selection Curves (TBD)

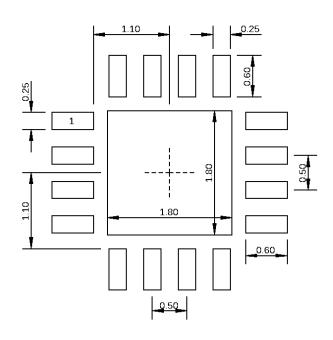


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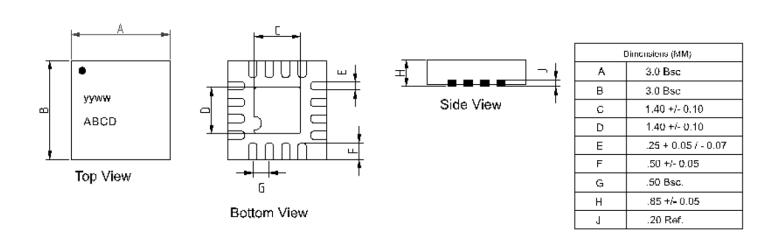
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Dimensions in millimeters

3.0 mm QFN-16 Suggested PCB Footprint (Top View)



3.0 mm QFN-16 Package Dimensions

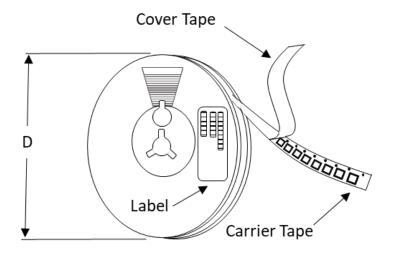


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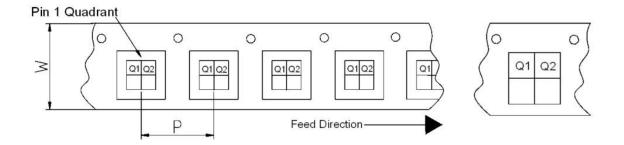
Tape and Reel Information:

Guerrilla RF's Tape and Reel specification complies with the Electronics Industries Association (EIA) standards for 'Embossed Carrier Tape of Surface Mount Components for Automatic Handling". Reference EIA-481. See the table on the following page for Tape and Reel specifications along with units per reel.

Devices are loaded with pins down into the carrier pocket with protective cover tape, wound into a plastic reel. Each reel will be packaged in a cardboard box. There will be product labels on the reel, the protective ESD bag and the outside surface of the box.



Tape and Reel Packaging with Reel Diameter Noted (D)



Carrier Tape Width (W), Pitch (P), Feed Direction and Pin 1 Quadrant Information



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Tape and Reel Specification and Device Package Information Table

Package			Carrier Tape			Reel		
Туре	Dimensions (mm)	Leads	Weight (mg)	Width (W) (mm)	Pocket Pitch (P) (mm)	Pin 1 Quad- rant	Diameter (D) (inches)	Units per Reel
QFN	2.0 x 2.0 x 0.50	12	7	8	4	Q1	7	2500
QFN	3.0 x 3.0 x 0.85	16	24	12	8	Q1	7	1500
DFN	1.5 x 1.5 x 0.45	6	4	8	4	Q1	7	2500
DFN	2.0 x 2.0 x 0.75	8	12	8	4	Q1	7	2500
LFM	3.5 x 3.5 x 0.75	See note	TBD	12	8	Q2	7	1500
LFM	4.0 x 4.0 x 0.75	See note	TBD	12	8	Q2	7	1500

Note: Lead count may vary. Reference applicable product data sheet



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Data Sheet Release Status:	Notes
Advance	S-parameter and NF data based on EM simulations for the fully packaged device using foundry supplied transistor s-parameters. Linearity estimates based on device size, bias condition and experience with related devices.
Preliminary	All data based on evaluation board measurements in the Guerrilla RF Applications Lab.
Released	All data based on device qualification data. Typically, this data is nearly identical to the data found in the preliminary version. Max and min values for key RF parameters are included.

Information in this datasheet is specific to the Guerrilla RF, Inc. ("Guerrilla RF") product identified.

Revision Date: 05/07/20

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