

650 V, 8 A Silicon Carbide Schottky Diode

Features

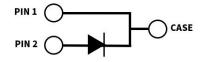
- 650-Volt Schottky rectifier
- Zero reverse recovery current
- Zero forward recovery voltage
- High-frequency operation
- Temperature-independent switching behavior
- Extremely fast switching
- Positive temperature coefficient on V_F







TO-252-2



Package Types: TO-252-2 Marking: C3D08065

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Applications

- Switch mode power supplies (SMPS)
- Power factor correction
- Motor drives

Benefits

- Replace bipolar with unipolar rectifiers
- Essentially no switching losses
- Higher efficiency
- Reduction of heat sink requirements
- Parallel devices without thermal runaway

Maximum Ratings (T_c = 25 °C Unless Otherwise Specified)

Parameter	Symbol	Value	Unit	Test Conditions	Note	
Repetitive Peak Reverse Voltage	V _{RRM}	650				
Surge Peak Reverse Voltage	V _{RSM}	650	V			
DC Blocking Voltage	V _{DC}	650				
Continuous Forward Current	I _F	25.5	A	T _c = 25 °C		
		12		T _c = 135 °C	Fig. 3	
		8		T _c = 155 °C		
Repetitive Peak Forward Surge Current		34		$T_c = 25 ^{\circ}\text{C}$, $t_p = 10 \text{ms}$, Half Sine Wave		
	FRM	25		$T_c = 110 ^{\circ}\text{C}, t_p = 10 \text{ms}, Half Sine Wave}$		
Non-Repetitive Peak Forward Surge Current	I _{FSM}	71		$T_c = 25 ^{\circ}\text{C}$, $t_p = 10 \text{ms}$, Half Sine Wave	Fig. 0	
		60		$T_c = 110 ^{\circ}\text{C}, t_p = 10 \text{ms}, Half Sine Wave}$	Fig. 8	
Non-Repetitive Peak Forward Surge Current	I _{F, Max}	650		$T_c = 25 ^{\circ}\text{C}, t_p = 10 \mu\text{s}, \text{Pulse}$	Fig. 8	
		530		$T_{c} = 110 {}^{\circ}\text{C}, t_{p} = 10 \mu\text{s}, \text{Pulse}$		
Power Dissipation	P _{tot}	120	W	T _c = 25 °C	Fig. 4	
		52		T _C =110 °C		
Operating Junction and Storage Temperature	T _J , T _{stg}	-55 to +175	°C			

Electrical Characteristics

Parameter	Symbol	Тур.	Max.	Unit	Test Conditions	Note
Forward Voltage	V _F	1.5	1.8	V	I _F = 8 A, T _J = 25 °C	Fig. 1
		2.1	2.4		I _F = 8 A, T _J = 175 °C	
B	rse Current I _R 10 50 µA	10	50		V _R = 650 V, T _J = 25 °C	F:- 2
Reverse Current		μΑ	V _R = 650 V, T _J = 175 °C	Fig. 2		
Total Capacitive Charge	Q _c	20		nC	$V_R = 650 \text{ V, I}_F = 8 \text{ A}$ $di/dt = 500 \text{ A}/\mu\text{S}$ $T_J = 25 \text{ °C}$	Fig. 5
Total Capacitance	С	395		pF	V _R = 0 V, T _J = 25 °C, f = 1 MHz	Fig. 6
		37			$V_R = 200 \text{ V}, T_J = 25 \text{ °C}, f = 1 \text{ MHz}$	
		32			V _R = 400 V, T _J = 25 °C, f = 1 MHz	
Capacitance Stored Energy	E _c	3.0		μJ	V _R = 400 V	Fig. 7

Note: This is a majority carrier diode, so there is no reverse recovery charge.

Thermal Characteristics

Parameter	Symbol	Typ.	Unit	Note
Thermal Resistance from Junction to Case	R _{eJC}	1.25	°C/W	Fig. 9

Typical Performance

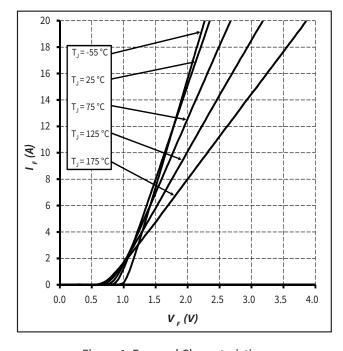


Figure 1. Forward Characteristics

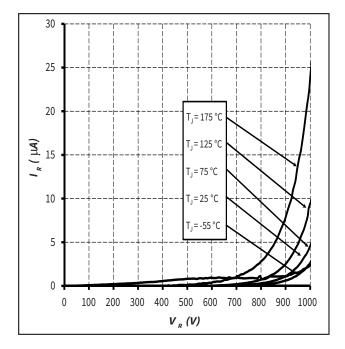
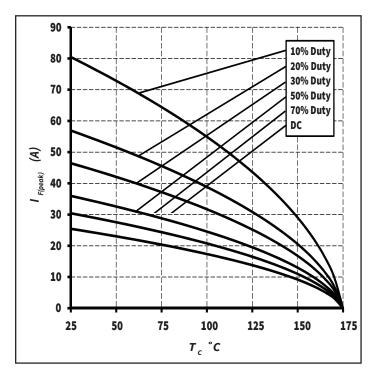


Figure 2. Reverse Characteristics

Typical Performance



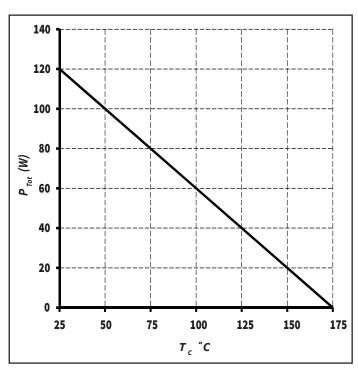
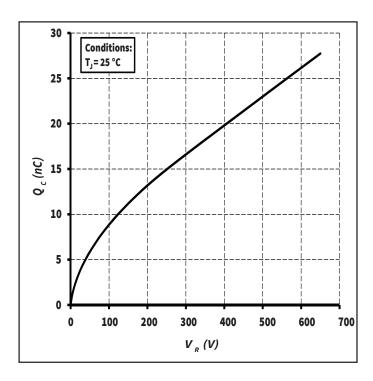


Figure 3. Current Derating

Figure 4. Power Derating





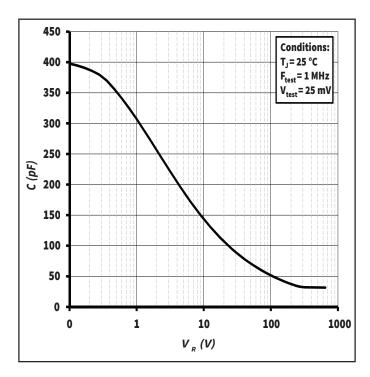
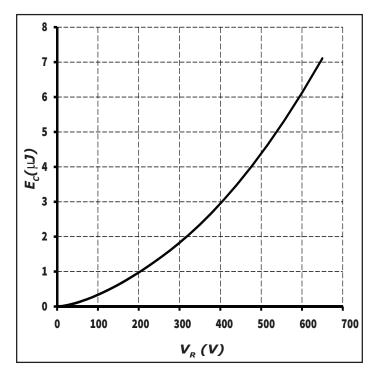


Figure 6. Capacitance vs. Reverse Voltage

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Typical Performance



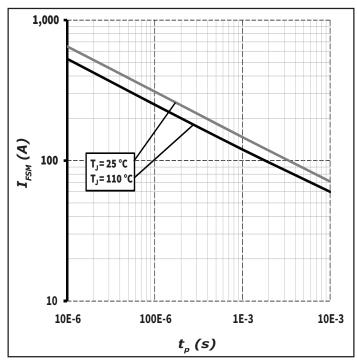


Figure 7. Capacitance Stored Energy

Figure 8. Non-Repetitive Peak Forward Surge Current Versus Pulse Duration (Sinusoidal Waveform)

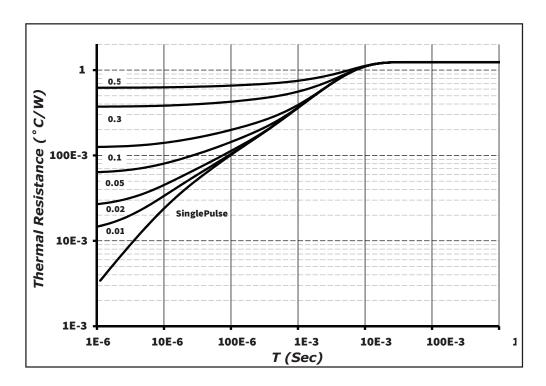
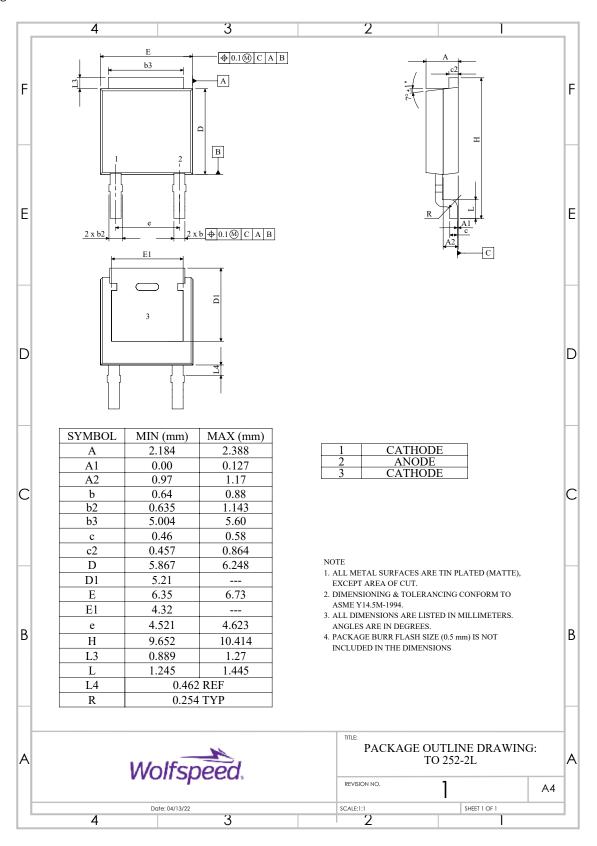


Figure 9. Transient Thermal Impedance

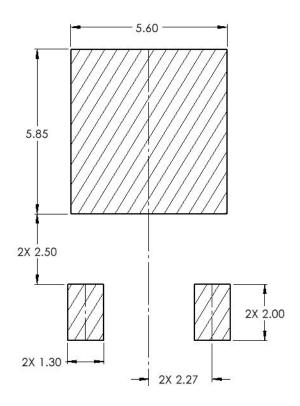


Package Dimensions

Package: TO-252-2



Recommended Solder Pad Layout



Part Number	Package	Marking
C3D08065E	TO-252-2	C3D08065

Diode Model

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$$Vf_T = V_T + If * R_T$$

$$V_T = 0.95 + (T_J * -1.2*10^{-3})$$

 $R_T = 0.054 + (T_J * 5.5*10^{-4})$

Note: T_j = Diode Junction Temperature In Degrees Celsius, valid from 25°C to 175°C

Revision History

Current Revision	Date of Release	Description of Changes
5	September-2023	Updated Wolfspeed branding, package drawing, and solder pad lay- out, Removed AEC-Q101 banner
6	October-2023	Corrected solder pad layout and diode model

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