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# MT29F2G08ABAGAH4-IT

[DATA SHEETS \(2\)](#) | 
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## Data Sheets (2)

**Errata**

**Errata: 2Gb Parallel NAND with ECC off**

MT29F2G08ABAGAWP-IT:G, MT29F2G08ABAGAH4-IT:G, MT29F2G08ABBGH4-IT:G

**File Type:** PDF **Updated:** 12/2016

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**Data Sheet**

**2Gb x8 1.8V and 3V M79A ECC off NAND Flash Memory Data Sheet**

Design Rev B; Data Sheet Rev B; MT29F2G08ABAGAWP-IT:G, MT29F2G08ABAGAH4-IT:G, MT29F2G08ABBGH4-IT:G

**File Type:** PDF **Updated:** 07/2016

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## Specs

Orderable Parts for: MT29F2G08ABAGAH4-IT

[SEE ALL SLC NAND PARTS](#)

	Status	Media	FBGA Code	SPD Data	Chipset Validation	PLP	Start Date	Alternative Part
MT29F2G08ABAGAH4-IT:G	Production	N/A	NW799	N/A	N/A	No		N/A

## Detailed Specifications

<b>Density</b>	2Gb	<b>Status</b>	Production	<b>RoHS</b>	Yes	<b>Width</b>	x8
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Voltage	3.3V	Package	VFBGA	Pin Count	63-ball	MT/s
I/O	Common	Op Temp	-40C to +85C			

## Sim Models & Software

### Sim Models

Verilog 

#### Verilog: 2Gbit, 1.8V and 3V, ECC off, M79A NAND Flash memory (ZIP)

Embedded Memory Solutions  
NAND Flash  
SLC NAND

M79A; ECC off; MT29F2G08ABAGAWP-IT:G, MT29F2G08ABAGAH4-IT:G, MT29F2G08ABBGAAH4-IT:G

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Updated: 12/15/2016

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HSpice

#### HSpice: NAND 2Gb SLC M79A (ZIP)

Embedded Memory Solutions  
NAND Flash  
SLC NAND

Rev 1.0, MT29F2G08ABAGAH4-IT:G, MT29F2G08ABAGAWP-IT:G, MT29F2G08ABBGAAH4-IT:G, MT29F2G08ABAGAH4-ITE:G, MT29F2G08ABAGAWP-ITE:G, MT29F2G08ABBGAAH4-ITE:G

File Type: ZIP

Updated: 01/18/2016

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IBIS

#### IBIS: NAND 2Gb SLC M79A (ZIP)

Embedded Memory Solutions  
NAND Flash  
SLC NAND

Rev 1.0, MT29F2G08ABAGAH4-IT:G, MT29F2G08ABAGAWP-IT:G, MT29F2G08ABBGAAH4-IT:G, MT29F2G08ABAGAH4-ITE:G, MT29F2G08ABAGAWP-ITE:G, MT29F2G08ABBGAAH4-ITE:G

File Type: ZIP

Updated: 01/18/2016

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### Software

Software Support Pack

#### YAFFS mark bad block issue (ZIP)

MLC NAND  
NAND Flash  
SLC NAND

If the bitflips num over mtd->bitflip\_threshold the mtd\_read\_oob will return -EUCLEAN and tags->ecc\_result > YAFFS\_ECC\_RESULT\_NO\_ERROR. Then we will call yaffs\_handle\_chunk\_error.

File Type: ZIP

Updated: 03/31/2016

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Software Support Pack

#### NAND FLASH lockunlock bug fix (ZIP)

MLC NAND  
NAND Flash  
SLC NAND

Reset NAND before write protect check.

File Type: ZIP

Updated: 03/31/2016

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Software Support Pack



**NANDcode Flash Translation Layer (ZIP)**

NAND Flash  
SLC NAND

NANDcode is a "C" FTL (Flash Translation Layer) software library for ARM platform.

File Type: ZIP

Updated: 08/08/2014

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Software Driver

**MT29F, SLC, 60/70 Series (1Gb, 2Gb, 4Gb, 8Gb, 16Gb, 32Gb) General Low-Level Driver for Parallel NAND Flash memory (ZIP)**

NAND Flash  
SLC NAND

M60A, M61A, M62B, M68A, M68M, M70M, M71M, M72A, M73A, M79M. Download the technical note for this driver [here](#).

File Type: ZIP

Updated: 04/05/2013

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Software Driver



**MT29F, SLC, 60 Series (1Gb, 2Gb, 4Gb) General Low-Level Driver for SPI NAND Flash memory (ZIP)**

NAND Flash  
Serial NAND  
SLC NAND

M68A, M69A, M60A

File Type: ZIP

Updated: 12/28/2011

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## RoHS Certificates

RoHS Certificates

**RoHS Certificate of Compliance (PDF)**

MT29F2G08ABAGAH4 IT

Part-specific certification of how this product meets the requirements of the current DIRECTIVE 2002/95/EC, a.k.a. Restriction of Hazardous Substances (RoHS) Directive.

File Type: (PDF)

Updated: 01/2017

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RoHS Certificates

**China RoHS Certificate (PDF)**

MT29F2G08ABAGAH4 IT

Part-specific certification as required by China's Management Methods for Controlling Pollution by Electronic Information Products.

File Type: (PDF)

Updated: 01/2017

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# Documentation & Support

[See All SLC NAND Documentation](#)

## Technical Notes

SEARCH (17) SLC NAND TECHNICAL NOTES

Technical Notes



### TN-13-49: SLC NAND Device Refresh (PDF)

NAND Flash  
SLC NAND

(TN-13-49) This technical note discusses the adverse effects of high temperature and PROGRAM/ERASE (P/E) cycles on flash memory data retention and how the REFRESH operation can be used to mitigate these adverse effects.

File Type: PDF

Updated: 10/31/2016

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Technical Notes



### TN-29-58: ONFI NV-DDR2 Design Guide (PDF)

NAND Flash  
SLC NAND

(TN-29-58) This technical note explains the device features that enable NV-DDR2 and provides guidelines for system designs to enable I/O transfer rates of up to 400 MT/s using the NV-DDR2 interface.

File Type: PDF

Updated: 09/29/2016

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Technical Notes

### TN-29-76: Using the Block Lock Feature on MT29F NAND Flash Memory Devices (PDF)

NAND Flash  
SLC NAND

(TN-29-76) This technical note describes the block lock feature on Micron's MT29F NAND Flash memory devices.

File Type: PDF

Updated: 08/16/2016

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Technical Notes

### TN-29-68: One-Time Programmable Operations in 2Gb: x8, x16 NAND Flash Memory (PDF)

NAND Flash  
SLC NAND

(TN-29-68) This technical note describes one-time programmable operations in Micron's 2Gb: x8, x16 NAND Flash memory devices.

File Type: PDF

Updated: 08/16/2016

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Technical Notes

(TN-26-03) This technical note explains how to read, program, and manage small page NAND memory.

File Type: PDF

Updated: 08/16/2016

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SEARCH (17) SLC NAND TECHNICAL NOTES

## Parts with the same Data Sheet (2)

MT29F2G08ABAGAH4-IT ( Current )		MT29F2G08ABAGAWP-IT	MT29F2G08ABBGAH4-IT
Status	Production	Production	Production
Density	2Gb	2Gb	2Gb
RoHS	Yes	Yes	Yes
Width	x8	x8	x8
Voltage	3.3V	3.3V	1.8V
Package	VFBGA	TSOP	VFBGA
Pin Count	63-ball	48-ball	63-ball
MT/s			
I/O	Common	Common	Common
Op Temp	-40C to +85C	-40C to +85C	-40C to +85C

## Where to Buy

### Orderable Parts

	Status	Media	FBGA Code	SPD Data	Chipset Validation	PLP	Start Date	Alternative Part
MT29F2G08ABAGAH4-IT:G	Production	N/A	NW799	N/A	N/A	No		N/A

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