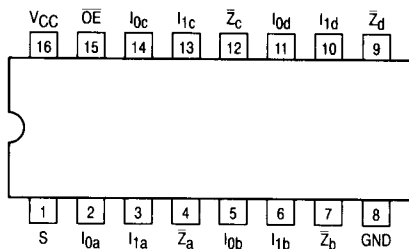




Quad 2-Input Multiplexer with 3-State Outputs

The MC74AC258/74ACT258 is a quad 2-input multiplexer with 3-state outputs. Four bits of data from two sources can be selected using a common data select input. The four outputs present the selected data in the complement (inverted) form. The outputs may be switched to a high impedance state with a HIGH on the common Output Enable (\overline{OE}) input, allowing the outputs to interface directly with bus-oriented systems.

- Multiplexer Expansion by Tying Outputs Together
- Inverting 3-State Outputs
- Outputs Source/Sink 24 mA
- 'ACT258 Has TTL Compatible Inputs



PIN NAMES

S Common Data Select Input
 \overline{OE} 3-State Output Enable Input
 $I_{0a}-I_{0d}$ Data Inputs from Source 0
 $I_{1a}-I_{1d}$ Data Inputs from Source 1
 Z_a-Z_d 3-State Multiplexer Outputs

TRUTH TABLE

| Output Enable | Select Input | Data Inputs | | Outputs |
|-----------------|--------------|-------------|-------|---------|
| \overline{OE} | S | I_0 | I_1 | Z |
| H | X | X | X | Z |
| L | H | X | L | H |
| L | H | X | H | L |
| L | L | L | X | H |
| L | L | L | L | L |

H = HIGH Voltage Level
 L = LOW Voltage Level
 X = Immaterial
 Z = High Impedance

MC74AC258
MC74ACT258

QUAD 2-INPUT
 MULTIPLEXER WITH
 3-STATE OUTPUTS

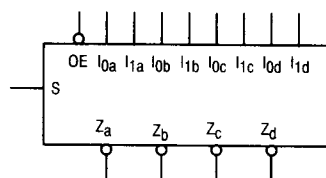


N SUFFIX
CASE 648-08
PLASTIC



D SUFFIX
CASE 751B-05
PLASTIC

LOGIC SYMBOL



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FUNCTIONAL DESCRIPTION

The MC74AC258/74ACT258 is a quad 2-input multiplexer with 3-state outputs. It selects four bits of data from two sources under control of a common Select input (S). When the Select input is LOW, the I_{0x} inputs are selected and when Select is HIGH, the I_{1x} inputs are selected. The data on the selected inputs appears at the outputs in inverted form. The MC74AC258/74ACT258 is the logic implementation of a 4-pole, 2-position switch where the position of the switch is determined by the logic levels supplied to the Select input. The logic equations for the outputs are shown below:

$$\bar{Z}_a = \overline{OE} \cdot (I_{1a} \cdot S + I_{0a} \cdot \bar{S})$$

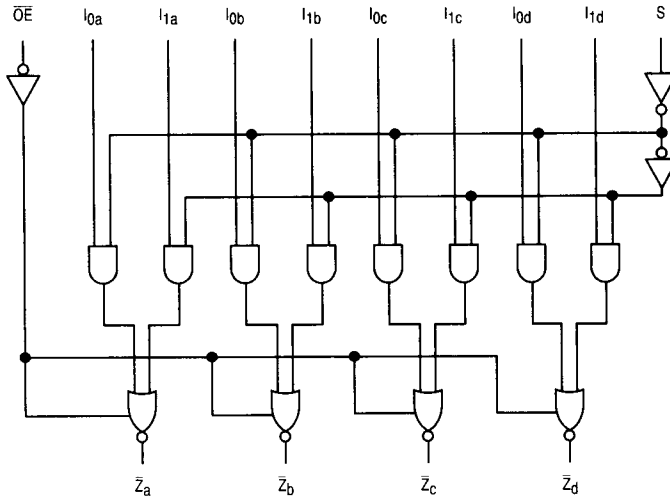
$$\bar{Z}_b = \overline{OE} \cdot (I_{1b} \cdot S + I_{0b} \cdot \bar{S})$$

$$\bar{Z}_c = \overline{OE} \cdot (I_{1c} \cdot S + I_{0c} \cdot \bar{S})$$

$$\bar{Z}_d = \overline{OE} \cdot (I_{1d} \cdot S + I_{0d} \cdot \bar{S})$$

When the Output Enable input (\overline{OE}) is HIGH, the outputs are forced to a high impedance state. If the outputs of the 3-state devices are tied together, all but one device must be in the high impedance state to avoid high currents that would exceed the maximum ratings. Designers should ensure the Output Enable signals to 3-state devices whose outputs are tied together are designed so there is no overlap.

LOGIC DIAGRAM



Please note that this diagram is provided only for the understanding of logic operations and should not be used to estimate propagation delays.

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MAXIMUM RATINGS*

| Symbol | Parameter | Value | Unit |
|------------------|--------------------------------------------------|------------------------------|------|
| V _{CC} | DC Supply Voltage (Referenced to GND) | -0.5 to +7.0 | V |
| V _{in} | DC Input Voltage (Referenced to GND) | -0.5 to V _{CC} +0.5 | V |
| V _{out} | DC Output Voltage (Referenced to GND) | -0.5 to V _{CC} +0.5 | V |
| I _{in} | DC Input Current, per Pin | ±20 | mA |
| I _{out} | DC Output Sink/Source Current, per Pin | ±50 | mA |
| I _{CC} | DC V _{CC} or GND Current per Output Pin | ±50 | mA |
| T _{stg} | Storage Temperature | -65 to +150 | °C |

* Maximum Ratings are those values beyond which damage to the device may occur. Functional operation should be restricted to the Recommended Operating Conditions.

RECOMMENDED OPERATING CONDITIONS

| Symbol | Parameter | Min | Typ | Max | Unit | |
|------------------------------------|-------------------------------------------------------------------------|-------------------------|-----|-----------------|------|------|
| V _{CC} | Supply Voltage | 'AC | 2.0 | 5.0 | 6.0 | V |
| | | 'ACT | 4.5 | 5.0 | 5.5 | |
| V _{in} , V _{out} | DC Input Voltage, Output Voltage (Ref. to GND) | 0 | | V _{CC} | V | |
| t _r , t _f | Input Rise and Fall Time (Note 1) 'AC Devices except Schmitt Inputs | V _{CC} @ 3.0 V | | 150 | | ns/V |
| | | V _{CC} @ 4.5 V | | 40 | | |
| | | V _{CC} @ 5.5 V | | 25 | | |
| t _r , t _f | Input Rise and Fall Time (Note 2) 'ACT Devices except Schmitt Inputs | V _{CC} @ 4.5 V | | 10 | | ns/V |
| | | V _{CC} @ 5.5 V | | 8.0 | | |
| T _J | Junction Temperature (PDIP) | | | 140 | °C | |
| T _A | Operating Ambient Temperature Range | -40 | 25 | 85 | °C | |
| I _{OH} | Output Current — High | | | -24 | mA | |
| I _{OL} | Output Current — Low | | | 24 | mA | |

1. V_{in} from 30% to 70% V_{CC}; see individual Data Sheets for devices that differ from the typical input rise and fall times.
2. V_{in} from 0.8 V to 2.0 V; see individual Data Sheets for devices that differ from the typical input rise and fall times.

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DC CHARACTERISTICS

| Symbol | Parameter | VCC (V) | 74AC | | 74AC | | Unit | Conditions |
|--------|-----------------------------------|---------|------------|-------------------|---------------------|--|------|------------------------------------------------------|
| | | | TA = +25°C | | TA = -40°C to +85°C | | | |
| | | | Typ | Guaranteed Limits | | | | |
| VIH | Minimum High Level Input Voltage | 3.0 | 1.5 | 2.1 | 2.1 | | V | VOUT = 0.1 V or VCC - 0.1 V |
| | | 4.5 | 2.25 | 3.15 | 3.15 | | | |
| | | 5.5 | 2.75 | 3.85 | 3.85 | | | |
| VIL | Maximum Low Level Input Voltage | 3.0 | 1.5 | 0.9 | 0.9 | | V | VOUT = 0.1 V or VCC - 0.1 V |
| | | 4.5 | 2.25 | 1.35 | 1.35 | | | |
| | | 5.5 | 2.75 | 1.65 | 1.65 | | | |
| VOH | Minimum High Level Output Voltage | 3.0 | 2.99 | 2.9 | 2.9 | | V | IOUT = -50 μA |
| | | 4.5 | 4.49 | 4.4 | 4.4 | | | |
| | | 5.5 | 5.49 | 5.4 | 5.4 | | | |
| | | 3.0 | | 2.56 | 2.46 | | V | *VIN = VIL or VIH -12 mA IOH -24 mA -24 mA |
| | | 4.5 | | 3.86 | 3.76 | | | |
| | | 5.5 | | 4.86 | 4.76 | | | |
| VOL | Maximum Low Level Output Voltage | 3.0 | 0.002 | 0.1 | 0.1 | | V | IOUT = 50 μA |
| | | 4.5 | 0.001 | 0.1 | 0.1 | | | |
| | | 5.5 | 0.001 | 0.1 | 0.1 | | | |
| | | 3.0 | | 0.36 | 0.44 | | V | *VIN = VIL or VIH 12 mA IOL 24 mA 24 mA |
| | | 4.5 | | 0.36 | 0.44 | | | |
| | | 5.5 | | 0.36 | 0.44 | | | |
| IIN | Maximum Input Leakage Current | 5.5 | | ±0.1 | ±1.0 | | μA | VI = VCC, GND |
| IOZ | Maximum 3-State Current | 5.5 | | ±0.5 | ±5.0 | | μA | VI (OE) = VIL, VIH VI = VCC, GND VO = VCC, GND |
| IOLD | †Minimum Dynamic Output Current | 5.5 | | | 75 | | mA | VOLD = 1.65 V Max |
| IOHD | | 5.5 | | | -75 | | mA | VOHD = 3.85 V Min |
| ICC | Maximum Quiescent Supply Current | 5.5 | | 8.0 | 80 | | μA | VIN = VCC or GND |

* All outputs loaded; thresholds on input associated with output under test.

† Maximum test duration 2.0 ms, one output loaded at a time.

Note: IIN and ICC @ 3.0 V are guaranteed to be less than or equal to the respective limit @ 5.5 V VCC.

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AC CHARACTERISTICS (For Figures and Waveforms — See Section 3)

| Symbol | Parameter | V _{CC} * (V) | 74AC | | | 74AC | | Unit | Fig. No. |
|------------------|----------------------------------------------------|--------------------------|--------------------------------------------------|------------|-------------|--------------------------------------------------------------|--------------|------|----------|
| | | | T _A = +25°C C _L = 50 pF | | | T _A = -40°C to +85°C C _L = 50 pF | | | |
| | | | Min | Typ | Max | Min | Max | | |
| t _{PLH} | Propagation Delay I _n to \bar{Z}_n | 3.3 5.0 | 2.0 1.5 | 6.0 4.5 | 9.5 7.5 | 1.5 1.0 | 11.0 8.5 | ns | 3-5 |
| t _{PHL} | Propagation Delay I _n to \bar{Z}_n | 3.3 5.0 | 2.0 1.5 | 5.0 4.0 | 8.5 6.5 | 1.5 1.0 | 9.5 7.0 | ns | 3-5 |
| t _{PLH} | Propagation Delay S to \bar{Z}_n | 3.3 5.0 | 3.0 2.0 | 7.5 6.0 | 12.0 9.5 | 2.5 1.5 | 14.0 10.5 | ns | 3-6 |
| t _{PHL} | Propagation Delay S to \bar{Z}_n | 3.3 5.0 | 2.5 1.5 | 7.5 5.5 | 11.5 9.0 | 2.0 1.5 | 13.0 10.0 | ns | 3-6 |
| t _{PZH} | Output Enable Time | 3.3 5.0 | 2.5 1.5 | 6.0 4.5 | 9.5 7.5 | 2.0 1.5 | 10.5 8.5 | ns | 3-7 |
| t _{PZL} | Output Enable Time | 3.3 5.0 | 2.0 1.5 | 5.5 5.5 | 9.0 7.0 | 1.5 1.0 | 10.0 8.0 | ns | 3-8 |
| t _{PHZ} | Output Disable Time | 3.3 5.0 | 2.5 2.0 | 5.5 5.5 | 10.0 8.5 | 2.0 1.5 | 11.0 9.0 | ns | 3-7 |
| t _{PLZ} | Output Disable Time | 3.3 5.0 | 2.0 1.5 | 5.5 5.0 | 9.0 7.0 | 2.0 1.5 | 10.0 8.0 | ns | 3-8 |

* Voltage Range 3.3 V is 3.3 V ±0.3 V.
Voltage Range 5.0 V is 5.0 V ±0.5 V.

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DC CHARACTERISTICS

| Symbol | Parameter | VCC (V) | 74ACT | | 74ACT | | Unit | Conditions |
|--------|-----------------------------------|---------|------------|-------------------|---------------------|----|------------------------------------------------------|------------|
| | | | TA = +25°C | | TA = -40°C to +85°C | | | |
| | | | Typ | Guaranteed Limits | | | | |
| VIH | Minimum High Level Input Voltage | 4.5 | 1.5 | 2.0 | 2.0 | V | VOUT = 0.1 V or VCC - 0.1 V | |
| | | 5.5 | 1.5 | 2.0 | 2.0 | | | |
| VIL | Maximum Low Level Input Voltage | 4.5 | 1.5 | 0.8 | 0.8 | V | VOUT = 0.1 V or VCC - 0.1 V | |
| | | 5.5 | 1.5 | 0.8 | 0.8 | | | |
| VOH | Minimum High Level Output Voltage | 4.5 | 4.49 | 4.4 | 4.4 | V | IOUT = -50 μA | |
| | | 5.5 | 5.49 | 5.4 | 5.4 | | | |
| | | 4.5 | | 3.86 | 3.76 | V | *VIN = VIL or VIH -24 mA IOH = -24 mA | |
| | | 5.5 | | 4.86 | 4.76 | | | |
| VOL | Maximum Low Level Output Voltage | 4.5 | 0.001 | 0.1 | 0.1 | V | IOUT = 50 μA | |
| | | 5.5 | 0.001 | 0.1 | 0.1 | | | |
| | | 4.5 | | 0.36 | 0.44 | V | *VIN = VIL or VIH 24 mA IOL = 24 mA | |
| | | 5.5 | | 0.36 | 0.44 | | | |
| IIN | Maximum Input Leakage Current | 5.5 | | ±0.1 | ±1.0 | μA | VI = VCC, GND | |
| ΔICCT | Additional Max. ICC/Input | 5.5 | 0.6 | | 1.5 | mA | VI = VCC - 2.1 V | |
| IOZ | Maximum 3-State Current | 5.5 | | ±0.5 | ±5.0 | μA | VI (OE) = VIL, VIH VI = VCC, GND VO = VCC, GND | |
| IOLD | †Minimum Dynamic Output Current | 5.5 | | | 75 | mA | VOLD = 1.65 V Max | |
| IOHD | | 5.5 | | | -75 | mA | VOHD = 3.85 V Min | |
| ICC | Maximum Quiescent Supply Current | 5.5 | | 8.0 | 80 | μA | VIN = VCC or GND | |

* All outputs loaded; thresholds on input associated with output under test.

† Maximum test duration 2.0 ms, one output loaded at a time.

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AC CHARACTERISTICS (For Figures and Waveforms — See Section 3)

| Symbol | Parameter | V _{CC} * (V) | 74ACT | | | 74ACT | | Unit | Fig. No. |
|------------------|----------------------------------------------------|--------------------------|--------------------------------------------------|-----|------|--------------------------------------------------------------|------|------|----------|
| | | | T _A = +25°C C _L = 50 pF | | | T _A = -40°C to +85°C C _L = 50 pF | | | |
| | | | Min | Typ | Max | Min | Max | | |
| t _{PLH} | Propagation Delay I _n to \bar{Z}_n | 5.0 | 2.0 | 6.5 | 8.5 | 1.5 | 9.5 | ns | 3-5 |
| t _{PHL} | Propagation Delay I _n to \bar{Z}_n | 5.0 | 2.0 | 5.5 | 7.5 | 1.5 | 8.0 | ns | 3-5 |
| t _{PLH} | Propagation Delay S to \bar{Z}_n | 5.0 | 3.0 | 7.5 | 10.5 | 2.0 | 11.5 | ns | 3-6 |
| t _{PHL} | Propagation Delay S to \bar{Z}_n | 5.0 | 1.5 | 7.0 | 9.5 | 1.5 | 11.0 | ns | 3-6 |
| t _{PZH} | Output Enable Time | 5.0 | 2.0 | 6.5 | 8.5 | 1.5 | 9.5 | ns | 3-7 |
| t _{PZL} | Output Enable Time | 5.0 | 2.0 | 6.5 | 8.5 | 1.5 | 9.5 | ns | 3-8 |
| t _{PHZ} | Output Disable Time | 5.0 | 1.5 | 7.0 | 9.0 | 1.0 | 10.0 | ns | 3-7 |
| t _{PLZ} | Output Disable Time | 5.0 | 2.0 | 6.0 | 8.0 | 1.5 | 9.0 | ns | 3-8 |

* Voltage Range 5.0 V is 5.0 V ±0.5 V.

CAPACITANCE

| Symbol | Parameter | Value Typ | Unit | Test Conditions |
|-----------------|-------------------------------|--------------|------|-------------------------|
| C _{IN} | Input Capacitance | 4.5 | pF | V _{CC} = 5.0 V |
| C _{PD} | Power Dissipation Capacitance | 55 | pF | V _{CC} = 5.0 V |