

### **General Description**

TLV760 series are a set of Low Dropout Linear Regulator ICs implemented in CMOS technology. Theycan withstand voltage 30V. And they are available with lowvoltage drop and low quiescent current, widely used in audio, video and communication appliances.

#### **Features**

- Low Power Consumption
- Low Voltage Drop
- Low Temperature Coefficient
- Withstanding Voltage 30V
- Quiescent Current 5.0μA
- Output Voltage Accuracy: tolerance ±2%
- High output current: 120mA

## **Application**

- Battery-powered Equipments
- Communication Equipments
- Audio/Video Equipments

## Pin Configuration And Descriptions



No.	Name	Functions Description
1	Vоит	Output
2	GND	Ground
3	Vin	Input

#### **Order Information**

Orderable Device	Package	Output Voltage	Packing Option
TLV76033DBZR	SOT-23	3.3V	3000/Reel
TLV76050DBZR	SOT-23	5.0V	3000/Reel



## **Absolute Maximum Ratings**

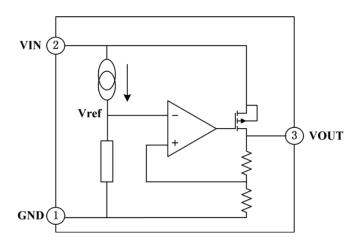
Description	Symbol	Value Range	Unit
Limit Power Voltage	Vin	<b>-</b> 0.3∼ <b>+</b> 35	V
Storage Temperature Range	Тѕтс	-50∼+125	°C
Operating Free-air Temperature Range	TA	-40∼ <b>+</b> 85	°C

Note:Stresses greater than those listed under "Absolute Maximum Ratingsmay" cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditionsis" not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.

# **Heat Dissipation**

Description	Symbol	Package	Value Range	Unit
Thermal resistance	θја	SOT-23	500	°C/W
Power dissipation	Pw	SOT-23	200	mW

# **Block Diagram**





## DC Characteristics (unless otherwise noted TA= 25°C)

(VIN= Vout+2.0V, CIN=CL=10uF,  $Ta=25^{\circ}C$ , unless otherwise noted)

#### Series +3.3V OUTPUT

Parameter	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Output Voltage	Vоит	Vin=Vout+2.0V, lout=10mA	3.234	3.30	3.366	V
Output Current	<b>І</b> оит	VIN=VOUT+2.0V		120		mA
Load Regulation	∆Vоит	Vin=Vout+2.0V 1mA≤lout≤50mA		25	60	mV
Voltage Drop	Vdif	lоuт=1mA,△Vоuт=2%		25	55	mV
Quiescent Current	Iss	No Load		5.0	8.0	uA
Line Regulation	riangle Vout/ $V$ out* $ riangle V$ in	Vout+1.0V≪Vin≪30V, Iout=1mA			0.2	%/V
Input Voltage	Vin				30	V
Temperature Coefficient	△Vout/ △Ta*Vout	VIN=VOUT+2.0V, IOUT=10mA, -40°C≪TA≪85°C		100		ppm/°C

Note: When VIN=VOUT+2.0V, as the output voltage declined 2%, the VDIF=VIN-VOUT.

#### Series +5.0V OUTPUT

Parameter	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Output Voltage	Vоит	VIN=VOUT+2.0V, IOUT=10mA	4.9	5.0	5.1	V
Output Current	<b>І</b> оит	VIN=VOUT+2.0V		120		mA
Load Regulation	∆Vоит	Vin=Vout+2.0V 1mA≤lout≤50mA		25	60	mV
Voltage Drop	Vdif	lоuт=1mA,△Vоuт=2%		25	55	mV
Quiescent Current	Iss	No Load		5.0	8.0	uA
Line Regulation	riangle Vout/ $V$ out* $ riangle V$ in	Vout+1.0V≪Vin≪30V, Iout=1mA			0.2	%/V
Input Voltage	Vin				30	V
Temperature Coefficient	△Vouт/ △Та*Vouт	VIN=VOUT+2.0V, IOUT=10mA, -40°C ≪TA≪85°C		100		ppm/°C

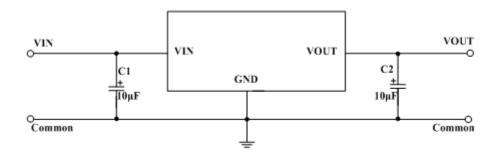
Note: When  $V_{\text{IN}}=V_{\text{OUT}}+2.0V$ , as the output voltage declined 2%, the  $V_{\text{DIF}}=V_{\text{IN}}-V_{\text{OUT}}$ .

## **Function Description**

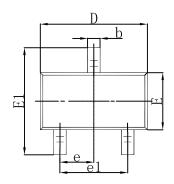
TLV760 series are linear voltage regulator ICs withstanding 30V voltage. The series IC consists of a voltage reference, an error amplifier, a current limiter and a phase compensation circuit plus a driver transistor. The output stabilization capacitor is also compatible with low ESR ceramic capacitors. The over current protection circuit and the over voltage protection circuit are built-in. The protection circuit will operate wheb the output current or input voltage reaches limit level.

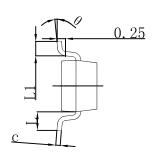
# **Application Circuit**

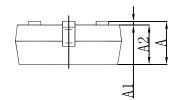
#### **Basic Circuits**



# **Package Dimensions SOT-23**

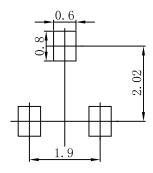






Cumbal	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min	Max	Min	Max	
Α	0.900	1.150	0.035	0.045	
A1	0.000	0.100	0.000	0.004	
A2	0.900	1.050	0.035	0.041	
b	0.300	0.500	0.012	0.020	
С	0.080	0.150	0.003	0.006	
D	2.800	3.000	0.110	0.118	
E	1.200	1.400	0.047	0.055	
E1	2.250	2.550	0.089	0.100	
е	0.950 TYP		0.037 TYP		
e1	1.800	2.000	0.071	0.079	
L	0.550 REF		0.022 REF		
L1	0.300	0.500	0.012	0.020	
θ	0°	8°	0°	8°	

## SuggestedPadLayout



- Note:
  1.Controlling dimension:in millimeters.
  2.General tolerance:± 0.05mm.
  3.The pad layout is for reference purposes only.



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