

# NTK3134NT1G-VB Datasheet N-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY							
V <sub>DS</sub> (V)	$R_{DS(on)}\left(\Omega\right)Max.$	I <sub>D</sub> (A)	Q <sub>g</sub> (Typ.)				
20	0.300 at $V_{GS} = 4.5 \text{ V}$	0.9	3.5				
	0.350 at V <sub>GS</sub> = 2.5 V	0.7	3.5				

### **FEATURES**

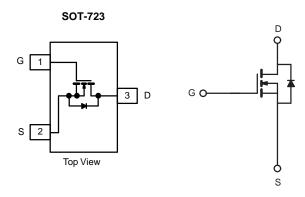
- Halogen-free According to IEC 61249-2-21 Definition
- Trench Power MOSFET
- 100 % R<sub>a</sub> Tested
- Compliant to RoHS Directive 2002/95/EC



ROHS COMPLIANT HALOGEN FREE

### **APPLICATIONS**

- · Load Switching for Portable Devices
- DC/DC Converter



<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C, unless otherwise noted)						
Parameter		Symbol	5 s	Steady State	Unit	
Drain-Source Voltage		V <sub>DS</sub>	20		V	
Gate-Source Voltage		V <sub>GS</sub>	± 12		V	
Continuous Dusin Comment /T 450 °C)	T <sub>A</sub> = 25 °C	I <sub>D</sub>	0.9	0.72	٨	
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 70 °C		0.68	0.57		
Pulsed Drain Current (t = 300 μs) <sup>b</sup>		I <sub>DM</sub>	3.5		Α	
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	0.72	0.6		
Develop Diseise Head	T <sub>A</sub> = 25 °C	P <sub>D</sub>	0.35	0.28	W	
Power Dissipation <sup>a</sup>	T <sub>A</sub> = 70 °C		0.21	0.16		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS						
Parameter	Symbol	Typical	Maximum	Unit		
Manipulation to Applicant	t ≤ 5 s	R <sub>thJA</sub>	120	145		
Maximum Junction-to-Ambient <sup>a</sup>	Steady State	' 'thJA	140	175	°C/W	
Maximum Junction-to-Foot	Steady State	R <sub>thJF</sub>	62	78		

#### Notes:

- a. Surface mounted on 1" x 1" FR4 board.
- b. Pulse width limited by maximum junction temperature.

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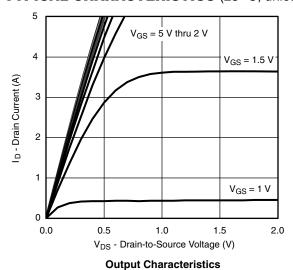
	IFICATIONS (T <sub>A</sub> = 25 °C, unless otherwise noted)			Limits			
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	20			٧	
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250 \mu A$	0.50		0.90	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			± 100	nA	
Zava Cata Valta aa Dusiin Couwant	ı	V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V		1	4	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 70 ^{\circ}\text{C}$			75	μΑ	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 10 \text{ V}, V_{GS} = 4.5 \text{ V}$	6			Α	
	D	$V_{GS} = 4.5 \text{ V}, I_D = 0.9 \text{A}$		0.300		0	
Drain-Source On-Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 2.5 \text{ V}, I_D = 0.7 \text{ A}$		0.350		Ω	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = 5 \text{ V}, I_D = 0.9 \text{ A}$		13		S	
Diode Forward Voltage	$V_{SD}$	I <sub>S</sub> = 0.95 A, V <sub>GS</sub> = 0 V		0.7	1.2	V	
Dynamic <sup>b</sup>							
Total Gate Charge	$Q_g$			1.5	1.9		
Gate-Source Charge	$Q_{gs}$	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 0.9 \text{A}$		0.25		nC	
Gate-Drain Charge	$Q_{gd}$			0.40			
Gate Resistance	$R_{g}$	f = 1 MHz	2	4	8	Ω	
Switching							
Turn-On Delay Time	t <sub>d(on)</sub>			8	15		
Rise Time	t <sub>r</sub>	$V_{DD}$ = 10 V, $R_L$ = 2.78 $\Omega$		7	15		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D\cong 0.9$ A, $V_{GEN}=4.5$ V, $R_g=1~\Omega$		30	45	ns	
Fall Time	t <sub>f</sub>			7	15		
Source-Drain Reverse Recovery Time		I <sub>E</sub> = 0.9 A, dl/dt = 100 A/μs		8.5	15		
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	$I_F = 0.9 \text{ A},        $		2	4	nC	

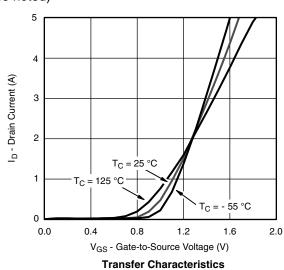
#### Notes:

- a. Pulse test: Pulse width  $\leq 300~\mu s,~duty~cycle \leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

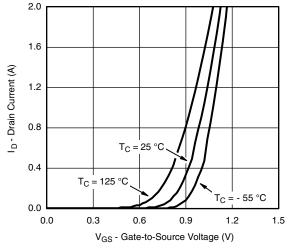
## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



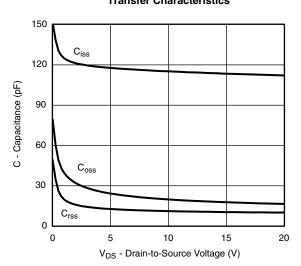




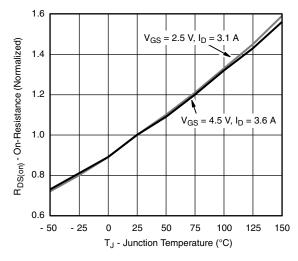
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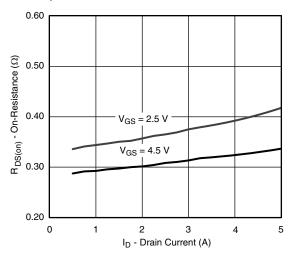
## Transfer Characteristics



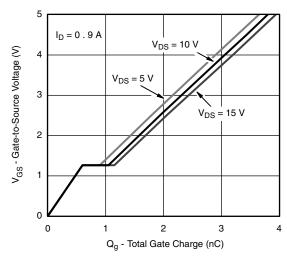
### Capacitance



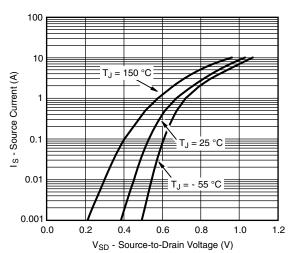
On-Resistance vs. Junction Temperature



### On-Resistance vs. Drain Current



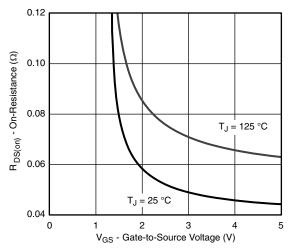
Gate Charge



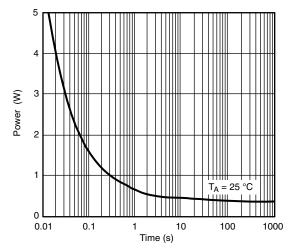
Source-Drain Diode Forward Voltage



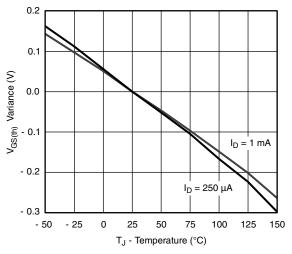
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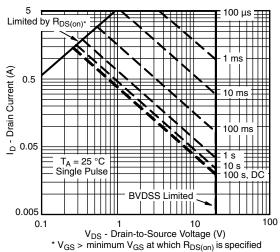
### On-Resistance vs. Gate-to-Source Voltage



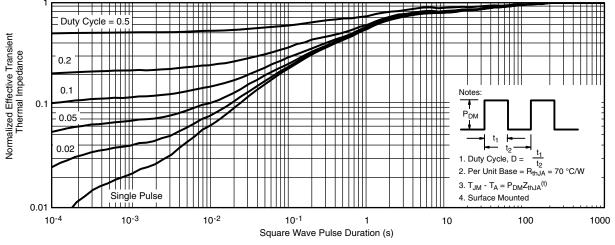
Single Pulse Power



#### **Threshold Voltage**



Safe Operating Area, Junction-to-Ambient

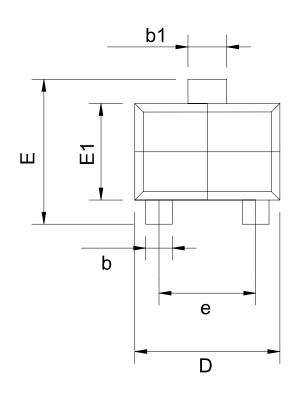


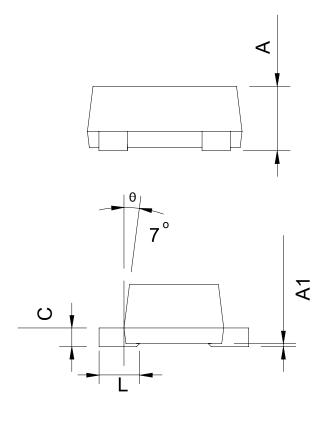
Normalized Thermal Transient Impedance, Junction-to-Ambient

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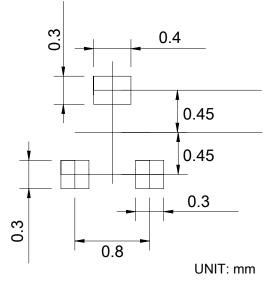
## **SOT-723: 3 Leads**





Ş	SOT-723					
SYMBOL	MILLIM	ETERS	INCHES			
<u>P</u>	MIN.	MAX.	MIN.	MAX.		
Α	-	0.500	-	0.020		
A1	0.000	0.050	0.000	0.002		
b	0.170	0.270	0.007	0.011		
b1	0.270	0.370	0.011	0.015		
С	-	0.150	-	0.006		
D	1.150	1.250	0.045	0.049		
Е	1.150	1.250	0.045	0.049		
E1	0.750	0.850	0.030	0.033		
е	0.800 TYP.		0.03	1 TYP.		
L	0.32 BSC		0.013	13 BSC		
-	°F	REF.	°F	REF.		

## **RECOMMENDED LAND PATTERN**



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