



## Description

### JMG N-channel Advanced Mode Power MOSFET

#### Features

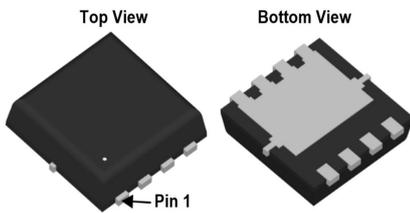
- 40V,140A  
 $R_{DS(ON)} < 2.4m\Omega @ V_{GS} = 10V$   
 $R_{DS(ON)} < 3.5m\Omega @ V_{GS} = 4.5V$
- Advanced Split Gate Trench Technology
- Excellent  $R_{DS(ON)}$  and Low Gate Charge
- Lead free product is acquired

#### Application

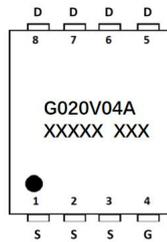
- Load Switch
- PWM Application
- Power management



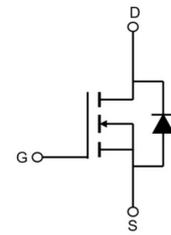
*100% UIS TESTED!*  
*100% ΔVds TESTED!*



PDFN5x6-8L



Marking and pin Assignment



Schematic Diagram

## Package Marking and Ordering Information

| Device Marking | Device      | OUTLINE | Device Package | Reel Size | Reel (PCS) | Per Carton (PCS) |
|----------------|-------------|---------|----------------|-----------|------------|------------------|
| G020V04A       | JMGG020V04A | TAPING  | PDFN5x6-8L     | 13inch    | 2500       | 25000            |

## Absolute Maximum Ratings (T<sub>C</sub>=25°C unless otherwise specified)

| Symbol                            | Parameter                                       | Max.                   | Units |
|-----------------------------------|---|------------------------|-------|
| V <sub>DSS</sub>                  | Drain-Source Voltage                            | 40                     | V     |
| V <sub>GSS</sub>                  | Gate-Source Voltage                             | ±20                    | V     |
| I <sub>D</sub>                    | Continuous Drain Current                        | T <sub>C</sub> = 25°C  | 140   |
|                                   |   | T <sub>C</sub> = 100°C | 91    |
| I <sub>DM</sub>                   | Pulsed Drain Current <sup>note1</sup>           | 560                    | A     |
| E <sub>AS</sub>                   | Single Pulsed Avalanche Energy <sup>note2</sup> | 196                    | mJ    |
| P <sub>D</sub>                    | Power Dissipation                               | 83                     | W     |
| R <sub>θJC</sub>                  | Thermal Resistance, Junction to Case            | 1.5                    | °C/W  |
| T <sub>J</sub> , T <sub>STG</sub> | Operating and Storage Temperature Range         | -55 to +150            | °C    |



## Electrical Characteristics (T<sub>J</sub>=25°C unless otherwise specified)

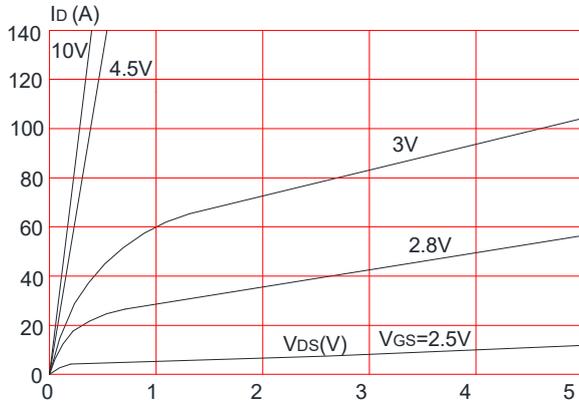
| Symbol  | Parameter   | Test Condition   | Min. | Typ. | Max. | Units |
|---|---|--|------|------|------|-------|
| <b>Off Characteristic</b>                                     |   |  |      |      |      |       |
| V <sub>(BR)DSS</sub>  | Drain-Source Breakdown Voltage                            | V <sub>GS</sub> =0V, I <sub>D</sub> =250μA   | 40   | -    | -    | V     |
| I <sub>DSS</sub>  | Zero Gate Voltage Drain Current                           | V <sub>DS</sub> =40V, V <sub>GS</sub> =0V,   | -    | -    | 1.0  | μA    |
| I <sub>GSS</sub>  | Gate to Body Leakage Current                              | V <sub>DS</sub> =0V, V <sub>GS</sub> = ±20V  | -    | -    | ±100 | nA    |
| <b>On Characteristics</b>                                     |   |  |      |      |      |       |
| V <sub>GS(th)</sub>   | Gate Threshold Voltage                                    | V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA                                 | 1.0  | -    | 2.5  | V     |
| R <sub>DS(on)</sub>   | Static Drain-Source on-Resistance<br><small>note3</small> | V <sub>GS</sub> =10V, I <sub>D</sub> =30A  | -    | 1.9  | 2.4  | mΩ    |
|   |   | V <sub>GS</sub> =4.5V, I <sub>D</sub> =20A   | -    | 2.7  | 3.5  |       |
| <b>Dynamic Characteristics</b>                                |   |  |      |      |      |       |
| C <sub>iss</sub>  | Input Capacitance   | V <sub>DS</sub> =20V, V <sub>GS</sub> =0V,<br>f=1.0MHz                                   | -    | 2625 | -    | pF    |
| C <sub>oss</sub>  | Output Capacitance  |  | -    | 1102 | -    | pF    |
| C <sub>rss</sub>  | Reverse Transfer Capacitance                              |  | -    | 57   | -    | pF    |
| Q <sub>g</sub>  | Total Gate Charge   | V <sub>DS</sub> =20V, I <sub>D</sub> =75A,<br>V <sub>GS</sub> =10V                       | -    | 42   | -    | nC    |
| Q <sub>gs</sub>   | Gate-Source Charge  |  | -    | 10   | -    | nC    |
| Q <sub>gd</sub>   | Gate-Drain("Miller") Charge                               |  | -    | 7    | -    | nC    |
| <b>Switching Characteristics</b>                              |   |  |      |      |      |       |
| t <sub>d(on)</sub>  | Turn-on Delay Time  | V <sub>DD</sub> =20V, I <sub>D</sub> =75A,<br>R <sub>G</sub> =1.6Ω, V <sub>GS</sub> =10V | -    | 9    | -    | ns    |
| t <sub>r</sub>  | Turn-on Rise Time   |  | -    | 103  | -    | ns    |
| t <sub>d(off)</sub>   | Turn-off Delay Time                                       |  | -    | 37   | -    | ns    |
| t <sub>f</sub>  | Turn-off Fall Time  |  | -    | 129  | -    | ns    |
| <b>Drain-Source Diode Characteristics and Maximum Ratings</b> |   |  |      |      |      |       |
| I <sub>S</sub>  | Maximum Continuous Drain to Source Diode Forward Current  |  | -    | -    | 140  | A     |
| I <sub>SM</sub>   | Maximum Pulsed Drain to Source Diode Forward Current      |  | -    | -    | 560  | A     |
| V <sub>SD</sub>   | Drain to Source Diode Forward Voltage                     | V <sub>GS</sub> =0V, I <sub>S</sub> =30A   | -    | -    | 1.2  | V     |
| t <sub>rr</sub>   | Body Diode Reverse Recovery Time                          | T <sub>J</sub> =25°C,<br>I <sub>F</sub> =20A, di/dt=100A/μs                              | -    | 38   | -    | ns    |
| Q <sub>rr</sub>   | Body Diode Reverse Recovery Charge                        |  | -    | 19   | -    | nC    |

- Notes: 1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature  
 2. EAS condition: T<sub>J</sub>=25°C, V<sub>DD</sub>=20V, V<sub>G</sub>=10V, R<sub>G</sub>=25Ω, L=0.5mH, I<sub>AS</sub>=28A  
 3. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%

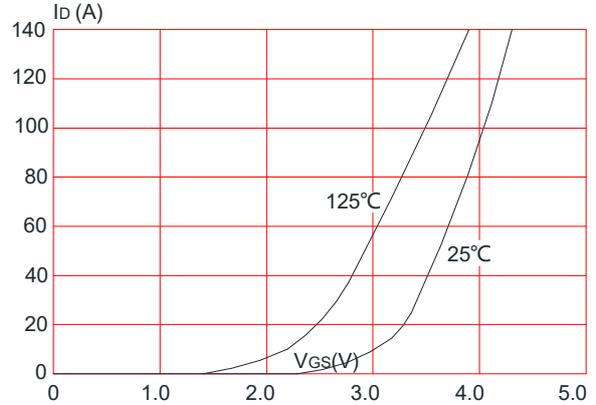


## Typical Performance Characteristics

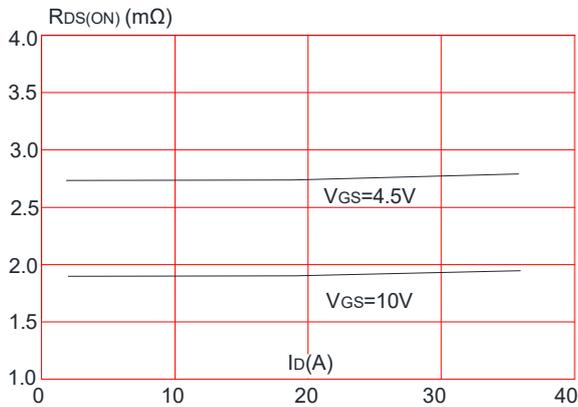
**Figure 1:** Output Characteristics



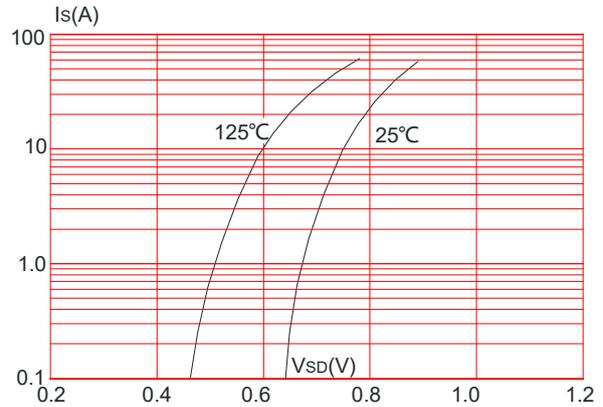
**Figure 2:** Typical Transfer Characteristics



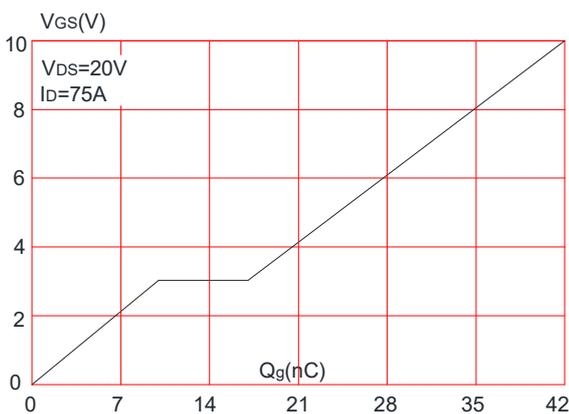
**Figure 3:** On-resistance vs. Drain Current



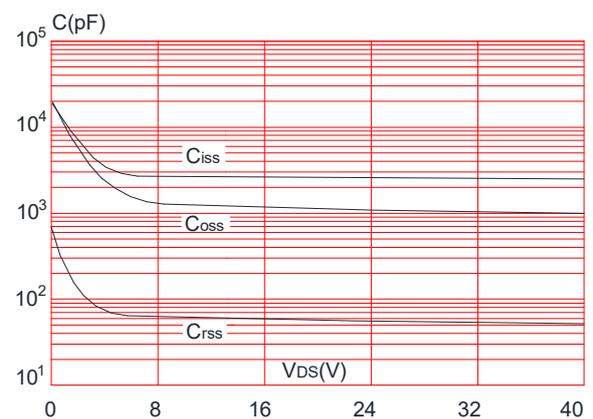
**Figure 4:** Body Diode Characteristics



**Figure 5:** Gate Charge Characteristics

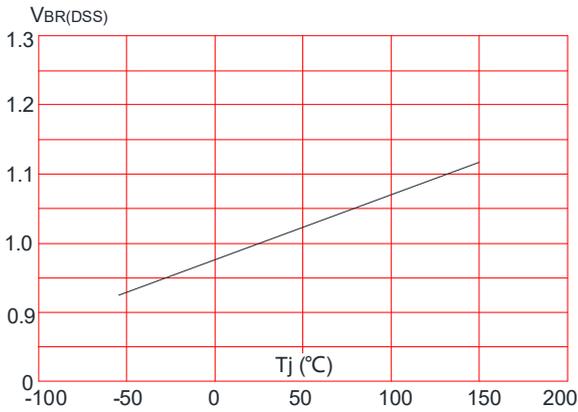


**Figure 6:** Capacitance Characteristics

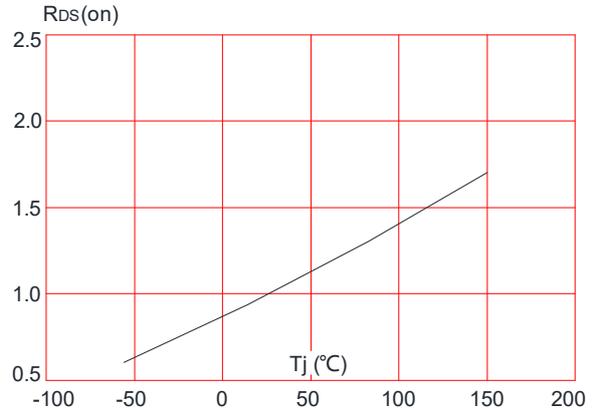




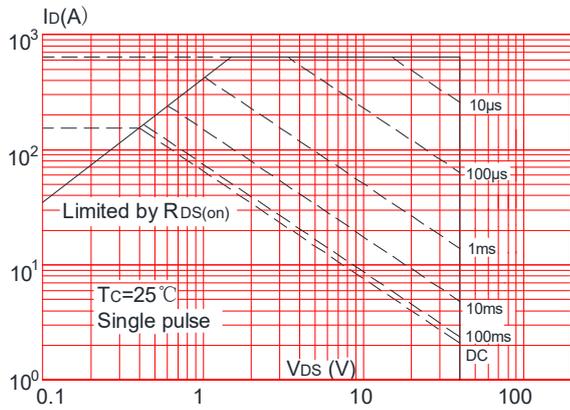
**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature



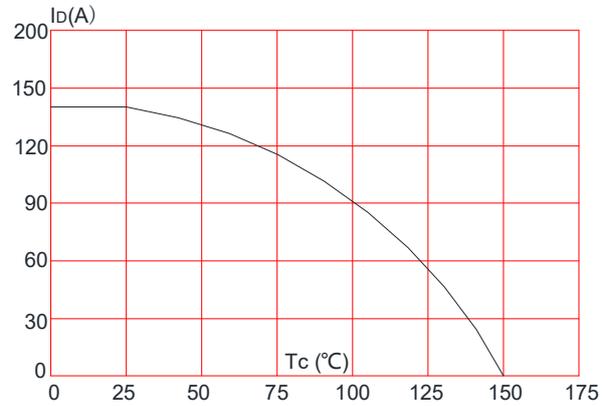
**Figure 8:** Normalized on Resistance vs. Junction Temperature



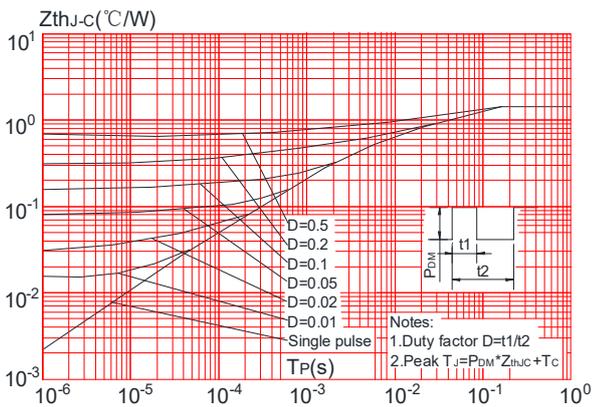
**Figure 9:** Maximum Safe Operating Area



**Figure 10:** Maximum Continuous Drain Current vs. Case Temperature



**Figure.11:** Maximum Effective Transient Thermal Impedance, Junction-to-Case



## Test Circuit

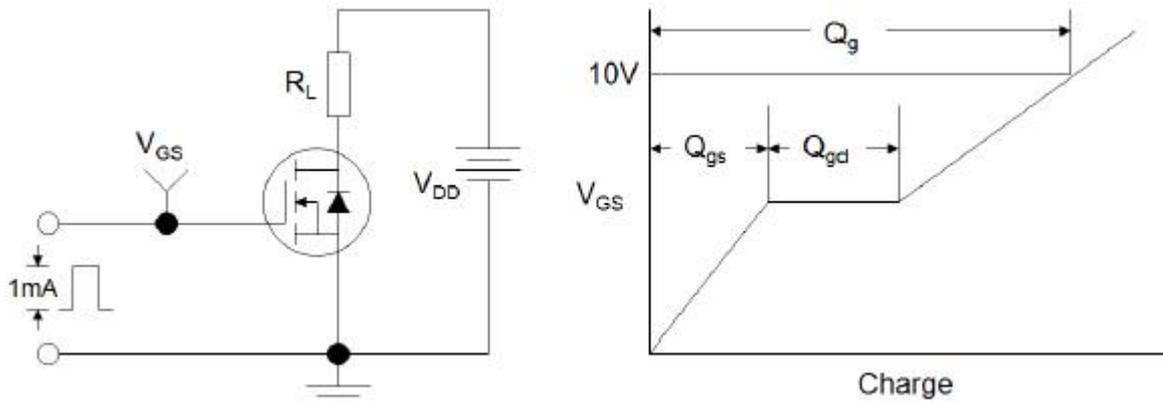


Figure1:Gate Charge Test Circuit & Waveform

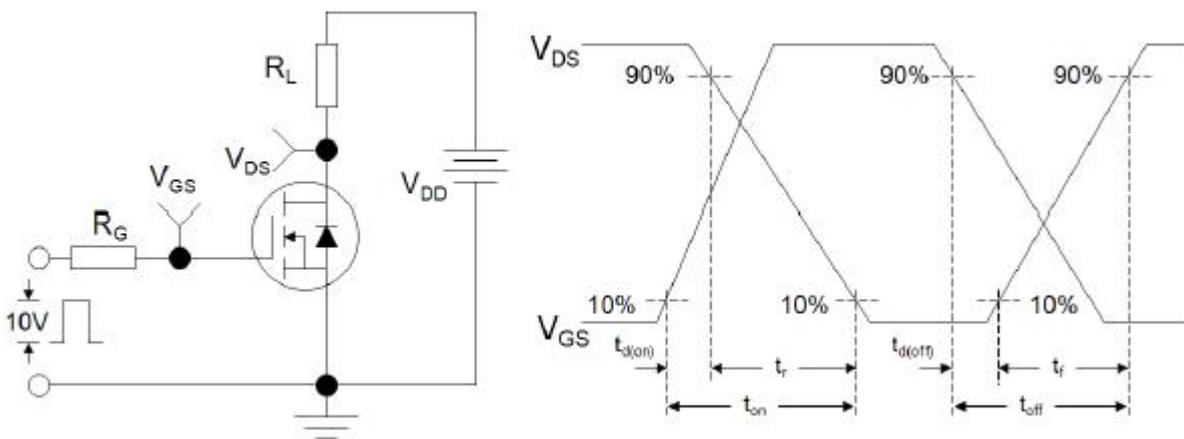


Figure 2: Resistive Switching Test Circuit & Waveforms

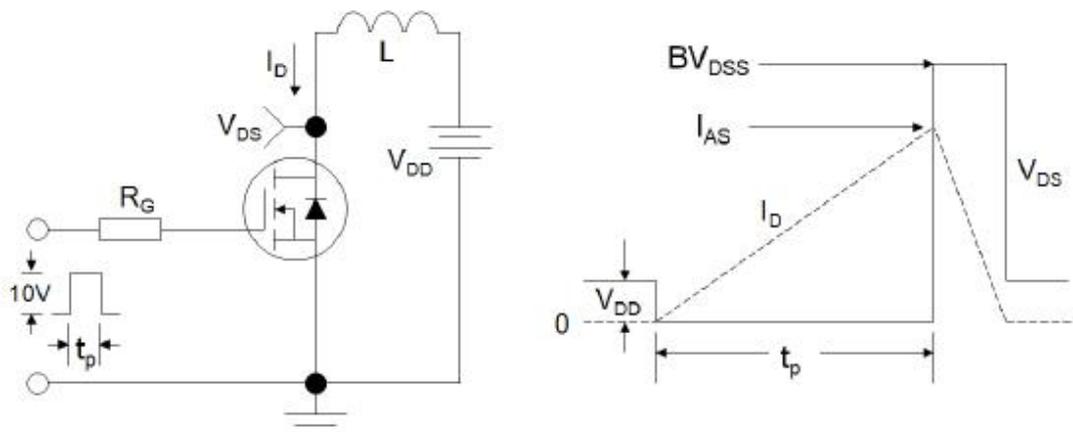
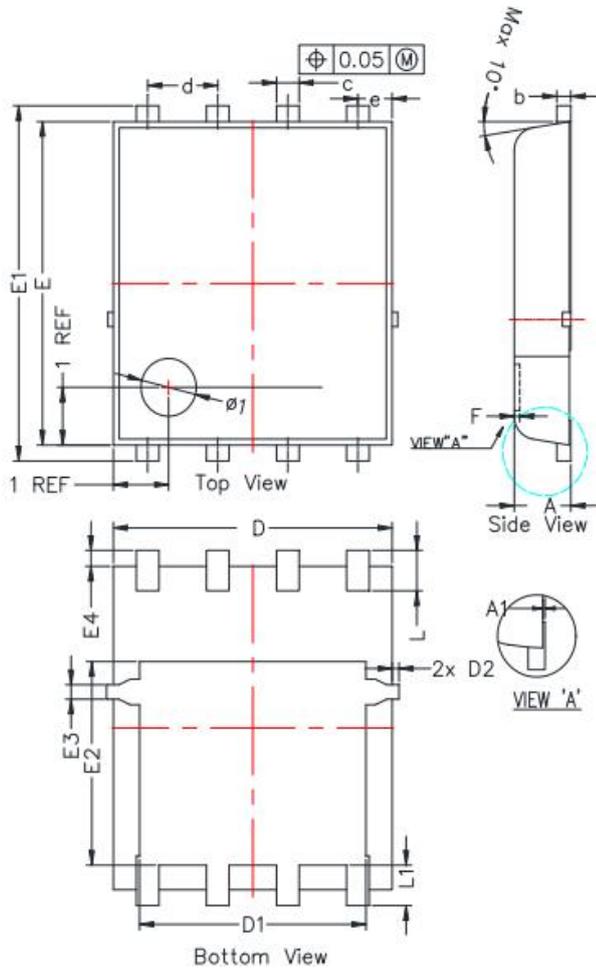


Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms



## Package Mechanical Data-PDFN5x6-8L



| SYMBOLS | DIMENSION IN MM |       |       | DIMENSION IN INCHES |       |       |
|---------|-----------------|-------|-------|---------------------|-------|-------|
|         | MIN             | NOM   | MAX   | MIN                 | NOM   | MAX   |
| * A     | 0.900           | 1.000 | 1.100 | 0.035               | 0.039 | 0.043 |
| A1      | 0.000           | ---   | 0.050 | 0.000               | ----  | 0.002 |
| b       | 0.246           | 0.254 | 0.312 | 0.010               | 0.010 | 0.012 |
| * c     | 0.310           | 0.410 | 0.510 | 0.012               | 0.016 | 0.020 |
| d       | 1.27 BSC        |       |       | 0.050 BSC           |       |       |
| * D     | 4.950           | 5.050 | 5.150 | 0.195               | 0.199 | 0.203 |
| D1      | 4.000           | 4.100 | 4.200 | 0.157               | 0.161 | 0.165 |
| * D2    | ---             | ---   | 0.125 | ---                 | ---   | 0.005 |
| e       | 0.62 BSC        |       |       | 0.024 BSC           |       |       |
| * E     | 5.500           | 5.600 | 5.700 | 0.217               | 0.220 | 0.224 |
| * E1    | 6.050           | 6.150 | 6.250 | 0.238               | 0.242 | 0.246 |
| E2      | 3.425           | 3.525 | 3.625 | 0.135               | 0.139 | 0.143 |
| E3      | 0.150           | 0.250 | 0.350 | 0.006               | 0.010 | 0.014 |
| * E4    | 0.175           | 0.275 | 0.375 | 0.007               | 0.011 | 0.015 |
| F       | -               | -     | 0.100 | -                   | -     | 0.004 |
| * L     | 0.500           | 0.600 | 0.700 | 0.02                | 0.02  | 0.03  |
| L1      | 0.600           | 0.700 | 0.800 | 0.02                | 0.03  | 0.03  |

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