

# 5V 5-Channel Micro STM&DC Motor Driver

#### **FEATURES**

- . Built-in 5-channel Motor Driver
- 1~4CH Control Two Stepper Motors or Four DC Motor, and the 5CH Channel Control One DC Motor or Coil
- 1CH/2CH & 3CH/4CH are Capable of Micro-steps Mode to Drive Maximum 512 Resolution Micro-steps
- . 2.4V to 5.5V Operating Voltage
- . Maximum Continuous Current: 0.6A
- . VM Under-voltage Lockout (UVLO)
- . Thermal Shutdown (TSD)
- . Low-power Sleep Mode
- . Automatic Fault Recovery
- A Small 20-pin QFN Package Type Has Been Adopted

#### **APPLICATIONS**

- . DC motor & STM motor
- PTZ of IPC
- . Robot

#### **GENERAL DESCRIPTION**

The TMI8152 is an integrated DC motor driver, applied to low input voltage working system. The TMI8152 uses serial communication to control 5 channels of motors, which supports PTZ motor and IR cut filter change motor. 1~4CH can drive 2 stepper motor of PTZ (X and Y axis), X-axis and Y-axis are controlled by orthogonal SPWM and support forward and reverse rotation. 5CH control IR-cut.

The current consumption can be hold at quite low level during standby or operation. In low-power sleep mode, the current is only 16µA.

The package form of TMI8152 is QFN3x3-20L, which conforms to ROHS specifications, and the frame is 100% lead-free.

# **TYPICAL APPILCATION**

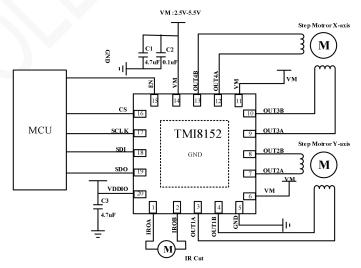


Figure 1. Application Circuit

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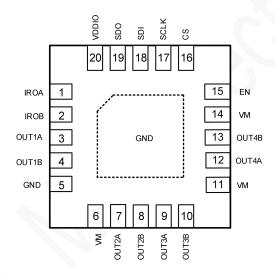
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# ABSOLUTE MAXIMUM RATINGS (Note 1)

Parameter	Value	Unit
Power Supply Voltage (VM)	-0.3~6	V
Logic power supply(VDDIO)	-0.3~6	V
EN, SCLK, SDI, CS Input Voltage	-0.3~VM	V
Maximum Operating Junction Temperature T <sub>J(Note 2)</sub>	150	°C
Storage Temperature T <sub>sdg</sub>	-55~150	°C
Operating Temperature T <sub>op</sub>	-40~85	°C
Maximum Chip Thermal Resistance θ <sub>JA</sub>	80	°C/W
Maximum Pin Solder Temperature (soldering 10s)	260	°C

# **PACKAGE/ORDER INFORMATION**



QFN3X3-20L TMI8152

Top Mark: TMI8152/XXXXX (TMI8152: Device Code, XXXXX: Inside Code)

Part Number	Package	Top mark	Quantity/ Reel
		TMI	
TMI8152	QFN3x3-20L	8152	5000
		XXXXX	

TMI8152 products meet lead-free requirements and ROHS standards





# **PIN DISCRIPTIONS**

Pin	Name	Function
1	IROA	IR Cut drive output pin A.
2	IROB	IR Cut drive output pin B.
3	OUT1A	PZT drive output pin 1A.
4	OUT1B	PZT drive output pin 1B.
5	GND	Logic ground. Connect to board ground.
6	VM	2.4V to 5.5V power supply.
7	OUT2A	PZT drive output pin 2A.
8	OUT2B	PZT drive output pin 2B.
9	OUT3A	PZT drive output pin 3A.
10	OUT3B	PZT drive output pin 3B.
11	VM	2.4V to 5.5V power supply.
12	OUT4A	PZT drive output pin 4A.
13	OUT4B	PZT drive output pin 4B.
14	VM	2.4V to 5.5V power supply.
15	EN	Enable pin. Pull-down $100k\Omega$ resister. When the input logic voltage is low, the chip function disabled. When the input logic voltage is high, the chip function enables.
16	CS	Chip select signal input pin. Pull-up 100kΩ resister. Default state is high.
17	SCLK	SPI clock input pin. Pull-down 100kΩ resister. Default state is low.
18	SDI	SPI data input pin. Pull-down $100k\Omega$ resister. Default state is low.
19	SDO	SPI data output pin.
20	VDDIO	1.8V to 5.5V Logic power supply. Connect a 4.7µF Chip capacitors to ground.
EPAD	GND	Logic ground. Connect to board ground.

# **ESD RATING**

Items	Description	Value	Unit
V <sub>ESD</sub>	Human body model for all pins	±2000	V

# JEDEC specification JS-001

# **RECOMMENDED OPERATING CONDITIONS**

Items	Description	Min	Max	Unit
VM	Power supply voltage range	2.5	5.5	V
VDDIO	Logic power supply range	1.8	5.5	V
V <sub>input</sub>	Logic signal voltage range	0	VM	V



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# **ELECTRICAL CHARACTERISTICS**

(If no special regulations VM=5V, Ta=25°C)

Parameter	Symbol	Test Condition	Min	Тур.	Max	Unit
POWER SUPPLY (VM)						
VM operating voltage	VM		2.5		5.5	V
stand-by current	I <sub>DDST</sub>	EN=0		0.1	1	μA
Low power mode current	I <sub>DDLP</sub>	G <sub>LPEN</sub> =1 (Low power register)		16	25	μA
Operating Current	I <sub>DD</sub>	No load			4.5	mA
LOGIC-LEVEL INPUTS						
Input logic low voltage	V <sub>INL</sub>	VDDIO=3.3V			0.9	V
Input logic high voltage	V <sub>INH</sub>	VDDIO=3.3V	1.2			V
Input logic low voltage	V <sub>INL</sub>	VDDIO=1.8V			0.6	V
Input logic high voltage	V <sub>INH</sub>	VDDIO=1.8V	0.9			V
EN/SDI/SCLK pull-down resister	R <sub>s1</sub>			100		kΩ
CS pull-up resister	R <sub>s2</sub>			100		kΩ
SDO output logic low voltage	Voutl	I <sub>load</sub> =5mA, VM=5V			0.3	V
SDO output logic high voltage	V <sub>оитн</sub>	I <sub>load</sub> =5mA, VM=5V	VDD			V
SCLK/SDI operating frequency	F <sub>sclk</sub>				5	MHz
PZT output speed	F <sub>pzt</sub>			18		kHz
MOTOR DRIVER OUTPUTS (OU	T1, OUT2)					
1~4CH on resistance	R <sub>ds(on)2</sub>	I <sub>LOAD</sub> =0.5A,HS_PMOS+LS_N MOS		1.3	1.6	Ω
5CH (IR Cut) on resistance	R <sub>ds(on)1</sub>	I <sub>LOAD</sub> =0.2A,HS_PMOS+LS_N MOS		2.4	3	Ω
Output dead time	t <sub>DEAD</sub>			150		ns
PROTECTION CIRCUITS				1	ı	
VM under voltage lockout	V <sub>UVLO</sub>	VM rises until operation recovers			2.4	V
VM under voltage hysteresis	V <sub>UV_HYS</sub>	Rising to falling threshold		150		mV
Thermal shutdown threshold (Note 3)	T <sub>SD</sub>			168		°C
Thermal shutdown hysteresis (Note 3)	T <sub>HYS</sub>			35		°C

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**Note 1:** Absolute Maximum Ratings are those values beyond which the life of a device may be impaired.

**Note 2:**  $T_J$  is calculated from the ambient temperature  $T_A$  and power dissipation  $P_D$  according to the following formula:  $T_J = T_A + P_D \times \theta_{JA}$ . The maximum allowable continuous power dissipation at any ambient temperature is calculated by  $P_{D (MAX)} = (T_{J(MAX)} - T_A)/\theta_{JA}$ .

Note 3: Thermal shutdown threshold and hysteresis are guaranteed by design.

#### **OPERATION**

#### Overview

The TMI8152 is a DC motor driver integrated chip that works under low input voltage with a total of 5 groups of drive output channels. Four groups of motor drive outputs control the X axis and Y axis of the PZT motor with independent quadrature SPWM. Each group of output channels is composed of 4 MOSFETs, and the internal resistance is  $1.3\Omega$ . One set of motor drive output ports control IR cut, and conduct internal resistance  $2.4\Omega$ . Using SPI control, it is more convenient and accurate to control the motor speed and move. The chip has an integrated low power consumption mode, which can be accessed through software or an enable signal, and the quiescent current is only  $16\mu\text{A}$  in the low power consumption mode. A variety of protection functions can prevent the equipment against failure occurs.

#### **PZT Control**

The TMI8152 can control the X-axis and Y-axis of the PZT motor When working in 'Start the control quadrature SPWM mode' or 'The number of steps controls the quadrature SPWM mode'. Start control quadrature SPWM mode can be called automatic control mode, It can continuously output SPWM control signal. Step control quadrature SPWM mode can be called manual control mode, It can work according to the set target number of turns and phase.

#### **IR-Cut Control**

The TMI8152 can control IR-Cut at the same time. It can support direction switching at any time. Three working states can be selected: Forward, Reverse and Brake.

#### **Function description**

The TMI8152 uses SPI communication mode to control the motor drive, which can set the target number of rotations, phase value, and PWM width of the motor, and record the motor rotation position in real time. Control the working mode of the motor:1. Drive port direct control mode; 2. 1-2 phase excitation mode; 3. 2 Phase excitation mode; 4. SPI pulse width control mode; 5. Start the control quadrature SPWM mode; 6. The number of steps controls the quadrature SPWM mode (default). It supports forward and reverse rotation, and different step values can be set to control the motor speed.



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# VM Under-voltage Lockout (UVLO)

If at any time the voltage on the VM pin falls below the under-voltage-lockout threshold voltage, all FETs in the H-bridge will be disabled. Operation resumes when VM rises above the UVLO threshold.

#### Thermal Shutdown (TSD)

If the die temperature exceeds safe limits, all FETs in the H-bridge are disabled. After the die temperature has fallen to a safe level, operation automatically resumes.

# **Block Diagram**

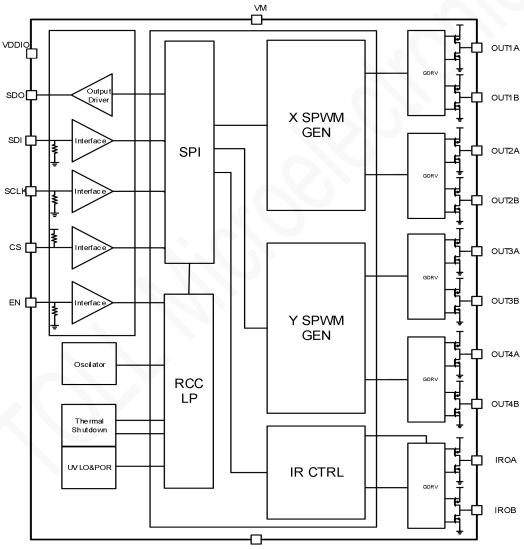


Figure 2. TMI8152 Block Diagram

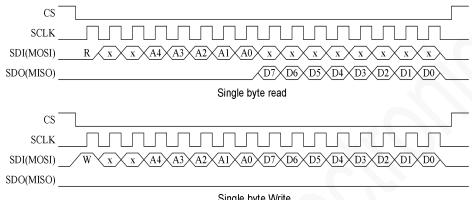
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# Logic control description

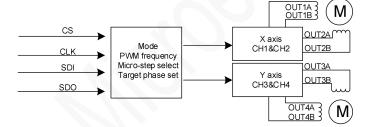
#### **SPI Logic Description**

The TMI8152 is controlled by SPI communication. By controlling the logic levels of CS, CLK, SDI, SDO, the output waveform of the chip can be controlled. CS is a logic control signal, active at low level. The maximum clock frequency is 5MHz. The logic control sequence of TMI8152 is as follows:



Single byte Write

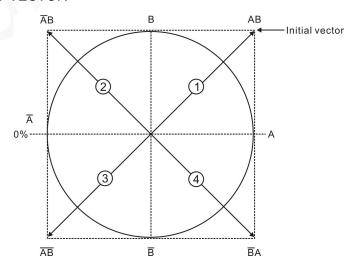
### Stepper motor control



#### 1~4CH Control mode

#### (1) 2 Phase

#### **OUTPUT CURRENT VECTOR**

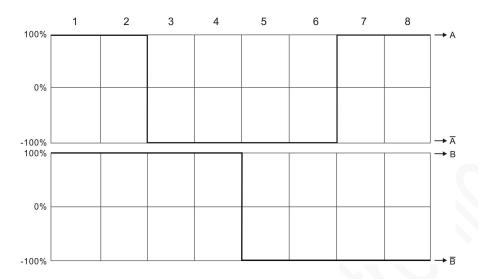


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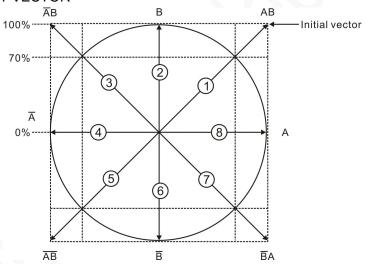


#### Output current waveform of 2-2 phase

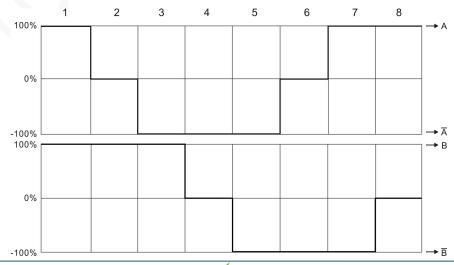


# (2) 1-2 Phase

#### **OUTPUT CURRENT VECTOR**



#### Output current waveform of 1-2 phase



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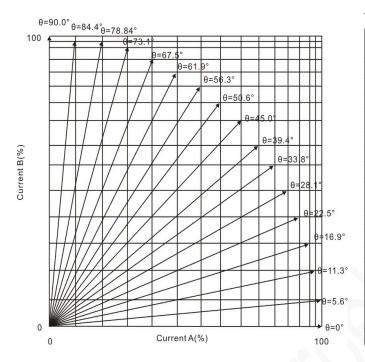
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# (3) Micro Step Control

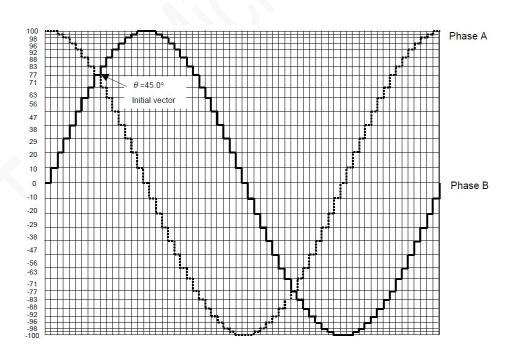
Any of the four resolutions are selectable: 64, 128, 256 and 512 steps. The example given below is for 64 steps:

#### **OUTPUT CURRENT VECTOR**



# ACH & BCH POSITION AND CURRENT

Set	Current	Current
Angle	В	Α
0.0	0.0%	100.0%
5.6	9.8%	100.0%
11.3	19.5%	98.1%
16.9	29.0%	95.7%
22.5	38.3%	92.4%
28.1	47.1%	88.2%
33.8	55.6%	83.1%
39.4	63.4%	77.3%
45.0	70.7%	70.7%
50.6	77.3%	63.4%
56.3	83.1%	55.6%
61.9	88.2%	47.1%
67.5	92.4%	38.3%
73.1	95.7%	29.0%
78.8	98.1%	19.5%
84.4	100.0%	9.8%
90.0	100.0%	0%
	Angle 0.0 5.6 11.3 16.9 22.5 28.1 33.8 39.4 45.0 50.6 56.3 61.9 67.5 73.1 78.8 84.4	Angle         B           0.0         0.0%           5.6         9.8%           11.3         19.5%           16.9         29.0%           22.5         38.3%           28.1         47.1%           33.8         55.6%           39.4         63.4%           45.0         70.7%           50.6         77.3%           56.3         83.1%           61.9         88.2%           67.5         92.4%           73.1         95.7%           78.8         98.1%           84.4         100.0%



Output current waveform of pseudo sine wave

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# (4) IR cut Control

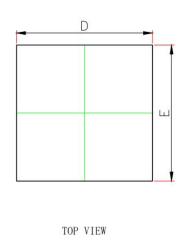
The TMI8152 can control IR cut to work in the following modes:

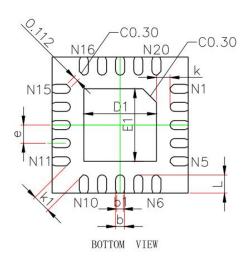
IREN	IRINA (register)	IRINB (register)	IROA(5 CH)	IROB(5 CH)	Status
Н	Н	L	Н	L	Forwards
Н	L	Н	L	Н	Backwards
Н	Н	Н	L	L	Brake
Н	L	L	High-Z	High-Z	Coast

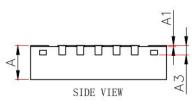


# **PACKAGE INFORMATION**

#### QFN3x3-20L







Cymahal	Dimensions I	n Millimeters	Dimension	s In Inches
Symbol	Min.	Max.	Min.	Max.
Α	0.700	0.800	0.028	0.031
A1	0.000	0.050	0.000	0.002
A3	0.203	REF.	0.008	REF.
D	2.950	3.050	0.116	0.120
E	2.950	3.050	0.116	0.120
D1	1.550	1.650	0.061	0.065
E1	1.550	1.650	0.061	0.065
k	0.300	REF.	0.012	REF.
k1	0.400	REF.	0.016	REF.
b	0.150	0.250	0.006	0.010
b1	0.150REF.		0.006REF.	
е	0.400BSC.		0.016	BSC.
L	0.300	0.500	0.012	0.020

#### Note:

- 1) Package length does not include mold flash, protrusion or gate burr.
- 2) Package width does not include inter lead flash or protrusion.
- 3) Lead popularity (bottom of leads after forming) shall be 0.10 millimeters max.
- 4) Pin 1 is upper left pin when reading top mark from left to right.

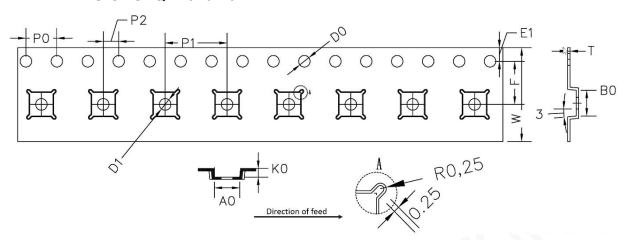


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# TAPE AND REEL INFORMATION

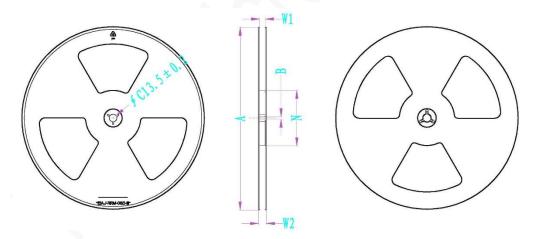
#### **TAPE DIMENSIONS: QFN3x3-20L**



Unit: mm

Symbol	Dimensions	Symbol	Dimensions	Symbol	Dimensions	Symbol	Dimensions
A0	3.30±0.10	P0	4.00±0.10	E1	1.75±0.10	D1	1.55±0.05
В0	3.30±0.10	P1	8.00±0.10	F	5.50±0.10	Т	0.30±0.05
K0	1.10±0.10	P2	2.00±0.10	D0	1.55±0.05	W	12.00±0.30

#### **REEL DIMENSIONS: QFN3x3-20L**



Unit: mm

ØA	В	ØС	ØN	W1	W2
330±1.0	4.7±0.5	13.5±0.2	100±0.5	13.4±0.5	17.4±0.5

#### Note:

- 1) All Dimensions are in Millimeter
- 2) Quantity of Units per Reel is 5000
- 3) MSL level is level 3.

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