

**General Description**

These P-Channel enhancement mode power field effect transistors use advanced trench technology and design to provide excellent RDS(ON) . This device is suitable for use as a load switch or in PWM applications.

**Features**

- Fast switching speed
- Lower On-resistance
- 100% EAS Guaranteed
- Simple Drive Requirement

**Product Summary**

BVDSS	RDS(on)	ID
-150V	78mΩ	-30A

**Applications**

- DC-DC Converters
- LCD Display inverter
- Power Management in Note book

**TO-252/251 Pin Configuration**

Type	Package	Marking
CMD5970	TO-252	CMD5970
CMU5970	TO-251	CMU5970

**Absolute Maximum Ratings**

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	-150	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub> @T <sub>C</sub> =25°C	Continuous Drain Current	-30	A
I <sub>DM</sub>	Pulsed Drain Current	-120	A
EAS	Single Pulse Avalanche Energy <sup>1</sup>	648	mJ
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation	200	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 175	°C
T <sub>J</sub>	Operating Junction Temperature Range	-55 to 175	°C

**Thermal Data**

Symbol	Parameter	Typ.	Max.	Unit
R <sub>θJA</sub>	Junction-to-Ambient	---	40	°C/W
R <sub>θJC</sub>	Junction-to-Case (Drain)	---	1.5	°C/W

Electrical Characteristics ( $T_J=25^\circ\text{C}$  , unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_D=-250\mu\text{A}$	-150	---	---	V
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=-10\text{V}$ , $I_D=-10\text{A}$	---	---	78	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}$ , $I_D=-8\text{A}$	---	---	90	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$ , $I_D=-250\mu\text{A}$	-1	---	-3	V
$I_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=-150\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25^\circ\text{C}$	---	---	-1	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	nA
$g_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=-5\text{V}$ , $I_D=-10\text{A}$	---	23	---	S
$Q_g$	Total Gate Charge	$V_{\text{DS}}=-80\text{V}$ , $I_D=-18\text{A}$ $V_{\text{GS}}=0$ to -10V	---	60	---	nC
$Q_{\text{gs}}$	Gate-Source Charge		---	10	---	
$Q_{\text{gd}}$	Gate-Drain Charge		---	20	---	
$T_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DS}}=-50\text{V}$ , $V_{\text{GS}}=-10\text{V}$ , $R_G=3.3\Omega$	---	15	---	ns
$T_r$	Rise Time		---	35	---	
$T_{\text{d(off)}}$	Turn-Off Delay Time		---	85	---	
$T_f$	Fall Time		---	80	---	
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=-25\text{V}$ , $V_{\text{GS}}=0\text{V}$ , f=1MHz	---	4000	---	pF
$C_{\text{oss}}$	Output Capacitance		---	250	---	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	140	---	

## Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	-30	A
$I_{\text{SM}}$	Pulsed Source Current		---	---	-120	A
$V_{\text{SD}}$	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$ , $I_s=-20\text{A}$	---	---	-1.2	V

Notes:

1.The EAS data shows Max. rating .The test condition is  $V_{\text{DS}}=-30\text{V}$  ,  $V_{\text{GS}}=-10\text{V}$  ,  $L=1\text{mH}$  ,  $I_{\text{AS}}=-36\text{A}$ .

This product has been designed and qualified for the consumer market.

Cmos assumes no liability for customers' product design or applications.

Cmos reserves the right to improve product design ,functions and reliability without notice.

## Typical Characteristics

