MOSFETs Silicon N-Channel MOS (π-MOSVII)

TK11A65D

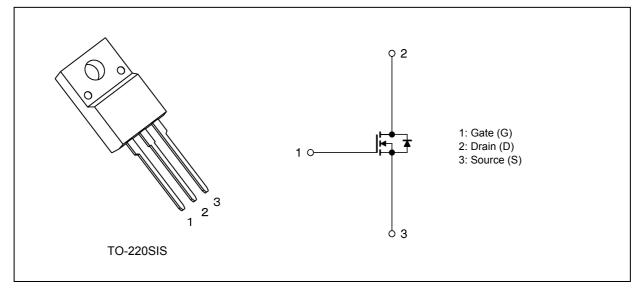
1. Applications

Switching Voltage Regulators

2. Features

- (1) Low drain-source on-resistance: $R_{DS(ON)} = 0.54 \Omega$ (typ.)
- (2) High forward transfer admittance: $|Y_{fs}| = 7.5 \text{ S}$ (typ.)
- (3) Low leakage current: $I_{DSS} = 10 \ \mu A \ (max) \ (V_{DS} = 650 \ V)$
- (4) Enhancement mode: V_{th} = 2.0 to 4.0 V (V_{DS} = 10 V, I_D = 1 mA)

3. Packaging and Internal Circuit



4. Absolute Maximum Ratings (Note) ($T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics	Symbol	Rating	Unit	
Drain-source voltage		V _{DSS}	650	V
Gate-source voltage		V _{GSS}	±30	
Drain current (DC)	(Note 1)	Ι _D	11	A
Drain current (pulsed)	(Note 1)	I _{DP}	44	
Power dissipation $(T_c = 25^{\circ}C)$		PD	45	W
Single-pulse avalanche energy	(Note 2)	E _{AS}	506	mJ
Avalanche current		I _{AR}	11	A
Repetitive avalanche energy	(Note 3)	E _{AR}	4.5	mJ
Channel temperature		T _{ch}	150	°C
Storage temperature		T _{stg}	-55 to 150	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Start of commercial production 2010-09 2013-12-25 Rev.2.0

5. Thermal Characteristics

Characteristics	Symbol	Max	Unit
Channel-to-case thermal resistance	R _{th(ch-c)}	2.78	°C/W
Channel-to-ambient thermal resistance	R _{th(ch-a)}	62.5	

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_DD = 90 V, T_ch = 25°C (initial), L = 7.4 mH, R_G = 25 Ω , I_AR = 11 A

Note 3: Repetitive rating; pulse width limited by maximum channel temperature

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.

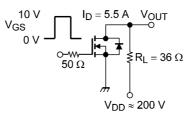
6. Electrical Characteristics

6.1. Static Characteristics (Ta = 25°C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I _{GSS}	V_{GS} = ±30 V, V_{DS} = 0 V	_	_	±1	μA
Drain cut-off current	I _{DSS}	V _{DS} = 650 V, V _{GS} = 0 V	_	_	10	
Drain-source breakdown voltage	V _{(BR)DSS}	I _D = 10 mA, V _{GS} = 0 V	650	_	_	V
Gate threshold voltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	—	4.0	
Drain-source on-resistance	R _{DS(ON)}	V _{GS} = 10 V, I _D = 5.5 A	_	0.54	0.70	Ω
Forward transfer admittance	Y _{fs}	V _{DS} = 10 V, I _D = 5.5 A	1.9	7.5		S

6.2. Dynamic Characteristics ($T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C _{iss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	_	1700	—	pF
Reverse transfer capacitance	C _{rss}		_	8	—	
Output capacitance	C _{oss}			157	_	
Switching time (rise time)	t _r	See Figure 6.2.1.		30	_	ns
Switching time (turn-on time)	t _{on}		_	80	—	
Switching time (fall time)	t _f			18	—	
Switching time (turn-off time)	t _{off}		_	135	_	



Duty \leq 1%, $t_W =$ 10 μs

Fig. 6.2.1 Switching Time Test Circuit

6.3. Gate Charge Characteristics ($T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus gate-drain)	Qg	$V_{DD} \approx 400 \text{ V}, \text{ V}_{GS}$ = 10 V, I _D = 11 A	_	30	_	nC
Gate-source charge	Q _{gs}			21	_	
Gate-drain charge	Q _{gd}		_	9	—	

6.4. Source-Drain Characteristics ($T_a = 25^{\circ}C$ unless otherwise specified)

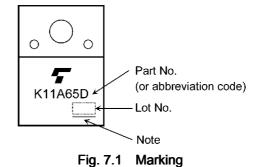
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Reverse drain current (DC)	(Note 1)	I _{DR}	—	_	_	11	A
Reverse drain current (pulsed)	(Note 1)	I _{DRP}	—	_	_	44	
Diode forward voltage		V _{DSF}	I _{DR1} = 11 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time		t _{rr}	I _{DR} = 11 A, V _{GS} = 0 V	_	1300	_	ns
Reverse recovery charge		Q _{rr}	-dI _{DR} /dt = 100 A/μs		14	_	μC

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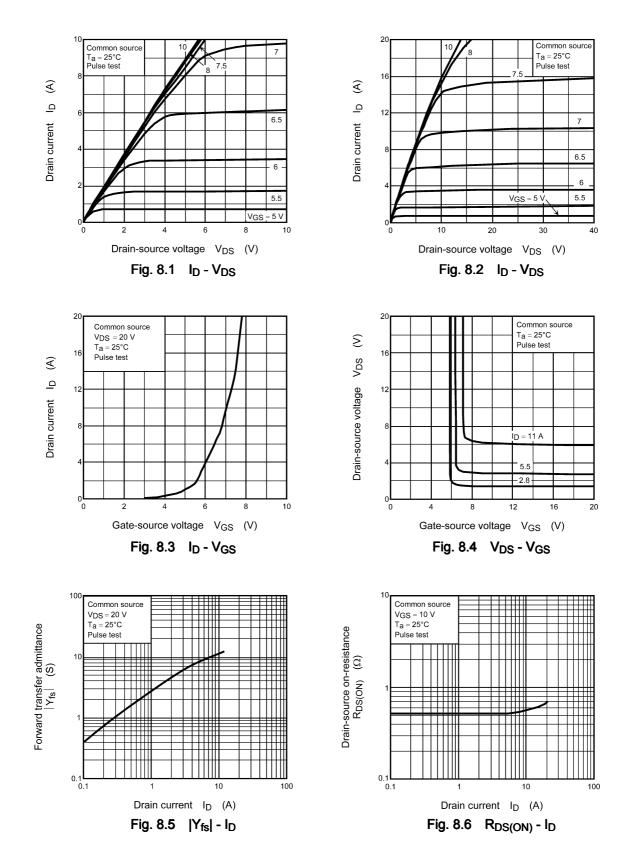
7. Marking (Note)

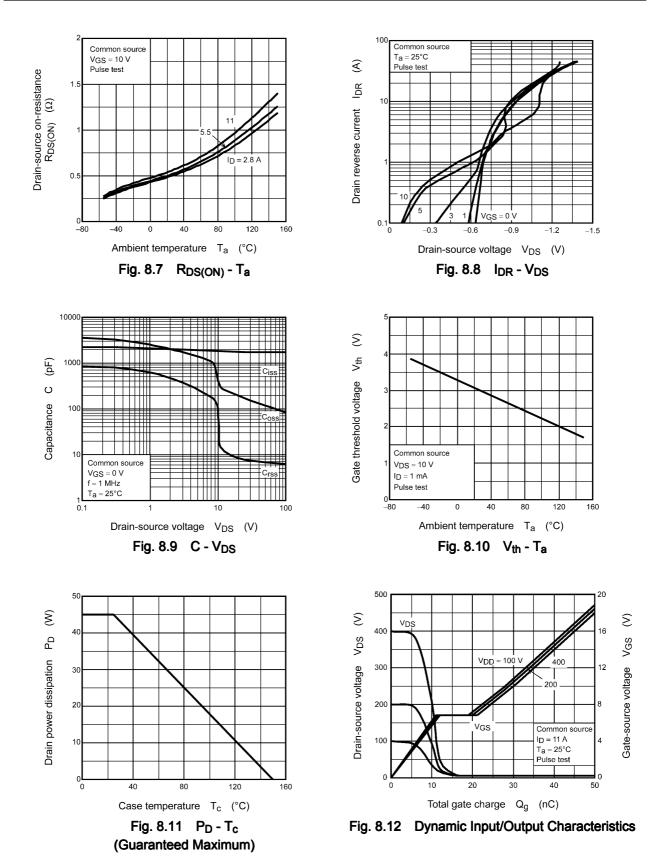


Note: A line under a Lot No. identifies the indication of product Labels. Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]] Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the

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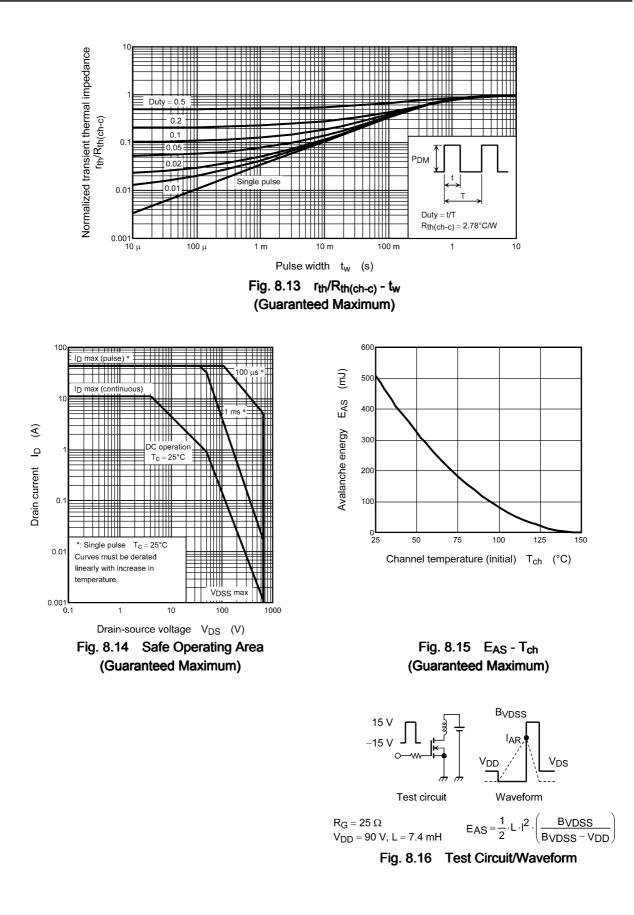
8. Characteristics Curves (Note)





2013-12-25

Rev.2.0

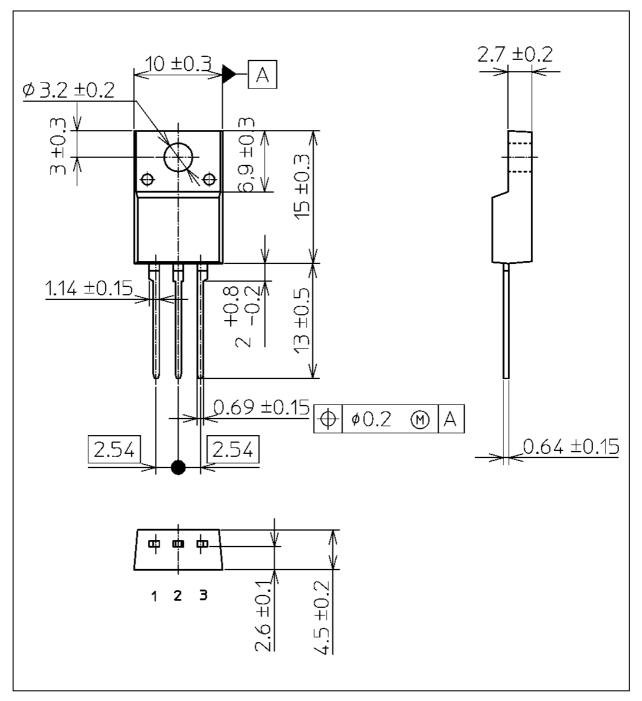


Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

Package Dimensions

TK11A65D

Unit: mm





Packa	ge Name(s)
JEITA: SC-67	
TOSHIBA: 2-10U1S	
Nickname: TO-220SIS	

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