

## Description

The ZXMN3A01F uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a Battery protection or in other Switching application.

# D S S G SOT-23

#### **General Features**

 $V_{DS} = 30V I_D = 4A$ 

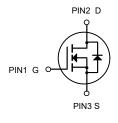
 $R_{DS(ON)}$  < 38m $\Omega$  @  $V_{GS}$ =10V

## **Application**

Battery protection

Load switch

Uninterruptible power supply



N-Channel MOSFET

## **Package Marking and Ordering Information**

Product ID	Pack	Brand	Qty(PCS)
ZXMN3A01F	SOT-23	HXY MOSFET	3000

## Absolute Maximum Ratings (T<sub>A</sub>=25 ℃ unless otherwise noted)

Symbol	Parameter	Limit	Unit
V <sub>DS</sub>	Drain-Source Voltage	30	V
Vgs	Gate-Source Voltage	±20	V
I <sub>D</sub>	Drain Current-Continuous	4	А
Ірм	Drain Current-Pulsed (Note 1)	16.4	А
P <sub>D</sub>	Maximum Power Dissipation	1	W
Тл,Тѕтс	Operating Junction and Storage Temperature Range	-55 To 150	$^{\circ}$ C
Reja	Thermal Resistance,Junction-to-Ambient (Note 2)	125	°C/W



## Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units	
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	30	-	-	V	
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V,	-	-	1.0	μΑ	
I <sub>GSS</sub>	Gate to Body Leakage Current	V <sub>DS</sub> =0V, V <sub>GS</sub> = ±20V	-	-	±100	nA	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	1.0	1.5	2.5	V	
	Static Drain-Source on-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =4A	-	29	38	mQ	
$R_{DS(on)}$		$V_{GS}$ =4.5V, $I_D$ =3A	-	45	65	11177	
$C_{iss}$	Input Capacitance	45)///	-	233	_	pF	
Coss	Output Capacitance	V <sub>DS</sub> =15V, V <sub>GS</sub> =0V,	-	44	-	pF	
Crss	Reverse Transfer Capacitance	f=1.0MHz	-	33	-	pF	
Qg	Total Gate Charge	V <sub>DS</sub> =15V, I <sub>D</sub> =2A, V <sub>GS</sub> =10V	-	3	-	nC	
Q <sub>gs</sub>	Gate-Source Charge		-	0.5	-	nC	
$Q_{gd}$	Gate-Drain("Miller") Charge		-	0.8	-	nC	
t <sub>d(on)</sub>	Turn-on Delay Time	$V_{DS}$ =15 $V$ , $I_{D}$ =4 $A$ , $R_{GEN}$ =3 $\Omega$ ,	-	4	-	ns	
t <sub>r</sub>	Turn-on Rise Time		-	2.1	-	ns	
t <sub>d(off)</sub>	Turn-off Delay Time		-	15	-	ns	
t <sub>f</sub>	Turn-off Fall Time	- V <sub>GS</sub> =10V	-	3.2	-	ns	
Is	Maximum Continuous Drain to Source Diode Forward		_	_	4	Α	
15	Current		-	-	4		
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	-	16	Α	
$V_{\text{SD}}$	Drain to Source Diode Forward Voltage V <sub>GS</sub> =0V, I <sub>S</sub> =4A		-	-	1.2	V	

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

<sup>2.</sup> Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%



## **Typical Characteristics**

Figure1: Output Characteristics

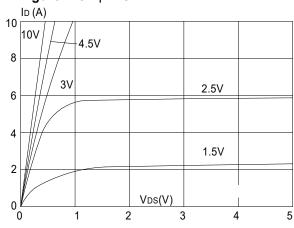


Figure 2: Typical Transfer Characteristics

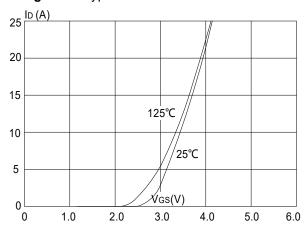


Figure 3:On-resistance vs. Drain Current

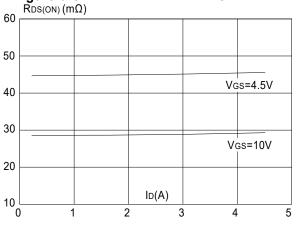


Figure 4: Body Diode Characteristics

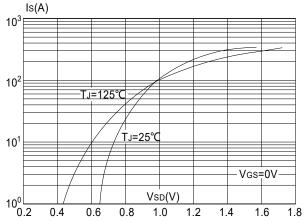


Figure 5: Gate Charge Characteristics

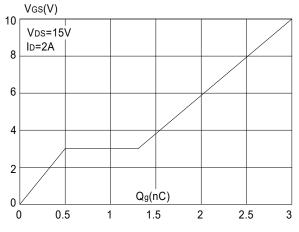
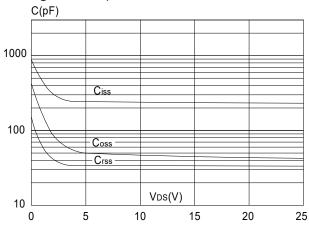
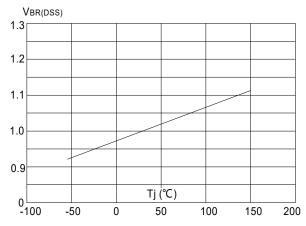


Figure 6: Capacitance Characteristics





**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature



**Figure 8:** Normalized on Resistance vs. Junction Temperature

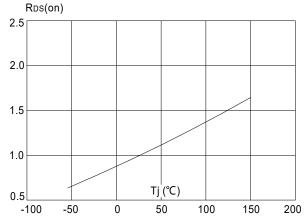
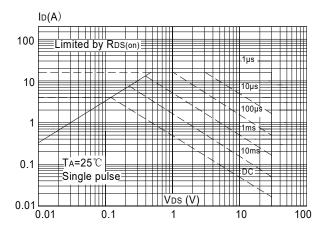
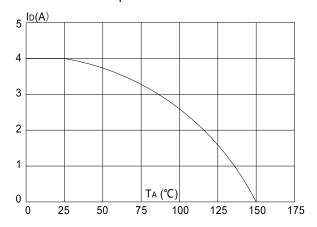


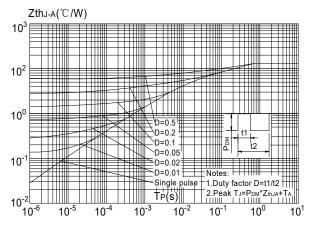
Figure 9: Maximum Safe Operating Area



**Figure 10:** Maximum Continuous Drain Current vs. Ambient Temperature

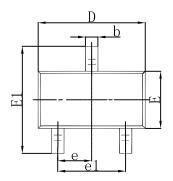


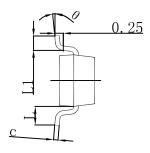
**Figure.11:** Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

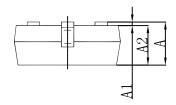




# **SOT-23 Package Outline Dimensions**

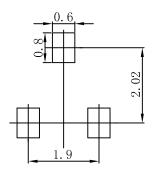






Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min	Max	Min	Max	
Α	0.900	1.150	0.035	0.045	
A1	0.000	0.100	0.000	0.004	
A2	0.900	1.050	0.035	0.041	
b	0.300	0.500	0.012	0.020	
С	0.080	0.150	0.003	0.006	
D	2.800	3.000	0.110	0.118	
E	1.200	1.400	0.047	0.055	
E1	2.250	2.550	0.089	0.100	
е	0.950 TYP		0.037 TYP		
e1	1.800	2.000	0.071	0.079	
L	0.550 REF		0.022 REF		
L1	0.300	0.500	0.012	0.020	
θ	0°	8°	0°	8°	

# **SOT-23 Suggested Pad Layout**



## Note:

- 1.Controlling dimension:in millimeters.
- 2.General tolerance:± 0.05mm.
  3.The pad layout is for reference purposes only.



#### **Attention**

- Any and all HUA XUAN YANG ELECTRONICS products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your HUA XUAN YANG ELECTRONICS representative nearest you before using any HUA XUAN YANG ELECTRONICS products described or contained herein in such applications.
- HUA XUAN YANG ELECTRONICS assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all HUA XUAN YANG ELECTRONICS products described or contained herein.
- Specifications of any and all HUA XUAN YANG ELECTRONICS products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- HUA XUAN YANG ELECTRONICS CO.,LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all HUA XUAN YANG ELECTRONICS products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of HUA XUAN YANG ELECTRONICS CO.,LTD.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production.

  HUA XUAN YANG ELECTRONICS believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the HUA XUAN YANG ELECTRONICS product that you intend to use.