

General Description

The WSD2068DN23 is the highest performance trench N-ch MOSFETs with extreme high cell density, which provide excellent RDSON and gate charge for most of the small power switching and load switch applications.

The WSD2068 meet the RoHS and Green Product requirement with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent Cdv/dt effect decline
- Green Device Available

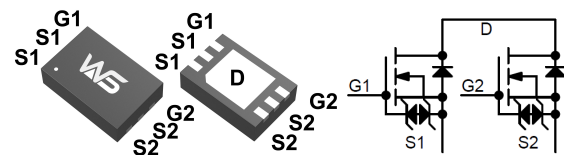
Product Summary

BV_{DSS}	$R_{DS(on)}$	I_D
20V	15.5mΩ	7.5A

Applications

- Power Management in Notebook Computer, Portable Equipment and Battery Powered Systems.
- DC-DC Power System
- ESD:2KV

DFN2X3A_EP Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	20	V
V_{GS}	Gate-Source Voltage	± 12	V
$I_D@T_A=25^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 4.5V^1$	7.5	A
$I_D@T_A=70^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 4.5V^1$	6.5	A
I_{DM}	Pulsed Drain Current ²	30	A
$P_D@T_A=25^\circ\text{C}$	Total Power Dissipation ³	1.5	W
$P_D@T_A=70^\circ\text{C}$	Total Power Dissipation ³	1.0	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient ¹ (Steady State)	---	120	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance Junction-ambient ¹ ($t < 10S$)	---	83	$^\circ\text{C/W}$

Electrical Characteristics ($T_J=25^{\circ}\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	20	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	BVDSS Temperature Coefficient	Reference to 25°C , $I_D=1mA$	---	0.022	---	V/ $^{\circ}\text{C}$
$R_{DS(ON)}$	Static Drain-Source On-Resistance ²	$V_{GS}=4.5V, I_D=5.5A$	---	12	15.5	$m\Omega$
		$V_{GS}=2.5V, I_D=5.5A$	---	16	20	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	0.3	0.7	1.0	V
$\Delta V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	-2.32	---	mV/ $^{\circ}\text{C}$
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=16V, V_{GS}=0V, T_J=25^{\circ}\text{C}$	---	---	1	μA
		$V_{DS}=16V, V_{GS}=0V, T_J=55^{\circ}\text{C}$	---	---	5	
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 12V, V_{DS}=0V$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{DS}=5V, I_D=10A$	---	20	---	S
R_g	Gate Resistance	$V_{DS}=0V, V_{GS}=0V, f=1MHz$	---	11	---	Ω
Q_g	Total Gate Charge (4.5V)	$V_{DS}=10V, V_{GS}=4.5V, I_D=5A$	---	15	20	nC
Q_{gs}	Gate-Source Charge		---	2.2	---	
Q_{gd}	Gate-Drain Charge		---	4.2	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DS}=10V, V_{GS}=10V, R_G=6\Omega, I_D=5A, R_L=2\Omega$	---	148	---	ns
T_r	Rise Time		---	277	---	
$T_{d(off)}$	Turn-Off Delay Time		---	1616	---	
T_f	Fall Time		---	751	---	
C_{iss}	Input Capacitance	$V_{DS}=10V, V_{GS}=0V, f=1MHz$	---	1219	1350	pF
C_{oss}	Output Capacitance		---	150	---	
C_{rss}	Reverse Transfer Capacitance		---	123	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current ^{1,4}	$V_G=V_D=0V$, Force Current	---	---	5	A
I_{SM}	Pulsed Source Current ^{2,4}		---	---	15	A
V_{SD}	Diode Forward Voltage ²	$V_{GS}=0V, I_S=1A, T_J=25^{\circ}\text{C}$	---	0.76	1.3	V
t_{rr}	Reverse Recovery Time	$I_F=5A, dI/dt=100A/\mu s, T_J=25^{\circ}\text{C}$	---	245	---	nS
Q_{rr}	Reverse Recovery Charge		---	1105	---	nC

Note :

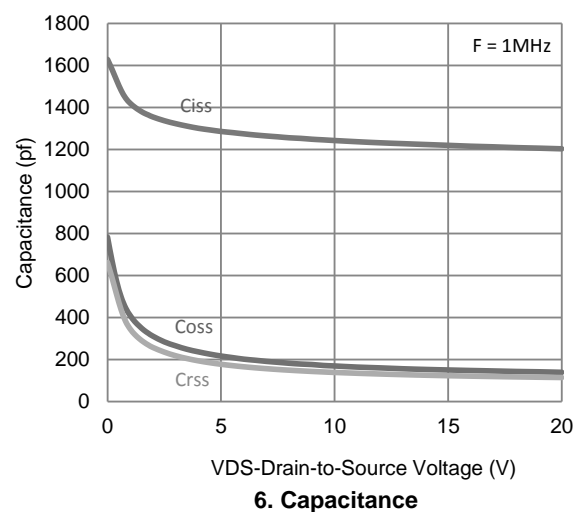
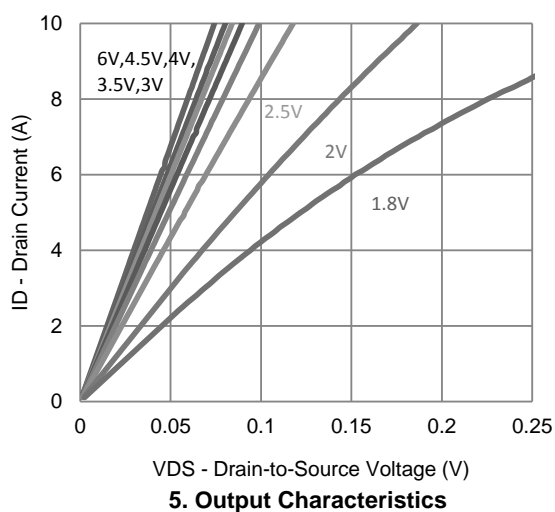
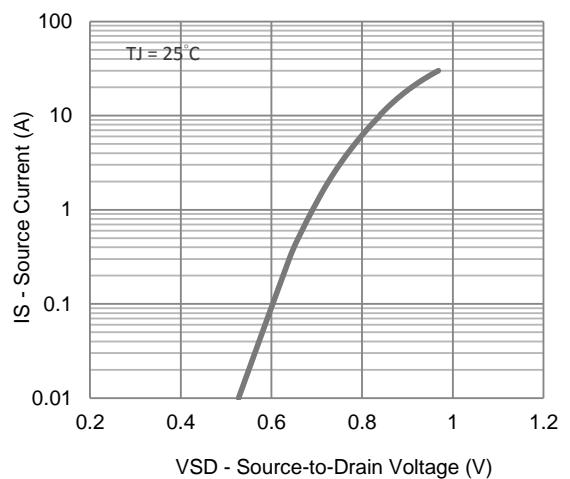
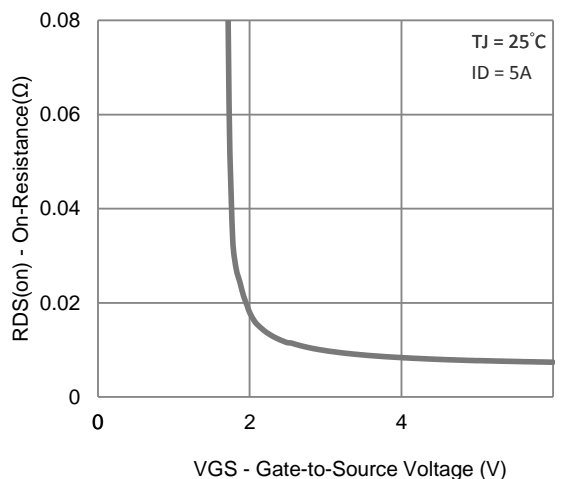
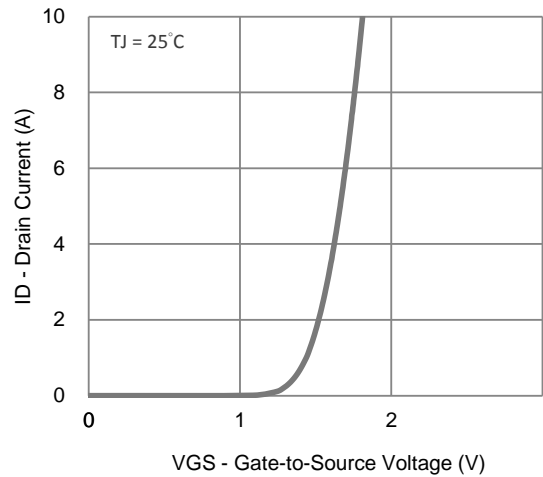
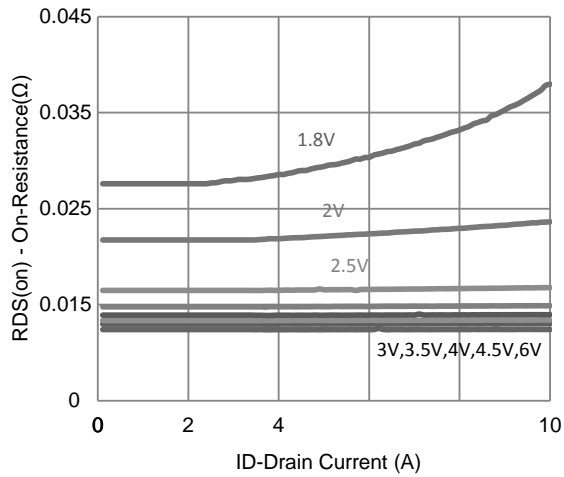
1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper, $t<10\text{sec}$.

2.The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$

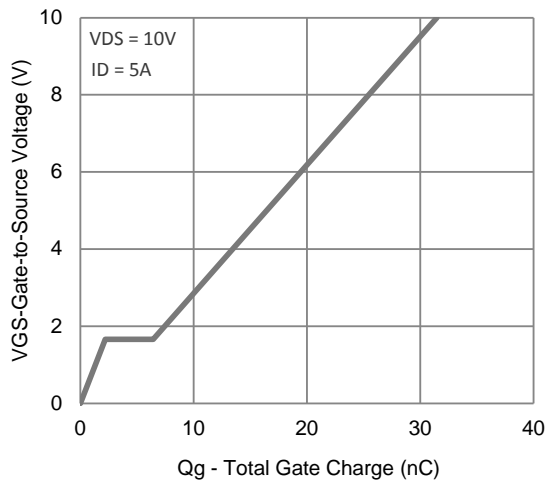
3.The power dissipation is limited by 150°C junction temperature

4.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

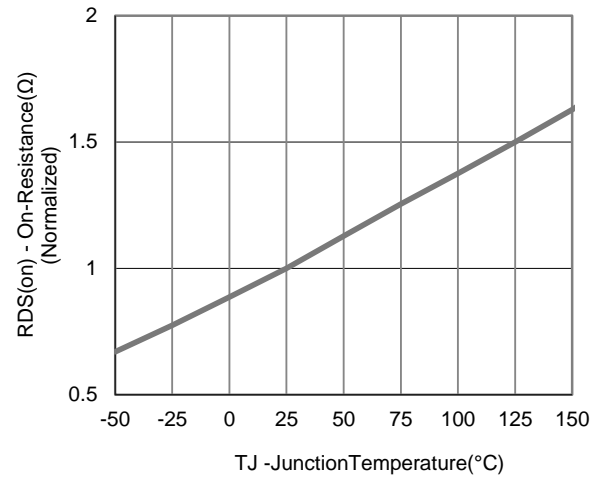
Typical Characteristics



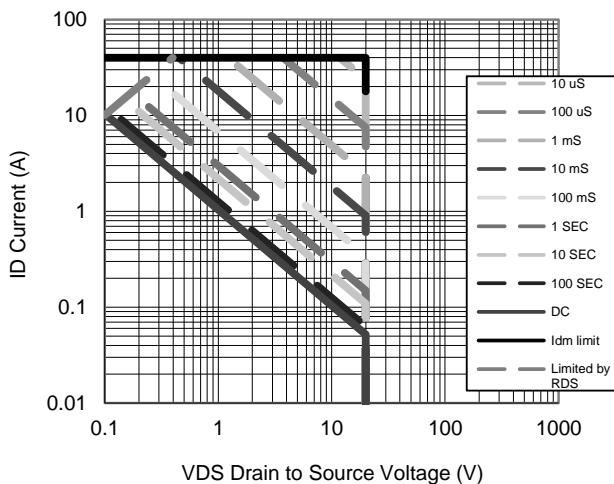
Typical Characteristics



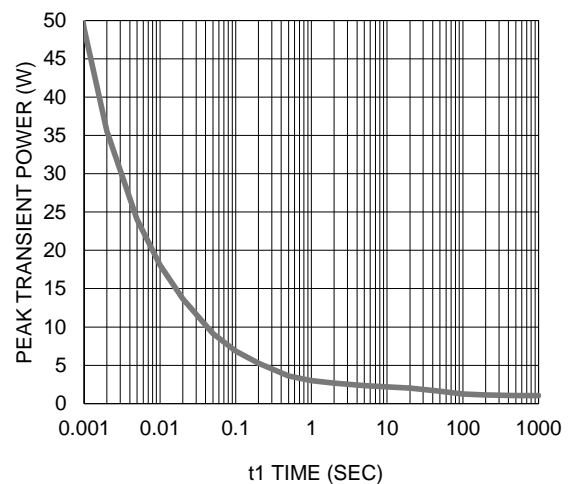
7. Gate Charge



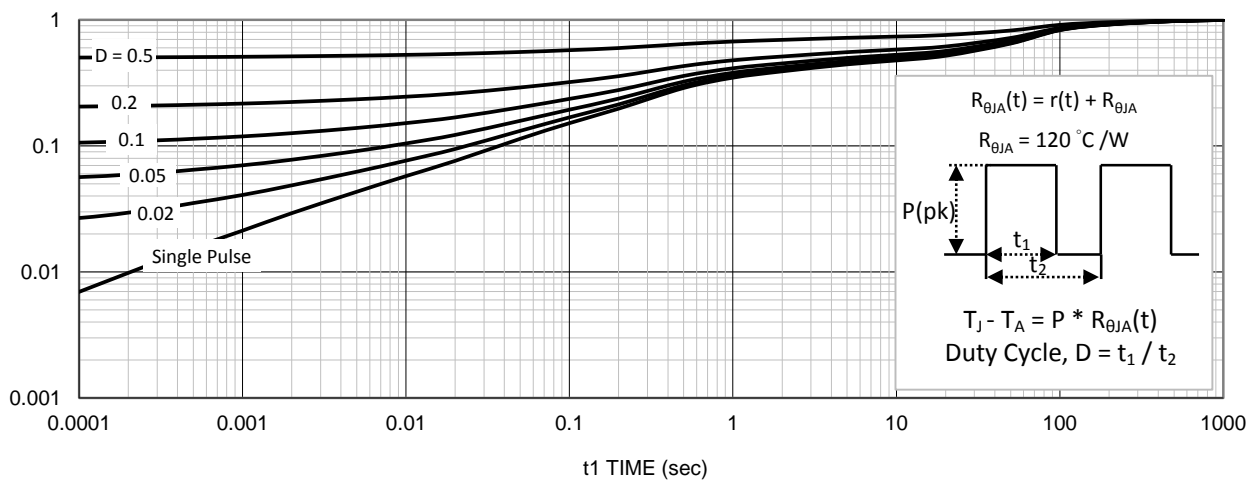
8. Normalized On-Resistance Vs Junction Temperature



9. Safe Operating Area



10. Single Pulse Maximum Power Dissipation



11. Normalized Thermal Transient Junction to Ambient

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