

# isc Silicon NPN Power Transistor

2N3865

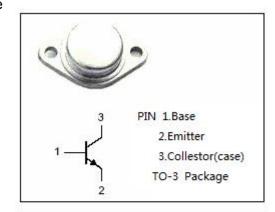
### **DESCRIPTION**

- Excellent Safe Operating Area
- · Low Collector-Emitter Saturation Voltage
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation.



### **APPLICATIONS**

 Designed for medium-speed switching and amplifier applications.

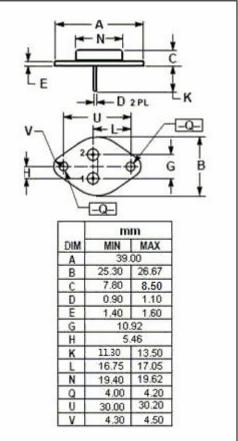


# ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT	
V <sub>CBO</sub>	Collector-Base Voltage	160	V	
V <sub>CEO</sub>	Collector-Emitter Voltage	150	V	
V <sub>EBO</sub>	Emitter-Base Voltage	7	V	
Ic	Collector Current-Continuous	7.5	Α	
Pc	Collector Power Dissipation@T <sub>c</sub> =25℃	117	W	
TJ	Junction Temperature	-65~200	$^{\circ}$ C	
T <sub>stg</sub>	Storage Temperature	-65~200	$^{\circ}$	

## THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
R <sub>th j-c</sub>	Thermal Resistance,Junction to Case	1.5	°C/W





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#### **ELECTRICAL CHARACTERISTICS**

T<sub>C</sub>=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
V <sub>CEO(SUS)</sub> *	Collector-Emitter Sustaining Voltage	I <sub>C</sub> =200mA; I <sub>B</sub> = 0	150		V
I <sub>EBO</sub>	Emitter Cutoff Current	V <sub>EB</sub> = 7V; I <sub>C</sub> = 0		5	mA
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = 3A; I <sub>B</sub> = 0.2A		1.0	V
V <sub>BE(sat)</sub>	Base-Emitter Saturation Voltage	I <sub>C</sub> =3A; I <sub>B</sub> = 0.2A		2.0	V
h <sub>FE</sub>	DC Current Gain	I <sub>C</sub> = 3A; V <sub>CE</sub> = 2V	30	90	

<sup>\*:</sup>Pulse test:Pulse width=300us,duty cycle≤2%



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