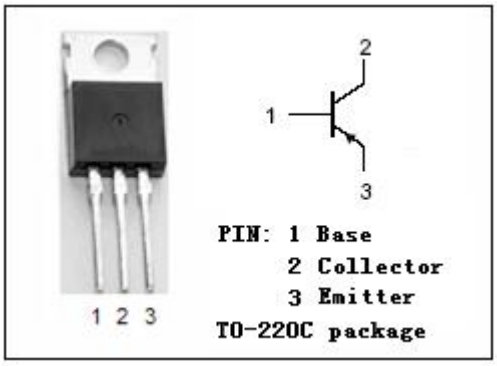
isc Silicon PNP Power Transistor — 2SB896



DESCRIPTION

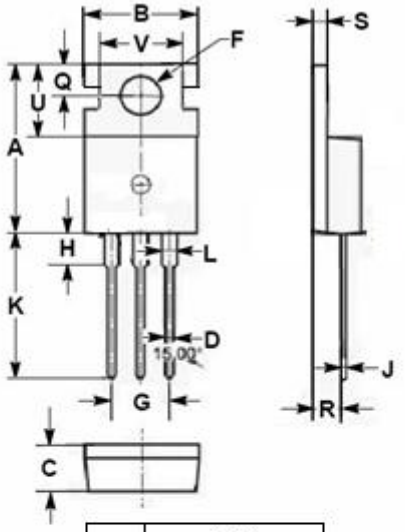
- Collector-Emitter BreakdownVoltage-
  : $V_{(BR)CEO}$= -40V(Min.)
- Low Collector Saturation Voltage-
  : $V_{CE(sat)}$= -0.6(Max.) @I_C= -7A
- High speed switching
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for low voltage switching applications .

ABSOLUTE MAXIMUM RATINGS(T_a=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-40	V
V_{CEO}	Collector-Emitter Voltage	-40	V
V_{EBO}	Emitter-Base Voltage	-5	V
I_C	Collector Current-Continuous	-10	A
I_{CM}	Collector Current-Peak	-15	A
P_C	Collector Power Dissipation@T_C=25℃	35	W
T_J	Junction Temperature	150	℃
T_{stg}	Storage Temperature	-55~150	℃



DIM	mm MIN	mm MAX
A	15.50	15.90
B	9.80	10.20
C	4.20	4.50
D	0.70	0.90
F	3.40	3.70
G	4.98	5.18
H	2.68	2.90
J	0.44	0.60
K	12.80	13.40
L	1.20	1.45
Q	2.70	2.90
R	2.30	2.70
S	1.29	1.35
U	6.45	6.65
V	8.66	8.86

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ELECTRICAL CHARACTERISTICS

Tj=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
$V_{(BR)CEO}$	Collector-Emitter Breakdown Voltage	I_C= -10mA; R_{BE}= ∞	-40			V
$V_{(BR)EBO}$	Emitter-Base Breakdown Voltage	I_E= -1mA; I_C= 0	-5			V
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	I_C= -7A; I_B= -0.23A			-0.6	V
$V_{BE(sat)}$	Base-Emitter Saturation Voltage	I_C= -7A; I_B= -0.23A			-1.5	V
I_{CBO}	Collector Cutoff Current	V_{CB}= -40V; I_E= 0			-50	μA
I_{EBO}	Emitter Cutoff Current	V_{EB}= -5V; I_C= 0			-10	μA
h_{FE1}	DC Current Gain	I_C= -0.1A ; V_{CE}= -2V	45			
h_{FE2}	DC Current Gain	I_C= -2A ; V_{CE}= -2V	60		260	
C_{OB}	Output Capacitance	I_E= 0; V_{CB}= -10V; f= 1MHz		200		pF
Switching Times						
t_{on}	Turn-on Time	I_C= -2A; I_{B1}= -I_{B2}= -66mA,			0.5	μs
t_{stg}	Storage Time				1.0	μs
t_f	Fall Time				0.5	μs

h_{FE-2} Classifications

R	Q	P
60-120	90-180	130-260

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