

isc Silicon PNP Darlington Power Transistor
2SB955
DESCRIPTION

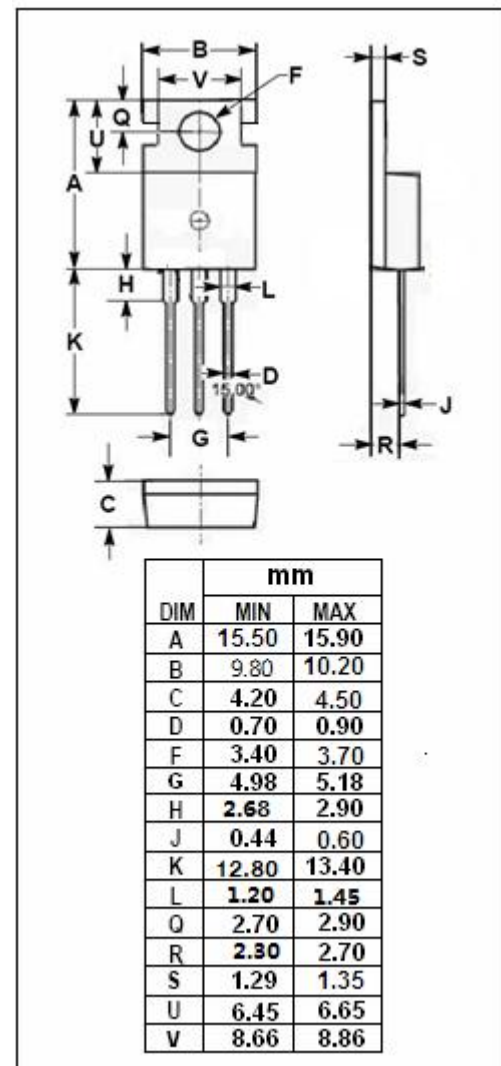
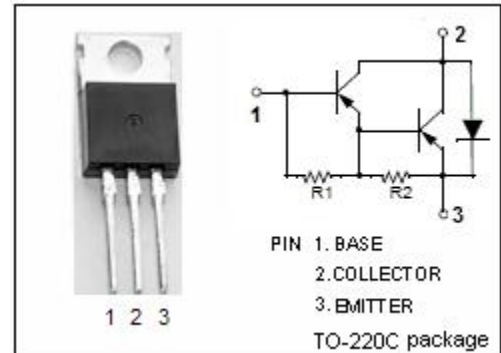
- High DC Current Gain-
: $h_{FE} = 1000(\text{Min})@ I_C = -5\text{A}$
- Collector-Emitter Breakdown Voltage-
: $V_{(BR)CEO} = -120\text{V}(\text{Min})$
- Low Collector-Emitter Saturation Voltage-
: $V_{CE(\text{sat})} = -1.5\text{V}(\text{Max})@ I_C = -5\text{A}$
- Complement to Type 2SD1126
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for power switching applications.

ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-120	V
V_{CEO}	Collector-Emitter Voltage	-120	V
V_{EBO}	Emitter-Base Voltage	-7	V
I_C	Collector Current-Continuous	-10	A
I_{CM}	Collector Current-Peak	-15	A
P_C	Collector Power Dissipation $T_C = 25^\circ\text{C}$	50	W
T_J	Junction Temperature	150	°C
T_{stg}	Storage Temperature Range	-55~150	°C



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ELECTRICAL CHARACTERISTICS

 T_c=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = -25mA, R _{BE} = ∞	-120			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = -200mA, I _C = 0	-7			V
V _{CE(sat)-1}	Collector-Emitter Saturation Voltage	I _C = -5A, I _B = -10mA			-1.5	V
V _{CE(sat)-2}	Collector-Emitter Saturation Voltage	I _C = -10A, I _B = -0.1A			-3.0	V
V _{BE(sat)-1}	Base-Emitter Saturation Voltage	I _C = -5A, I _B = -10mA			-2.0	V
V _{BE(sat)-2}	Base-Emitter Saturation Voltage	I _C = -10A, I _B = -0.1A			-3.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} = -120V, I _E = 0			-100	μ A
I _{CEO}	Collector Cutoff Current	V _{CE} = -100V, R _{BE} = ∞			-10	μ A
h _{FE}	DC Current Gain	I _C = -5A; V _{CE} = -3V	1000		20000	

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