

isc Silicon NPN Power Transistor

2SC4507

DESCRIPTION

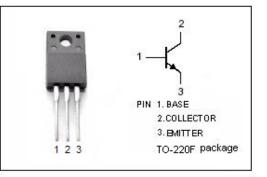
- · Fast switching speed
- · Silicon NPN planar diffused planar transistor
- 100% avalanche tested
- · Minimum Lot-to-Lot variations for robust device performance and reliable operation

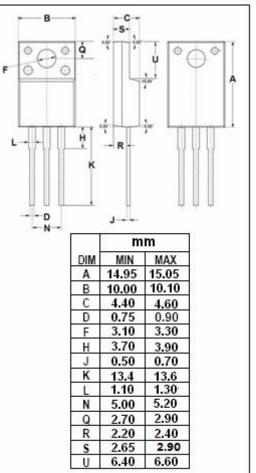
APPLICATIONS

· Audio temperature compensation and general purpose

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
Vсво	Collector-Base Voltage	500	V
V _{CEO}	Collector-Emitter Voltage	400	V
V _{EBO}	Emitter-Base Voltage	7	V
lc	Collector Current-Continuous	5	A
I _B	Base Current-Continuous	1	A
Pc	Collector Power Dissipation @T _c =25℃	40	W
TJ	Junction Temperature	150	°C
T _{stg}	Storage Temperature	-55~150	°C







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ELECTRICAL CHARACTERISTICS

Tj=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	МАХ	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 25mA; I _B = 0	400			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 2A; I _B = 0.4A			0.8	V
V _{BE(sat)}	Collector-Emitter Saturation Voltage	I _C = 2A; I _B = 0.4A			1.2	V
I _{СВО}	Collector Cutoff Current	V _{CB} = 500V; I _E = 0			100	μ Α
I _{EBO}	Emitter Cutoff Current	V _{EB} = 6V; I _C = 0			100	μA
h _{FE-1}	DC Current Gain	I _C = 0.5A; V _{CE} = 5V	25		65	
h _{FE-2}	DC Current Gain	I _C = 2A; V _{CE} = 5V	20			

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