

isc Silicon NPN Power Transistor

2SD1736

DESCRIPTION

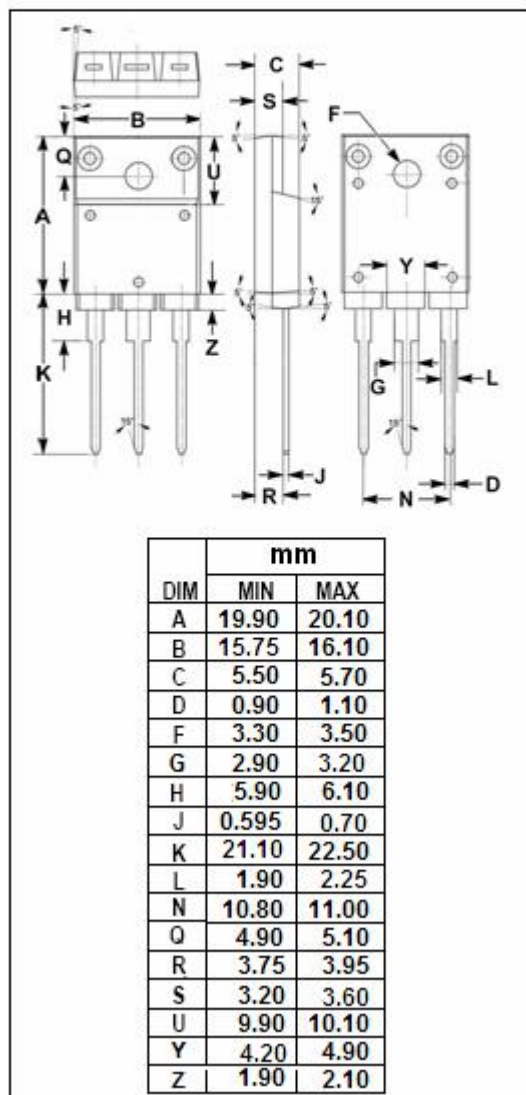
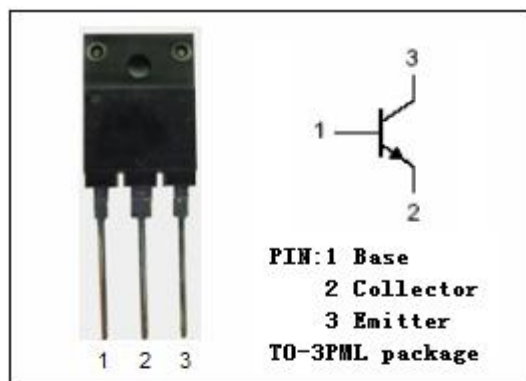
- High Voltage
- High Switching Speed
- Wide Area of Safe Operation
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for horizontal deflection output applications.

ABSOLUTE MAXIMUM RATINGS (T_a=25°C)

| SYMBOL | PARAMETER | VALUE | UNIT |
|------------------|--|---------|------|
| V _{CBO} | Collector-Base Voltage | 1300 | V |
| V _{CES} | Collector-Emitter Voltage | 1300 | V |
| V _{CEO} | Collector-Emitter Voltage | 700 | V |
| V _{EBO} | Emitter-Base Voltage | 7 | V |
| I _C | Collector Current-Continuous | 2.5 | A |
| I _{CP} | Collector Current-Peak | 7 | A |
| I _B | Base Current- Continuous | 1 | A |
| P _C | Collector Power Dissipation @T _C =25°C | 60 | W |
| T _j | Junction Temperature | 150 | °C |
| T _{stg} | Storage Temperature Range | -55-150 | °C |



isc Silicon NPN Power Transistor**2SD1736****ELECTRICAL CHARACTERISTICS** $T_c=25^{\circ}\text{C}$ unless otherwise specified

| SYMBOL | PARAMETER | CONDITIONS | MIN | TYP | MAX | UNIT |
|---------------|--------------------------------------|---|-----|-----|-----|---------------|
| $V_{(BR)EBO}$ | Emitter-Base Breakdown Voltage | $I_E = 1\text{mA}; I_C = 0$ | 7 | | | V |
| $V_{CE(sat)}$ | Collector-Emitter Saturation Voltage | $I_C = 2\text{A}; I_B = 0.6\text{A}$ | | | 8.0 | V |
| $V_{BE(sat)}$ | Base-Emitter Saturation Voltage | $I_C = 2\text{A}; I_B = 0.6\text{A}$ | | | 1.5 | V |
| h_{FE} | DC Current Gain | $I_C = 0.5\text{A}; V_{CE} = 5\text{V}$ | 6 | | 30 | |
| I_{CBO} | Collector Cutoff Current | $V_{CB} = 750\text{V}; I_E = 0$ | | | 10 | μA |
| | | $V_{CB} = 1300\text{V}; I_E = 0$ | | | 1.0 | mA |

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