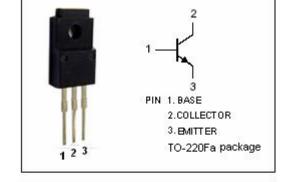


isc Silicon NPN Power Transistor

2SD2061

DESCRIPTION

- · Low Collector Saturation Voltage
 - : $V_{CE(sat)} = 0.3V(TYP.)$ @ $I_C = 2A$
- · Collector Power Dissipation
 - $: P_C = 30W (Max)$
- Wide Area of Safe Operation
- Minimum Lot-to-Lot variations for robust device performance and reliable operation



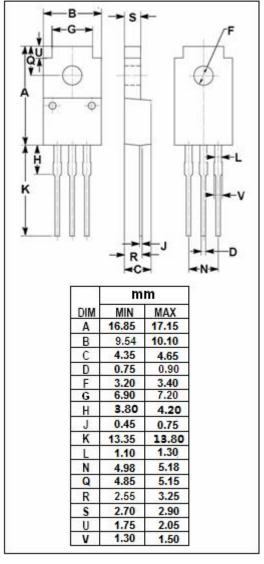


APPLICATIONS

Designed for low frequency power amplifier applications

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT	
V_{CBO}	Collector-Base Voltage	80	V	
$V_{\sf CEO}$	Collector-Emitter Voltage	60	V	
V _{EBO}	Emitter-Base Voltage	5	V	
Ic	Collector Current-Continuous	3	А	
Ісм	Collector Current-Pulse	6	А	
P _C	Collector Power Dissipation @ T _C =25°C	30	W	
	Collector Power Dissipation @ T _a =25℃	2.0		
Тл	Junction Temperature	150	°C	
T _{stg}	Storage Temperature Range	-55~150	°C	





isc Silicon NPN Power Transistor

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ELECTRICAL CHARACTERISTICS

Tc=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 1mA ; I _B = 0	60			V
V _{(BR)CBO}	Collector-Base Breakdown Voltage	I _C = 50 μ A ; I _E = 0	80			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = 50 μ A ; I _C = 0	5			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 2A; I _B = 0.2A			1.0	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 2A; I _B = 0.2A			1.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 60V; I _E = 0			10	μА
I _{EBO}	Emitter Cutoff Current	V _{EB} = 4V; I _C =0			10	μA
h _{FE}	DC Current Gain	I _C = 0.5A; V _{CE} = 5V	100		320	
f _T	Current-Gain—Bandwidth Product	I _C = 0.5A ; V _{CE} = 5V		8		MHz
Сов	Output Capacitance	I _E = 0 ; V _{CB} = 10V;f _{test} = 1.0MHz		70		pF

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