

## P-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A) <sup>c</sup>	Q <sub>g</sub> (Typ.)			
- 20	0.080 at V <sub>GS</sub> = - 4.5 V	- 3.1	4.3 nC			
	0.100 at V <sub>GS</sub> = - 2.5 V	- 2.3	4.5110			

#### **FEATURES**

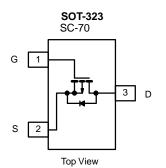
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- 100 % R<sub>g</sub> Tested
  Compliant to RoHS Directive 2002/95/EC

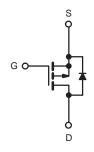


HALOGEN FREE

#### **APPLICATIONS**

- Load Switch
- DC/DC Converters





P-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b> (7	$\Gamma_A = 25  ^{\circ}\text{C}$ , unless oth	erwise noted)			
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage		V <sub>DS</sub>	- 20	V	
Gate-Source Voltage		V <sub>GS</sub>	± 12		
	T <sub>C</sub> = 25 °C		- 3.1	A	
Continuous Drain Current (T <sub>J</sub> = 150 °C)	T <sub>C</sub> = 70 °C		- 2.1		
Continuous Diam Current (1 j = 150 °C)	T <sub>A</sub> = 25 °C	l <sub>D</sub>	- 1.4 <sup>a, b</sup>		
	T <sub>A</sub> = 70 °C		- 1.1 <sup>a, b</sup>		
Pulsed Drain Current	I <sub>DM</sub>	- 6			
0 1 0 0 0 0	T <sub>C</sub> = 25 °C	L	- 0.4		
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	- I <sub>S</sub> -	- 0.3		
	T <sub>C</sub> = 25 °C		0.5	W	
Maximum Daylar Dissination	T <sub>C</sub> = 70 °C	D .	0.3		
Maximum Power Dissipation	T <sub>A</sub> = 25 °C	P <sub>D</sub>	0.4 <sup>a, b</sup>		
	T <sub>A</sub> = 70 °C		0.3 <sup>a, b</sup>		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 50 to 150	°C	
Soldering Recommendations (Peak Temperature)		260			

#### Notes:

a. Surface mounted on 1" x 1" FR4 board.

b. t = 10 s.

c. Based on  $T_C = 25$  °C.



THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient <sup>a, b</sup>	t ≤ 10 s	R <sub>thJA</sub>	250	300	°C/W		
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	225	270	C/VV		

#### Notes:

- a. Surface mounted on 1" x 1" FR4 board.
- b. Maximum under steady state conditions is 360 °C/W.

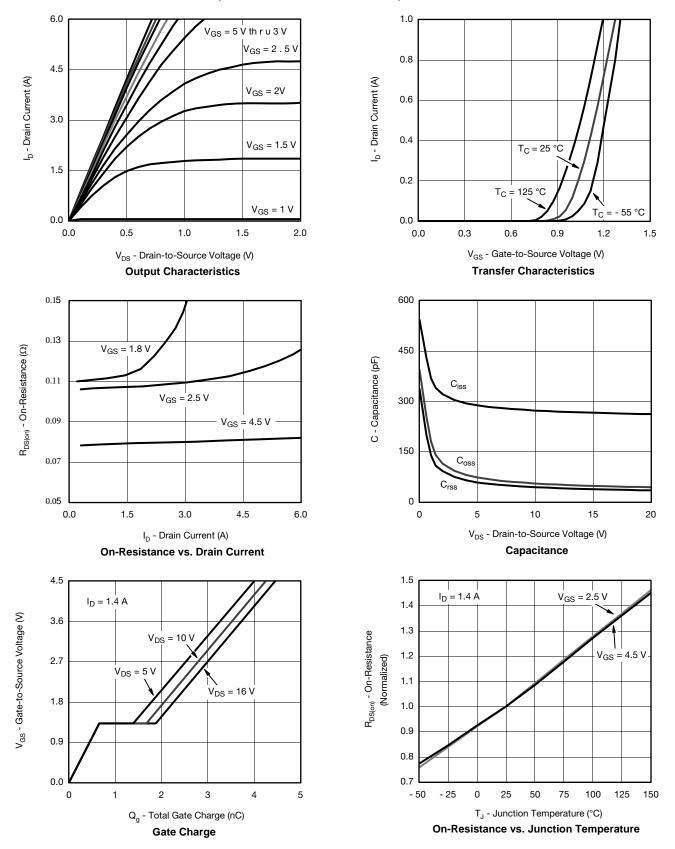
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static				I.	I.	
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = - 250 μA	- 20			V
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	-/T.		- 14		\//00
V <sub>GS(th)</sub> Temperature Coefficient	In = - 250 IIA			2.4		mV/°C
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \mu\text{A}$	- 0.45		- 1.5	V
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			± 100	nA
7 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1		V <sub>DS</sub> = - 20 V, V <sub>GS</sub> = 0 V			- 1	μA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = - 20 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			- 10	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \le -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	- 2			Α
		V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 1.4 A		0.080		Ω
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 2.5 V, I <sub>D</sub> = - 1.2 A		0.120		
	` ′	V <sub>GS</sub> = - 1.8 V, I <sub>D</sub> = - 0.3 A		0.140		
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = - 5 V, I <sub>D</sub> = - 1.4 A		5		S
Dynamic <sup>b</sup>						
Input Capacitance	citance C <sub>iss</sub>			272		
Output Capacitance	C <sub>oss</sub>	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		55		pF
Reverse Transfer Capacitance	C <sub>rss</sub>			44		
T. 10	Q <sub>g</sub>	$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -1.4 \text{ A}$		4.3	6.5	nC
Total Gate Charge				2.7	4.1	
Gate-Source Charge		$V_{DS} = -10 \text{ V}, V_{GS} = -2.5 \text{ V}, I_{D} = -1.4 \text{ A}$		0.7		
Gate-Drain Charge	Q <sub>gd</sub>			1.0		1
Gate Resistance	R <sub>q</sub>	f = 1 MHz	1.4	7	14	Ω
Turn-On Delay Time	t <sub>d(on)</sub>			12	20	
Rise Time	t <sub>r</sub>	$V_{DD} = -10 \text{ V, R}_{L} = 9.1 \Omega$		20	30	= - -
Turn-Off DelayTime	t <sub>d(off)</sub>	$I_{D} \cong -1.1 \text{ A, V}_{GEN} = -4.5 \text{ V, R}_{g} = 1 \Omega$		23	35	
Fall Time	t <sub>f</sub>			9	18	
Turn-On Delay Time	t <sub>d(on)</sub>			5	10	ns
Rise Time	t <sub>r</sub>	$V_{DD} = -10 \text{ V}, R_L = 9.1 \Omega$		10	20	<del>-</del> - -
Turn-Off DelayTime	t <sub>d(off)</sub>			18	27	
Fall Time	t <sub>f</sub> =			7	14	
Drain-Source Body Diode Characterist						
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			- 2.4	
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>	Ü			- 6	Α
Body Diode Voltage	V <sub>SD</sub>	I <sub>F</sub> = - 0.7 A		- 0.8	- 1.2	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>	·		18	27	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			7	14	nC
Reverse Recovery Fall Time	t <sub>a</sub>	$I_F = -0.7 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$		7		1
Reverse Recovery Rise Time	t <sub>b</sub>			11		ns

#### Notes:

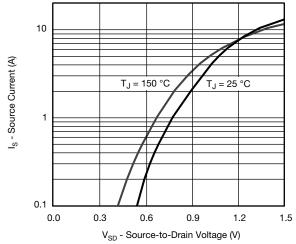
- a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

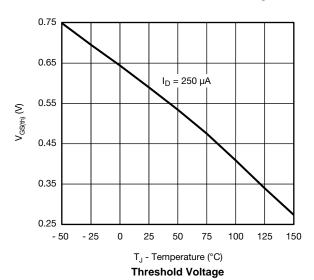


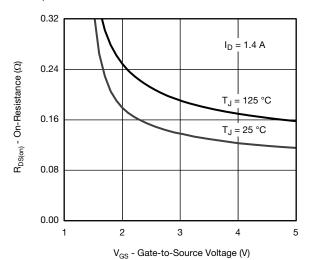




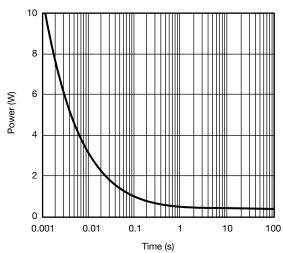


#### Source-Drain Diode Forward Voltage

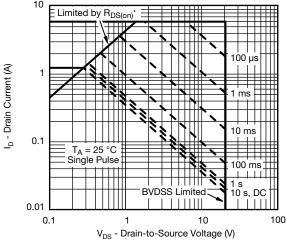




On-Resistance vs. Gate-to-Source Voltage



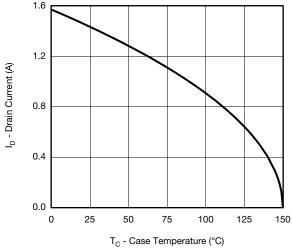
Single Pulse Power, Junction-to-Ambient



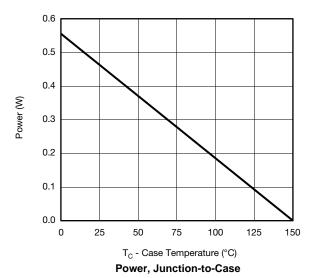
\*  $V_{GS}$  > minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified

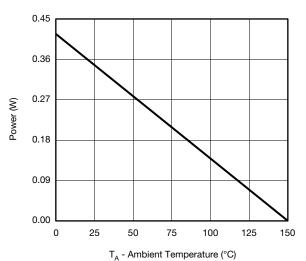
Safe Operating Area, Junction-to-Ambient





Current Derating\*

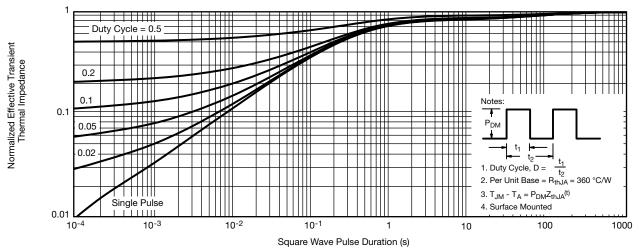




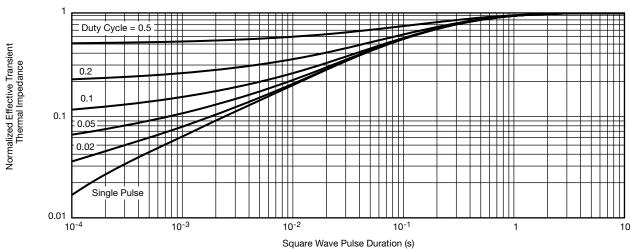
Power, Junction-to-Ambient

<sup>\*</sup> The power dissipation  $P_D$  is based on  $T_{J(max)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit





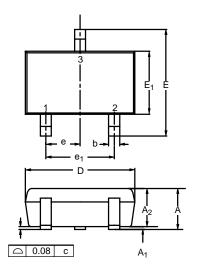
Normalized Thermal Transient Impedance, Junction-to-Ambient

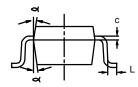


Normalized Thermal Transient Impedance, Junction-to-Foot



SC-70: 3-LEADS





	MILLIMETERS			INCHES			
Dim	Min	Nom	Max	Min	Nom	Max	
Α	0.90	-	1.10	0.035	-	0.043	
$A_1$	_	-	0.10	-	_	0.004	
A <sub>2</sub>	0.80	-	1.00	0.031	_	0.039	
b	0.25	-	0.40	0.010	_	0.016	
С	0.10	-	0.25	0.004	-	0.010	
D	1.80	2.00	2.20	0.071	0.079	0.087	
Ε	1.80	2.10	2.40	0.071	0.083	0.094	
E <sub>1</sub>	1.15	1.25	1.35	0.045	0.049	0.053	
е		0.65BSC		0.026BSC			
e <sub>1</sub>	1.20	1.30	1.40	0.047	0.051	0.055	
L	0.10	0.20	0.30	0.004	0.008	0.012	
ø		7°Nom		7°Nom			
ECN: S-03946—Rev. C, 09-Jul-01 DWG: 5549							

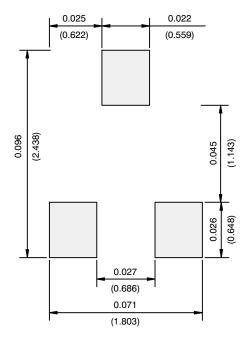
服务热线:400-655-8788

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## **RECOMMENDED MINIMUM PADS FOR SC-70: 3-Lead**



Recommended Minimum Pads Dimensions in Inches/(mm)



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