

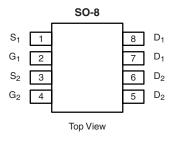
Dual N-Channel 20-V (D-S) MOSFET

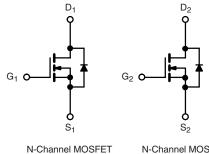
PRODUCT SUMMARY					
V _{DS} (V)	V _{DS} (V) R _{DS(on)} (Ω)				
20	0.019 at V _{GS} = 4.5 V	7.1			
	0.026 at V _{GS} = 2.5 V	6.0			

FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFET ٠
- 100 % R_g Tested
- Compliant to RoHS Directive 2002/95/EC







N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $T_A = 25 \text{ °C}$, unless otherwise noted					
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	20		
Gate-Source Voltage		V _{GS}	± 12	V	
	T _A = 25 °C		7.1		
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 70 °C	۱ _D	5.7	•	
Pulsed Drain Current (10 µs Pulse Width)		I _{DM}	40	A	
Continuous Source Current (Diode Conduction) ^a		۱ _S	1.7		
	T _A = 25 °C	PD	2	W	
Maximum Power Dissipation ^a	T _A = 70 °C	۲D	1.3	VV	
Operating Junction and Storage Temperature Range	e	T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Limit	Unit		
Maximum Junction-to-Ambient ^a	R _{thJA}	62.5	°C/W		

Notes:

a. Surface Mounted on FR4 board, t \leq 10 s.

Rise Time

Fall Time

Turn-Off Delay Time

				B	® VBsem	
C. unless o	otherwise noted			www.	VBsemi.con	
Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$	0.6		1.5	V	
I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 12 V$			± 100	nA	
I _{DSS}	$V_{DS} = 20 V, V_{GS} = 0 V$			1		
	V_{DS} = 20 V, V_{GS} = 0 V, T_{J} = 55 °C			5	μΑ	
I _{D(on)}	$V_{DS} \ge 5 \text{ V}, \text{ V}_{GS} = 4.5 \text{ V}$	20			А	
R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 7.1 \text{ A}$		0.019	019 Ω		
	$V_{GS} = 2.5 \text{ V}, \text{ I}_{D} = 6.0 \text{ A}$		0.026		12	
9 _{fs}	V _{DS} = 10 V, I _D = 7.1 A		27		S	
V _{SD}	$I_{\rm S} = 1.7$ A, $V_{\rm GS} = 0$ V			1.2	V	
Qg			9.5			
Q _{gs}	V_{DS} = 10 V, V_{GS} = 4.5 V, I_D = 7.1 A		1.5		nC	
Q _{gd}			2.5			
Rg	f = 1 MHz		1.6	2.7	Ω	
t _{d(on)}			10			
	Symbol V _{GS(th)} I _{GSS} I _{DSS} I _{D(on)} R _{DS(on)} G _{fs} V _{SD} Q _g Q _{gs} Q _{gd} R _g	$\begin{tabular}{ c c c c c } \hline V_{GS}(th) & V_{DS} = V_{GS}, I_D = 250 \ \mu A \\ \hline I_{GSS} & V_{DS} = 0 \ V, \ V_{GS} = \pm 12 \ V \\ \hline V_{DS} = 20 \ V, \ V_{GS} = 0 \ V, \ V_{GS} = 0 \ V \\ \hline V_{DS} = 20 \ V, \ V_{GS} = 0 \ V, \ T_J = 55 \ ^{\circ}C \\ \hline I_{D(on)} & V_{DS} \ge 5 \ V, \ V_{GS} = 4.5 \ V \\ \hline R_{DS(on)} & V_{GS} = 4.5 \ V, \ I_D = 7.1 \ A \\ \hline V_{GS} = 10 \ V, \ I_D = 7.1 \ A \\ \hline V_{SD} & I_S = 1.7 \ A, \ V_{GS} = 0 \ V \\ \hline \hline Q_g \\ \hline Q_{gs} \\ \hline R_g & f = 1 \ MHz \\ \hline \end{tabular}$	$\begin{tabular}{ c c c c } \hline Symbol & Test Conditions & Min. \\ \hline $V_{GS(th)}$ & $V_{DS} = V_{GS}, I_D = 250 \ \mu A$ & 0.6$ \\ \hline I_{GSS} & $V_{DS} = 0 \ V, V_{GS} = \pm 12 \ V$ & $V_{DS} = 20 \ V, V_{GS} = 0 \ V$ & $V_{DS} = 20 \ V, V_{GS} = 0 \ V, T_J = 55 \ ^\circ C$ & $V_{DS} = 20 \ V, V_{GS} = 4.5 \ V, I_D = 7.1 \ A$ & $V_{CS} = 10 \ V, I_D = 7.1 \ A$ & V_{SD} & $I_S = 1.7 \ A, V_{GS} = 0 \ V$ & $V_{DS} = 0 \ V$ & $V_{DS} = 10 \ V, I_D = 7.1 \ A$ & V_{SD} & $I_S = 1.7 \ A, V_{GS} = 0 \ V$ & $V_{DS} = 10 \ V, I_D = 7.1 \ A$ & V_{SD} & $I_S = 1.7 \ A, V_{GS} = 0 \ V$ & $V_{DS} = 10 \ V, I_D = 7.1 \ A$ & $V_{CS} = 10 \ V, I_D = 7.1 \ A$$	$\begin{array}{c c c c c c c c } \hline Symbol & Test Conditions & Min. & Typ. \\ \hline & & & & & & & & & & & & & & & & & &$	$\begin{array}{c c c c c c c c c c c c c c c c c c c $	

Notes:

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

Source-Drain Reverse Recovery Time

b. Guaranteed by design, not subject to production testing.

t_r

t_{d(off)}

t_f

t_{rr}

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

 V_{DD} = 10 V, R_L = 10 Ω

 $I_D \cong 1$ A, V_{GEN} = 4.5 V, R_g = 10 Ω

 $I_F = 1.7 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}$

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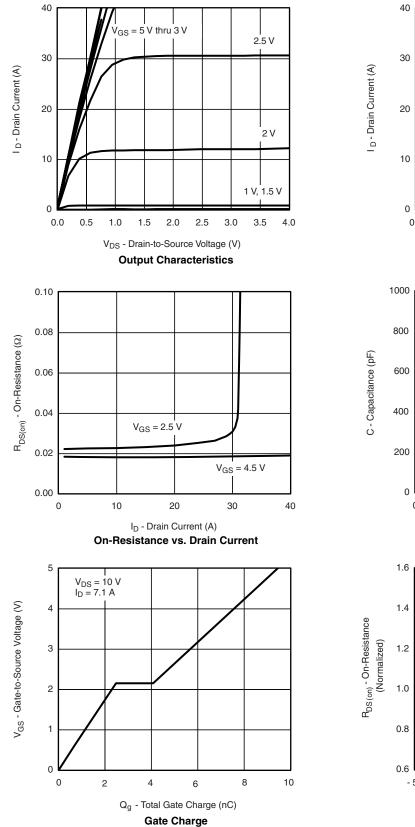
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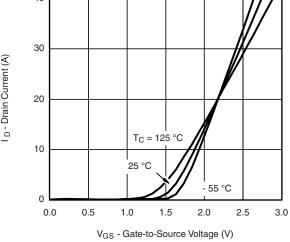
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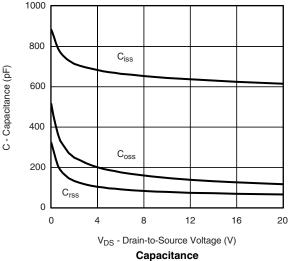


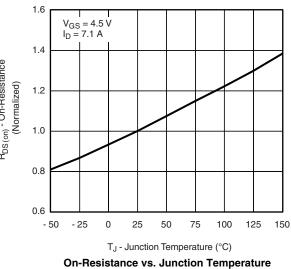


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

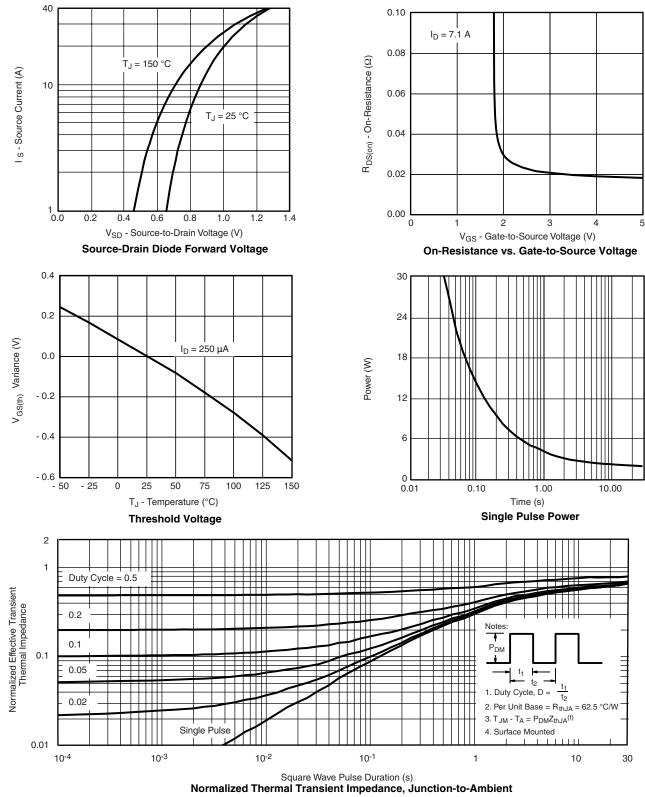


Transfer Characteristics









TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



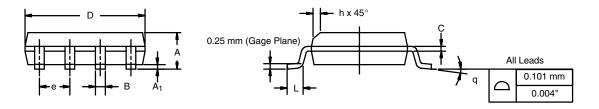




SOIC (NARROW): 8-LEAD

JEDEC Part Number: MS-012





	MILLIM	IETERS	INC	IES	
DIM	Min	Мах	Min	Max	
A	1.35	1.75	0.053	0.069	
A ₁	0.10	0.20	0.004	0.008	
В	0.35	0.51	0.014	0.020	
С	0.19	0.25	0.0075	0.010	
D	4.80	5.00	0.189	0.196	
E	3.80	4.00	0.150	0.157	
е	1.27 BSC		0.050 BSC		
Н	5.80	6.20	0.228	0.244	
h	0.25	0.50	0.010	0.020	
L	0.50	0.93	0.020	0.037	
q	0°	8°	0°	8°	
S	0.44	0.64	0.018	0.026	
ECN: C-06527-Rev. I, 11-Sep-06 DWG: 5498					



RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads Dimensions in Inches/(mm)



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